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14 **ATTORNEYS FOR PLAINTIFF CONTINENTAL CIRCUITS, LLC**

15 IN THE UNITED STATES DISTRICT COURT

16 FOR THE DISTRICT OF ARIZONA

17 CONTINENTAL CIRCUITS, LLC,

18 Plaintiff,

19 v.

20 INTEL CORP.; IBIDEN U.S.A. CORP.;

21 IBIDEN CO., LTD.,

22 Defendants.

CASE NO. CV-16-2026-PHX-DGC

**AMENDED COMPLAINT FOR
PATENT INFRINGEMENT**

JURY TRIAL DEMANDED

23
24 Plaintiff Continental Circuits LLC files this Amended Complaint against Defendants
25 Intel Corporation, Ividen U.S.A. Corporation, and Ividen Co., Ltd. (collectively,
26 “Defendants”) for patent infringement under 35 U.S.C. § 271 and alleges, based on its own
27 personal knowledge with respect to its own actions and based upon information and belief
28 with respect to all others’ actions, as follows:

THE PARTIES

1
2 1. Plaintiff Continental Circuits LLC (hereinafter “Plaintiff” or “Continental
3 Circuits”) is a limited liability company organized and existing under the laws of the State of
4 Arizona, with its principal place of business at 1 East Washington Street, Suite 590, Phoenix,
5 Arizona 85004.

6 2. Defendant Intel Corporation (hereinafter “Intel”) is a corporation organized
7 and existing under the laws of the State of Delaware, with its principal place of business at
8 2200 Mission College Blvd., RNB-5-125, Santa Clara, California 95054. Intel has
9 designated CT Corporation System, 3800 N. Central Avenue, Suite 460, Phoenix, Arizona
10 85012 as its agent for service of process.

11 3. Defendant Ividen U.S.A. Corporation (“Ividen USA”) is a corporation
12 organized and existing under the laws of the State of California, with its principal place of
13 business at 3900 Freedom Circle, Suite 130, Santa Clara, California 95054. Ividen USA has
14 designated CT Corporation System, 3800 N. Central Avenue, Suite 460, Phoenix, Arizona
15 85012 as its agent for service of process.

16 4. Defendant Ividen Corporation, Ltd., (“Ividen Japan”) is a corporation
17 organized and existing under the laws of Japan, with its principal place of business at 2-1,
18 Kanda-cho, Ogaki, Gifu 503-8604, Japan.

19 **JURISDICTION AND VENUE**

20 5. This is an action for patent infringement arising under the patent laws of the
21 United States, 35 U.S.C. §§ 1 *et seq.* This Court has jurisdiction over this action pursuant to
22 28 U.S.C. §§ 1331 and 1338(a).

23 6. This Court has personal jurisdiction over Intel. Intel has multiple facilities
24 located in the State of Arizona, including facilities at 5000 West Chandler Boulevard,
25 Chandler, Arizona 85226 and 4500 South Dobson Road, Chandler, Arizona 85248. Intel’s
26 main research and development facility for global packaging is located in Chandler, Arizona,
27 where Intel employs over 1,000 packaging engineers.

1 7. In addition to Intel's global packaging hub being located in this District, Intel
2 also imports finished products into the United States through Arizona. Intel enjoys the
3 benefits of Foreign Trade Zone 75C, granted by the city of Phoenix, which include savings
4 on duty rates of foreign-sourced items and streamlined customs procedures.

5 8. Intel conducts business and has committed acts of patent infringement and/or
6 has induced acts of patent infringement by others in the District of Arizona and/or has
7 contributed to patent infringement by others in the District of Arizona and elsewhere in the
8 United States.

9 9. Ividen USA is a wholly-owned subsidiary of Ividen Japan (collectively,
10 "Ividen"). Ividen has at least one facility in the State of Arizona at 2727 W. Frye Road, Suite
11 140, Chandler, Arizona 85224.

12 10. This Court has personal jurisdiction over Ividen. Ividen conducts business and
13 has committed acts of patent infringement and/or has induced acts of patent infringement by
14 others in the District of Arizona and/or has contributed to patent infringement by others in
15 the District of Arizona and elsewhere in the United States. Ividen has placed infringing
16 products in the stream of commerce with the expectation that such infringing products would
17 be made, used, sold, and/or offered for sale within the District of Arizona.

18 11. Venue is proper in this district pursuant to 28 U.S.C. § 1391(b), 1391(c) and
19 1400(b) because, among other things, each of Intel and Ividen are subject to personal
20 jurisdiction in the District of Arizona, has regularly conducted business in this judicial
21 district, and certain of the acts complained of herein occurred in this judicial district.

22 **PATENTS-IN-SUIT**

23 12. On November 7, 2000, the United States Patent and Trademark Office duly and
24 legally issued U.S. Patent No. 6,141,870 (the "870 patent"), entitled "Method for Making
25 Electrical Device" to Mr. Brian J. McDermott, et al.

26 13. On March 2, 2004, the United States Patent and Trademark Office duly and
27 legally issued U.S. Patent No. 6,700,069 (the "069 patent"), entitled "Circuit Board or Other
28

1 Multilayer Electrical Device Made by Forming Teeth to Join Layers” to Mr. Brian J.
2 McDermott, et al.

3 14. On March 10, 2009, the United States Patent and Trademark Office duly and
4 legally issued U.S. Patent No. 7,501,582 (the “’582 patent”), entitled “Electrical Device and
5 Method for Making Same” to Mr. Brian J. McDermott, et al. A true and correct copy of the
6 ’582 patent is attached hereto as Exhibit A.

7 15. On October 2, 2012, the United States Patent and Trademark Office duly and
8 legally issued U.S. Patent No. 8,278,560 (the “’560 patent”), entitled “Electrical Device with
9 Teeth Joining Layers and Method for Making the Same” to Mr. Brian J. McDermott, et al. A
10 true and correct copy of the ’560 patent is attached hereto as Exhibit B.

11 16. On November 12, 2013, the United States Patent and Trademark Office duly
12 and legally issued U.S. Patent No. 8,581,105 (the “’105 patent”), entitled “Electrical Device
13 with Teeth Joining Layers and Method for Making the Same” to Mr. Brian J. McDermott, et
14 al. A true and correct copy of the ’105 patent is attached hereto as Exhibit C.

15 17. On June 21, 2016, the United States Patent and Trademark Office duly and
16 legally issued U.S. Patent No. 9,374,912 (the “’912 patent”), entitled “Electrical Device with
17 Teeth Joining Layers and Method for Making the Same” to Mr. Brian J. McDermott, et al.
18 A true and correct copy of the ’912 patent is attached hereto as Exhibit D.

19 18. The ’870 patent, ’069 patent, ’582 patent, ’560 patent, ’105 patent, and ’912
20 patent share a common specification.

21 19. Continental Circuits owns all rights, title, and interest in and to the ’582 patent,
22 the ’560 patent, the ’105 patent, and the ’912 patent (the “patents-in-suit”) and possesses all
23 rights of recovery.

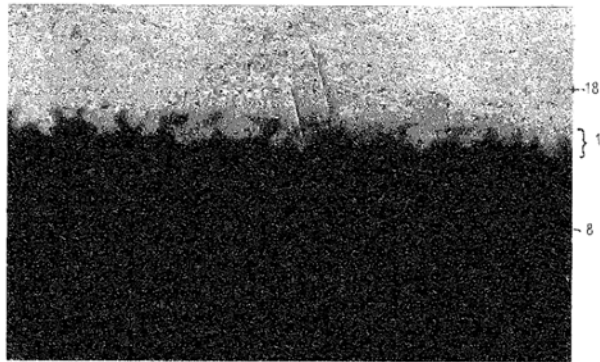
24 **FACTUAL ALLEGATIONS**

25 20. The patents-in-suit generally cover a multilayer electrical device, such as a
26 circuit board, having a roughened surface structure for joining at least one of the layers, and
27 methods of making the same.

28

1 21. The technology covered by the patents-in-suit is illustrated in Figure 1:

2 FIG. 1



9 Fig. 1, '560 patent.

10 22. Continental Circuits Inc. was formed in 1970 to manufacture printed circuit boards.

11 23. Continental Circuits Inc. manufactured printed circuit boards for large
12 companies such as Intel, Motorola, and others.

13 24. In the 1996 to 1998 timeframe, representatives from Continental Circuits Inc.
14 held multiple meetings with representatives of Intel to discuss, *inter alia*, design rules and
15 manufacturing reliability of printed circuit boards.

16 25. Continental Circuits Inc.'s products were widely distributed in the printed
17 circuit board market.

18 26. By 1997, Continental Circuits Inc. ranked in the top 10% of all United States
19 printed circuit board suppliers.

20 27. Mr. Brian McDermott, Mr. Daniel McGowan, Mr. Ralph Leo Spotts, Jr., and
21 Mr. Sid Tryzbiak, the co-inventors of the patents-in-suit, were employees of Continental
22 Circuits Inc.

23 28. The co-inventors observed poor adhesion and delamination between the
24 dielectric material and the conductive layer in multilayer electrical devices in the industry.
25 To solve this problem, the co-inventors developed a novel surface roughening technique that
26 offered stronger adhesion between these layers. This roughening technique produced a non-
27 uniformly roughened surface, as shown in Figure 1.
28

1 29. Jeff Long, a sales representative at Continental Circuits Inc. in the mid-1990s,
2 learned of the technology covered by the patents-in-suit during his tenure working with the
3 company.

4 30. Unbeknownst to Continental Circuits Inc., in approximately 1997, Mr. Long
5 entered into a business arrangement with Ibiden while still employed by Continental Circuits
6 Inc.

7 31. Continental Circuits Inc. terminated its relationship with Mr. Long in 1997.

8 32. Without permission from Continental Circuits Inc., Mr. Long shared the
9 technology described in the patents-in-suit with Ibiden and began directing business away
10 from Continental Circuits Inc. to Ibiden.

11 33. Ibiden's supplier relationship with Intel grew significantly in the late 1990s.

12 34. In early 2005, a representative of Continental Circuits contacted Mr. David
13 Simon, Chief Legal Counsel for Intel, informing Mr. Simon of the '870 patent, the '069
14 patent, and the continuation application that led to the issuance of the '582, '560, '105, and
15 '912 patents, enclosing a copy of the patents and application, and expressing an interest in
16 licensing of the portfolio to Intel.

17 35. In early 2005, a representative of Continental Circuits contacted Mr. Asusushi
18 Uchida, General Counsel for Ibiden Circuits of America, informing Mr. Uchida of the '870
19 patent, the '069 patent, and the continuation application that led to the issuance of the '582,
20 '560, '105, and '912 patents, enclosing a copy of the patents and application, and expressing
21 an interest in licensing of the portfolio to Ibiden.

22 36. On March 2, 2005, Ms. Martha Peralez, Outside Submissions Coordinator for
23 Intel, responded to the letter of Paragraph 34, stating that "[a]n Intel attorney is reviewing
24 the matter."

25 37. On April 21, 2005, Ms. Peralez sent an additional letter, stating that "Intel has
26 determined not to pursue this matter."

27 38. The patents-in-suit have been publicly available from the United States Patent
28 Office website since their respective dates of issuance.

1 39. Defendants are familiar with the United States patent system, having filed for
2 and been granted U.S. patents of their own.

3 40. Defendants are familiar with the process of searching for and identifying issued
4 U.S. patents.

5 41. In 2014, a conversation took place between a representative of Continental
6 Circuits and a business development manager at Ibiden. In that conversation, it was
7 confirmed that the technology claimed in the patents-in-suit is still in use at Ibiden
8 today. When questioned further about specific processes claimed in the patents-in-suit, the
9 Ibiden manager became evasive and ended the discussion.

10 42. Intel is one of the world's largest manufacturers of central processing units
11 ("CPUs").

12 43. Intel buys package substrates for its products, including Intel's CPUs, chipsets,
13 and wireless network adapters from package substrate suppliers.

14 44. Ibiden is Intel's largest package substrate supplier, and Ibiden's substrate
15 packaging materials have been deemed essential to Intel's success. (*See* Exhibit E.)

16 45. Intel collaborates with its suppliers, including Ibiden, to develop specifications
17 for the design of packaging to ultimately be used in Intel products, including specifications
18 for delamination, signal integrity, thermal integrity, mechanical integrity, and overall
19 package design.

20 46. If a particular package design is unsatisfactory, Intel will alter the
21 specifications.

22 47. If a supplier fails to meet Intel's specifications, Intel will cease purchasing
23 package substrates from that supplier.

24 48. Approximately ninety-percent of the interaction between Intel and its package
25 substrate suppliers (*e.g.*, Ibiden) takes place in Phoenix, AZ.

26 49. Intel does not have any rights to the patents-in-suit.

27 50. Ibiden does not have any rights to the patents-in-suit.

28

1 51. Intel's Atom series of processors consists of at least the following products:
2 Intel's Atom D2700, D2550, D2500, E3805, E3815, E3825, E3826, E3827, E3845, E680T,
3 E680, E665CT, E665C, E660T, E660, E645CT, E645C, E640T, E640, E620T, E620, N2800,
4 N2600, N570, N550, Z670, Z650, C2750, C2730, C2550, C2530, C2350, S1260, S1240,
5 S1220, S1289, S1279, S1269, C2758, C2738, C2718, C2558, C2538, C2518, C2508, C2358,
6 C2338, C2308, x7-Z8700, x7-Z8750, x5-Z8500, x5-Z8300, x5-Z8330, x5-E8000, x5-Z8350,
7 x5-Z8550, x3-C3445, x3-C3405, x3-C3230RK, x3-C3200RK, x3-C3130, Z3795, Z3785,
8 Z3775D, Z3775, Z3770D, Z3770, Z3745D, Z3745, Z3740D, Z3740, Z3736G, Z3736F,
9 Z3735G, Z3735F, Z3735E, Z3735D, Z3580, Z3570, Z3560, Z3530, Z3590, Z3480, Z3460,
10 Z2760, Z2580, Z2560, Z2520, Z2480, Z2460, and Z2420 processors, and any same or later
11 generation Atom Processors (hereinafter Intel's "Atom series" of processors).

12 52. Intel's Atom series of processors meet or embody the limitations of at least one
13 claim of the '582 patent.

14 53. Intel's Atom series of processors meet or embody the limitations of at least one
15 claim of the '560 patent.

16 54. Intel's Atom series of processors meet or embody the limitations of at least one
17 claim of the '105 patent.

18 55. Intel's Atom series of processors meet or embody the limitations of at least one
19 claim of the '912 patent.

20 56. Intel's Core series of processors consists of at least the following products:
21 Intel's Core i3-2100, i3-2100T, i3-2102, i3-2105, i3-2115C, i3-2120, i3-2120T, i3-2125, i3-
22 2130, i3-2310E, i3-2310M, i3-2312M, i3-2328M, i3-2330E, i3-2330M, i3-2340UE, i3-
23 2348M, i3-2350M, i3-2357M, i3-2365M, i3-2367M, i3-2370M, i3-2375M, i3-2377M, i3-
24 3110M, i3-3115C, i3-3120M, i3-3120ME, i3-3130M, i3-3210, i3-3217U, i3-3217UE, i3-
25 3220, i3-3220T, i3-3225, i3-3227U, i3-3229Y, i3-3240, i3-3240T, i3-3245, i3-3250, i3-
26 3250T, i3-370M, i3-380M, i3-380UM, i3-390M, i3-4000M, i3-4005U, i3-4010U, i3-4010Y,
27 i3-4012Y, i3-4020Y, i3-4025U, i3-4030U, i3-4030Y, i3-4100E, i3-4100M, i3-4100U, i3-
28 4102E, i3-4110E, i3-4110M, i3-4112E, i3-4120U, i3-4130, i3-4130T, i3-4150, i3-4150T, i3-

1 4158U, i3-4160, i3-4160T, i3-4170, i3-4170T, i3-4330, i3-4330T, i3-4330TE, i3-4340, i3-
2 4340TE, i3-4350, i3-4350T, i3-4360, i3-4360T, i3-4370, i3-4370T, i3-5005U, i3-5010U, i3-
3 5015U, i3-5020U, i3-5157U, i3-560, i3-6098P, i3-6100, i3-6100E, i3-6100H, i3-6100T, i3-
4 6100TE, i3-6100U, i3-6102E, i3-6167U, i3-6300, i3-6300T, i3-6320, i5-2300, i5-2310, i5-
5 2320, i5-2380P, i5-2390T, i5-2400, i5-2400S, i5-2405S, i5-2410M, i5-2430M, i5-2435M,
6 i5-2450M, i5-2450P, i5-2467M, i5-2500, i5-2500K, i5-2500S, i5-2500T, i5-2510E, i5-
7 2515E, i5-2520M, i5-2537M, i5-2540M, i5-2550K, i5-2557M, i5-3210M (BGA), i5-3210M
8 (rPGA), i5-3230M (BGA), i5-3230M (rPGA), i5-3317U, i5-3320M, i5-3330, i5-3330S, i5-
9 3337U, i5-3339Y, i5-3340, i5-3340M, i5-3340S, i5-3350P, i5-3360M, i5-3380M, i5-3427U,
10 i5-3437U, i5-3439Y, i5-3450, i5-3450S, i5-3470, i5-3470S, i5-3470T, i5-3475S, i5-3550,
11 i5-3550S, i5-3570, i5-3570K, i5-3570S, i5-3570T, i5-3610ME, i5-4200H, i5-4200M, i5-
12 4200U, i5-4200Y, i5-4202Y, i5-4210H, i5-4210M, i5-4210U, i5-4210Y, i5-4220Y, i5-
13 4250U, i5-4258U, i5-4260U, i5-4278U, i5-4288U, i5-4300M, i5-4300U, i5-4300Y, i5-
14 4302Y, i5-4308U, i5-4310M, i5-4310U, i5-4330M, i5-4340M, i5-4350U, i5-4360U, i5-
15 4400E, i5-4402E, i5-4402EC, i5-4410E, i5-4422E, i5-4430, i5-4430S, i5-4440, i5-4440S,
16 i5-4460, i5-4460S, i5-4460T, i5-4570, i5-4570R, i5-4570S, i5-4570T, i5-4570TE, i5-4590,
17 i5-4590S, i5-4590T, i5-460M, i5-4670, i5-4670K, i5-4670R, i5-4670S, i5-4670T, i5-4690,
18 i5-4690K, i5-4690S, i5-4690T, i5-470UM, i5-480M, i5-5200U, i5-5250U, i5-5257U, i5-
19 5287U, i5-5300U, i5-5350H, i5-5350U, i5-5575R, i5-560M, i5-560UM, i5-5675C, i5-
20 5675R, i5-580M, i5-6200U, i5-6260U, i5-6267U, i5-6287U, i5-6300HQ, i5-6300U, i5-
21 6350HQ, i5-6360U, i5-6400, i5-6400T, i5-6402P, i5-6440EQ, i5-6440HQ, i5-6442EQ, i5-
22 6500, i5-6500T, i5-6500TE, i5-6585R, i5-6600, i5-6600K, i5-6600T, i5-6685R, i5-760, i7-
23 2600, i7-2600K, i7-2600S, i7-2610UE, i7-2617M, i7-2620M, i7-2629M, i7-2630QM, i7-
24 2635QM, i7-2637M, i7-2640M, i7-2649M, i7-2655LE, i7-2657M, i7-2670QM, i7-2675QM,
25 i7-2677M, i7-2700K, i7-2710QE, i7-2715QE, i7-2720QM, i7-2760QM, i7-2820QM, i7-
26 2860QM, i7-2920XM, i7-2960XM, i7-3517U, i7-3517UE, i7-3520M, i7-3537U, i7-3540M,
27 i7-3555LE, i7-3610QE, i7-3610QM, i7-3612QE, i7-3612QM (BGA), i7-3612QM (rPGA),
28 i7-3615QE, i7-3615QM, i7-3630QM, i7-3632QM (BGA), i7-3632QM (rPGA), i7-3635QM,

1 i7-3667U, i7-3687U, i7-3689Y, i7-3720QM, i7-3740QM, i7-3770, i7-3770K, i7-3770S, i7-
2 3770T, i7-3820, i7-3820QM, i7-3840QM, i7-3920XM, i7-3930K, i7-3940XM, i7-3960X,
3 i7-3970X, i7-4500U, i7-4510U, i7-4550U, i7-4558U, i7-4578U, i7-4600M, i7-4600U, i7-
4 4610M, i7-4610Y, i7-4650U, i7-4700EC, i7-4700EQ, i7-4700HQ, i7-4700MQ, i7-4702EC,
5 i7-4702HQ, i7-4702MQ, i7-4710HQ, i7-4710MQ, i7-4712HQ, i7-4712MQ, i7-4720HQ, i7-
6 4722HQ, i7-4750HQ, i7-4760HQ, i7-4765T, i7-4770, i7-4770HQ, i7-4770K, i7-4770R, i7-
7 4770S, i7-4770T, i7-4770TE, i7-4771, i7-4785T, i7-4790, i7-4790K, i7-4790S, i7-4790T,
8 i7-4800MQ, i7-4810MQ, i7-4820K, i7-4850HQ, i7-4860HQ, i7-4870HQ, i7-4900MQ, i7-
9 4910MQ, i7-4930K, i7-4930MX, i7-4940MX, i7-4950HQ, i7-4960HQ, i7-4960X, i7-
10 4980HQ, i7-5500U, i7-5550U, i7-5557U, i7-5600U, i7-5650U, i7-5700EQ, i7-5700HQ, i7-
11 5750HQ, i7-5775C, i7-5775R, i7-5820K, i7-5850EQ, i7-5850HQ, i7-5930K, i7-5950HQ,
12 i7-5960X, i7-640M, i7-6500U, i7-6560U, i7-6567U, i7-6600U, i7-660LM, i7-6650U, i7-
13 6660U, i7-6700, i7-6700HQ, i7-6700K, i7-6700T, i7-6700TE, i7-6770HQ, i7-6785R, i7-
14 6800K, i7-680UM, i7-6820EQ, i7-6820HK, i7-6820HQ, i7-6822EQ, i7-6850K, i7-6870HQ,
15 i7-6900K, i7-6920HQ, i7-6950X, i7-6970HQ, i7-740QM, i7-840QM, i7-940XM, i7-970, i7-
16 980, i7-990X, M-5Y10, M-5Y10a, M-5Y10c, M-5Y31, M-5Y51, M-5Y70, M-5Y71, m3-
17 6Y30, m5-6Y54, m5-6Y57, and m7-6Y75 processors, and any same or later generation Core
18 processors (hereinafter Intel’s “Core series” of processors).

19 57. Intel’s Core series of processors meet or embody the limitations of at least one
20 claim of the ’582 patent.

21 58. Intel’s Core series of processors meet or embody the limitations of at least one
22 claim of the ’560 patent.

23 59. Intel’s Core series of processors meet or embody the limitations of at least one
24 claim of the ’105 patent.

25 60. Intel’s Core series of processors meet or embody the limitations of at least one
26 claim of the ’912 patent.

27 61. Intel’s Pentium series of processors consists of at least the following products:
28 Intel’s Pentium N3540, N3530, N3520, N3510, N3700, N3710, G3470, G3460T, G3460,

1 G3450T, G3450, G3440T, G3440, G3430, G3420T, G3420, G3320TE, G3260T, G3260,
2 G3258, G3250T, G3250, G3240T, G3240, G3220T, G3220, J2900, J2850, G2140, G2130,
3 G2120T, G2120, G2100T, G2030T, G2030, G2020T, G2020, G2010, 3550M, 3556U,
4 3558U, 3560M, 3560Y, 3561Y, 3805U, 3825U, 2129Y, 2127U, 2117U, 2030M, 2020M,
5 1405 (v2), 1405, A1018, A1020, B980, B970, B960, B950, B940, B925C, B915C, 977, 967,
6 957, 987, 997, G870, G860T, G860, G850, G840, G645T, G645, G640T, G640, G632,
7 G630T, G630, G622, G620T, G620, 4405U, 4405Y, G4400T, G4400TE, G4400, G4500,
8 G4500T, G4520, J3710, D1507, D1508, D1509, D1517, D1519, E5700, E5800, E6800,
9 G6951, G6960, P6100, P6200, P6300, and U5600 processors, and any same or later
10 generation Pentium processors (hereinafter Intel’s “Pentium series” of processors).

11 62. Intel’s Pentium series of processors meet or embody the limitations of at least
12 one claim of the ’582 patent.

13 63. Intel’s Pentium series of processors meet or embody the limitations of at least
14 one claim of the ’560 patent.

15 64. Intel’s Pentium series of processors meet or embody the limitations of at least
16 one claim of the ’105 patent.

17 65. Intel’s Pentium series of processors meet or embody the limitations of at least
18 one claim of the ’912 patent.

19 66. Intel’s Celeron series of processors consists of at least the following products:
20 Intel’s Celeron N3000, N3050, N3150, N3010, N3160, N3060, 3765U, 3755U, 3215U,
21 3205U, 3955U, 3855U, N2940, N2930, N2920, N2910, N2840, N2830, N2820, N2815,
22 N2810, N2808, N2807, N2806, N2805, 2981U, 2980U, 2970M, 2961Y, 2957U, 2955U,
23 2950M, 2002E, 2000E, J1900, J1850, J1800, J1750, G1850, G1840T, G1840, G1830,
24 G1820TE, G1820T, G1820, G1630, G1620T, G1620, G1610T, G1610, 1047UE, 1037U,
25 1020M, 1020E, 1019Y, 1017U, 1007U, 1005M, 1000M, 927UE, 925, B840, B830, B820,
26 B815, B810E, B810, B800, 887, 877, 867, 857, 847E, 847, 827E, 807UE, 807, B720, B710,
27 797, 787, 725C, G555, G550T, G550, G540T, G540, G530T, G530, G470, G465, G460,
28 G440, G3902E, G3900E, G3900TE, G3920, G3900, G3900T, J3160, J3060, E3500, P4600,

1 T3500, U3600, and ULV 763 processors, and any same or later generation Celeron
2 processors (hereinafter Intel's "Celeron series" of processors).

3 67. Intel's Celeron series of processors meet or embody the limitations of at least
4 one claim of the '582 patent.

5 68. Intel's Celeron series of processors meet or embody the limitations of at least
6 one claim of the '560 patent.

7 69. Intel's Celeron series of processors meet or embody the limitations of at least
8 one claim of the '105 patent.

9 70. Intel's Celeron series of processors meet or embody the limitations of at least
10 one claim of the '912 patent.

11 71. Intel's Xeon series of processors consists of at least the following products:
12 Intel's Xeon Phi 3120A, 3120P, 5110P, 5120D, 7120A, 7120D, 7120P, and 7120X
13 coprocessors, and any same or later generation Xeon Phi coprocessors, and Intel's Xeon D-
14 1518, D-1520, D-1521, D-1527, D-1528, D-1529, D-1531, D-1537, D-1539, D-1540, D-
15 1541, D-1548, D-1557, D-1559, D-1567, D-1571, D-1577, E3-1105C, E3-1105C (v2), E3-
16 1125C, E3-1125C (v2), E3-1220, E3-1220 (v2), E3-1220 (v3), E3-1220 (v5), E3-1220L, E3-
17 1220L (v2), E3-1220L (v3), E3-1225, E3-1225 (v2), E3-1225 (v3), E3-1225 (v5), E3-1226
18 (v3), E3-1230, E3-1230 (v2), E3-1230 (v3), E3-1230 (v5), E3-1230L (v3), E3-1231 (v3),
19 E3-1235, E3-1235L (v5), E3-1240, E3-1240 (v2), E3-1240 (v3), E3-1240 (v5), E3-1240L
20 (v3), E3-1240L (v5), E3-1241 (v3), E3-1245, E3-1245 (v2), E3-1245 (v3), E3-1245 (v5),
21 E3-1246 (v3), E3-1258L (v4), E3-1260L, E3-1260L (v5), E3-1265L (v2), E3-1265L (v3),
22 E3-1265L (v4), E3-1268L (v3), E3-1268L (v5), E3-1270, E3-1270 (v2), E3-1270 (v3), E3-
23 1270 (v5), E3-1271 (v3), E3-1275, E3-1275 (v2), E3-1275 (v3), E3-1275 (v5), E3-1275L
24 (v3), E3-1276 (v3), E3-1278L (v4), E3-1280, E3-1280 (v2), E3-1280 (v3), E3-1280 (v5),
25 E3-1281 (v3), E3-1285 (v3), E3-1285 (v4), E3-1285L (v3), E3-1286 (v3), E3-1286L (v3),
26 E3-1290, E3-1290 (v2), E3-1505L (v5), E3-1505M (v5), E3-1515M (v5), E3-1535M (v5),
27 E3-1545M (v5), E3-1558L (v5), E3-1565L (v5), E3-1575M (v5), E3-1578L (v5), E3-1585
28 (v5), E3-1585L (v5), E5-1428L, E5-1428L (v2), E5-1428L (v3), E5-1620, E5-1620 (v2), E5-

1 1620 (v3), E5-1630 (v3), E5-1650, E5-1650 (v2), E5-1650 (v3), E5-1660, E5-1660 (v2), E5-
2 1660 (v3), E5-1680 (v3), E5-2403, E5-2403 (v2), E5-2407, E5-2407 (v2), E5-2408L (v3),
3 E5-2418L, E5-2418L (v2), E5-2418L (v3), E5-2420, E5-2420 (v2), E5-2428L, E5-2428L
4 (v2), E5-2428L (v3), E5-2430, E5-2430 (v2), E5-2430L, E5-2430L (v2), E5-2438L (v3), E5-
5 2440, E5-2440 (v2), E5-2448L, E5-2448L (v2), E5-2450, E5-2450 (v2), E5-2450L, E5-
6 2450L (v2), E5-2470, E5-2470 (v2), E5-2603, E5-2603 (v2), E5-2603 (v3), E5-2603 (v4),
7 E5-2608L (v3), E5-2608L (v4), E5-2609, E5-2609 (v2), E5-2609 (v3), E5-2609 (v4), E5-
8 2618L (v2), E5-2618L (v3), E5-2618L (v4), E5-2620, E5-2620 (v2), E5-2620 (v3), E5-2620
9 (v4), E5-2623 (v3), E5-2623 (v4), E5-2628L (v2), E5-2628L (v3), E5-2628L (v4), E5-2630,
10 E5-2630 (v2), E5-2630 (v3), E5-2630 (v4), E5-2630L, E5-2630L (v2), E5-2630L (v3), E5-
11 2630L (v4), E5-2637, E5-2637 (v2), E5-2637 (v3), E5-2637 (v4), E5-2640, E5-2640 (v2),
12 E5-2640 (v3), E5-2640 (v4), E5-2643, E5-2643 (v2), E5-2643 (v3), E5-2643 (v4), E5-
13 2648L, E5-2648L (v2), E5-2648L (v3), E5-2648L (v4), E5-2650, E5-2650 (v2), E5-2650
14 (v3), E5-2650 (v4), E5-2650L, E5-2650L (v2), E5-2650L (v3), E5-2650L (v4), E5-2658, E5-
15 2658 (v2), E5-2658 (v3), E5-2658 (v4), E5-2658A (v3), E5-2660, E5-2660 (v2), E5-2660
16 (v3), E5-2660 (v4), E5-2665, E5-2667, E5-2667 (v2), E5-2667 (v3), E5-2667 (v4), E5-2670,
17 E5-2670 (v2), E5-2670 (v3), E5-2680, E5-2680 (v2), E5-2680 (v3), E5-2680 (v4), E5-2683
18 (v3), E5-2683 (v4), E5-2687W, E5-2687W (v2), E5-2687W (v3), E5-2687W (v4), E5-2690,
19 E5-2690 (v2), E5-2690 (v3), E5-2690 (v4), E5-2695 (v2), E5-2695 (v3), E5-2695 (v4), E5-
20 2697 (v2), E5-2697 (v3), E5-2697 (v4), E5-2697A (v4), E5-2698 (v3), E5-2698 (v4), E5-
21 2699 (v3), E5-2699 (v4), E5-4603, E5-4603 (v2), E5-4607, E5-4607 (v2), E5-4610, E5-4610
22 (v2), E5-4610 (v3), E5-4617, E5-4620, E5-4620 (v2), E5-4620 (v3), E5-4624L (v2), E5-
23 4627 (v2), E5-4627 (v3), E5-4640, E5-4640 (v2), E5-4640 (v3), E5-4648 (v3), E5-4650, E5-
24 4650 (v2), E5-4650 (v3), E5-4650L, E5-4655 (v3), E5-4657L (v2), E5-4660 (v3), E5-4667
25 (v3), E5-4669 (v3), E5603, E5606, E5607, E5649, E7-2803, E7-2820, E7-2830, E7-2850,
26 E7-2850 (v2), E7-2860, E7-2870, E7-2870 (v2), E7-2880 (v2), E7-2890 (v2), E7-4807, E7-
27 4809 (v2), E7-4809 (v3), E7-4809 (v4), E7-4820, E7-4820 (v2), E7-4820 (v3), E7-4820
28 (v4), E7-4830, E7-4830 (v2), E7-4830 (v3), E7-4830 (v4), E7-4850, E7-4850 (v2), E7-4850

1 (v3), E7-4850 (v4), E7-4860, E7-4860 (v2), E7-4870, E7-4870 (v2), E7-4880 (v2), E7-4890
2 (v2), E7-8830, E7-8837, E7-8850, E7-8850 (v2), E7-8857 (v2), E7-8860, E7-8860 (v3), E7-
3 8860 (v4), E7-8867 (v3), E7-8867 (v4), E7-8867L, E7-8870, E7-8870 (v2), E7-8870 (v3),
4 E7-8870 (v4), E7-8880 (v2), E7-8880 (v3), E7-8880 (v4), E7-8880L (v2), E7-8880L (v3),
5 E7-8890 (v2), E7-8890 (v3), E7-8890 (v4), E7-8891 (v2), E7-8891 (v3), E7-8891 (v4), E7-
6 8893 (v2), E7-8893 (v3), E7-8893 (v4), W3670, W3690, X5647, X5672, X5675, X5687, and
7 X5690 processors, and any same or later generation Xeon processors (hereinafter Intel’s
8 “Xeon series” of processors).

9 72. Intel’s Xeon series of processors meet or embody the limitations of at least one
10 claim of the ’582 patent.

11 73. Intel’s Xeon series of processors meet or embody the limitations of at least one
12 claim of the ’560 patent.

13 74. Intel’s Xeon series of processors meet or embody the limitations of at least one
14 claim of the ’105 patent.

15 75. Intel’s Xeon series of processors meet or embody the limitations of at least one
16 claim of the ’912 patent.

17 76. Intel’s Itanium series of processors consists of at least the following products:
18 Intel’s Itanium 9560, 9550, 9540, and 9520 processors, and any same or later generation
19 Itanium processors (hereinafter Intel’s “Itanium series” of processors).

20 77. Intel’s Itanium series of processors meet or embody the limitations of at least
21 one claim of the ’582 patent.

22 78. Intel’s Itanium series of processors meet or embody the limitations of at least
23 one claim of the ’560 patent.

24 79. Intel’s Itanium series of processors meet or embody the limitations of at least
25 one claim of the ’105 patent.

26 80. Intel’s Itanium series of processors meet or embody the limitations of at least
27 one claim of the ’912 patent.

28

1 81. Intel's Quark series of processors consists of at least the following products:
2 Intel's Quark Soc X1000, SoC X1001, SoC X1010, SoC X1011, SoC X1020, SoC X1020D,
3 SoC X1021, SoC X1021D, Microcontroller D1000, Microcontroller D2000, and SE C1000
4 Microcontroller processors, and any same or later generation Quark processors (hereinafter
5 Intel's "Quark series" of processors).

6 82. Intel's Quark series of processors meet or embody the limitations of at least
7 one claim of the '582 patent.

8 83. Intel's Quark series of processors meet or embody the limitations of at least
9 one claim of the '560 patent.

10 84. Intel's Quark series of processors meet or embody the limitations of at least
11 one claim of the '105 patent.

12 85. Intel's Quark series of processors meet or embody the limitations of at least
13 one claim of the '912 patent.

14 86. Intel's chipsets consist of at least the following products: Intel's Z170, Z97,
15 Z87, Z77, Z75, Z68, X99, X38, Q170, Q150, Q87, Q85, Q77, Q75, Q67, Q65, P67, H170,
16 H110, H97, H87, H81, H77, H67, H61, B150, B85, B75, B65, SM35, QM87, HM87, HM86,
17 UM77, UM67, QS77, QS67, QM170, QM77, QM67, HM170, HM77, HM76, HM75, HM70,
18 HM67, HM65, CM236, C612, C608, C606, C604, C602J, C602, C236, C232, C226, C224,
19 C222, C216, C206, C204, C202 chipsets, and any same or later generation chipsets
20 (hereinafter Intel's "Chipsets").

21 87. Intel's Chipsets meet or embody the limitations of at least one claim of the '582
22 patent.

23 88. Intel's Chipsets meet or embody the limitations of at least one claim of the '560
24 patent.

25 89. Intel's Chipsets meet or embody the limitations of at least one claim of the '105
26 patent.

27 90. Intel's Chipsets meet or embody the limitations of at least one claim of the '912
28 patent.

1 91. Intel's wireless network adapters consist of at least the following products:
2 Intel's W13100, AC 7265, N 7265 (Dual Band), N 7265, AC 7260, N 7260 (Dual Band), N
3 7260, AC 3160, AC 3165, AC 3168, N 2200 (Single Band), N 2230 (Single Band), N 1030
4 (Single Band), N 1000 (Single Band), N 100 (Single Band), N 105 (Single Band), N 130
5 (Single Band), N 135 (Single Band), N 6300 (Dual Band), N 6200 (Dual Band), N 6205
6 (Dual Band), N 6230 (Dual Band), N 6235 (Dual Band), AC 8260, AC 8265, AC 18260, AC
7 17265, W11000, N + WiMAX 6250 (Dual Band), and N + WiMAX 6150 (Single Band)
8 wireless network adapters, and any same or later generation Intel wireless network adapters
9 (hereinafter Intel's "Wireless Network Adapters").

10 92. Intel's Wireless Network Adapters meet or embody the limitations of at least
11 one claim of the '582 patent.

12 93. Intel's Wireless Network Adapters meet or embody the limitations of at least
13 one claim of the '560 patent.

14 94. Intel's Wireless Network Adapters meet or embody the limitations of at least
15 one claim of the '105 patent.

16 95. Intel's Wireless Network Adapters meet or embody the limitations of at least
17 one claim of the '912 patent.

18 96. Ibidem makes printed circuit boards, such as those which can be found in Intel's
19 Atom series, Core series, Pentium series, Celeron series, Xeon series, Itanium series, and
20 Quark series processors, Chipsets, and Wireless Network Adapters (hereinafter Ibidem's
21 "Package Substrates").

22 97. Ibidem's Package Substrates meet or embody the limitations of at least one
23 claim of the '582 patent.

24 98. Ibidem's Package Substrates meet or embody the limitations of at least one
25 claim of the '560 patent.

26 99. Ibidem's Package Substrates meet or embody the limitations of at least one
27 claim of the '105 patent.

28

1 100. Ibiden’s Package Substrates meet or embody the limitations of at least one
2 claim of the ’912 patent.

3 101. Intel makes, uses, offers to sell, sells, and/or imports the Atom series, Core
4 series, Pentium series, Celeron series, Xeon series, Itanium series, and Quark series
5 processors, Chipsets, Wireless Network Adapters, and Ibiden’s Package Substrates within
6 the United States.

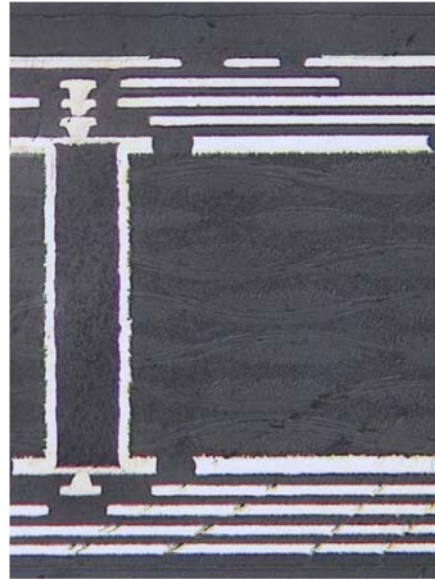
7 102. Ibiden offers to sell and sells its Package Substrates within the United States,
8 such as those which can be found in Intel’s Atom series, Core series, Pentium series, Celeron
9 series, Xeon series, Itanium series, and Quark series processors, Chipsets, and Wireless
10 Network Adapters.

11 103. Defendants have committed and continue to commit acts of infringement under
12 35 U.S.C. § 271 with (i) any version of the Atom series of processors; (ii) any version of the
13 Core series of processors; (iii) any version of the Pentium series of processors; (iv) any
14 version of the Celeron series of processors; (v) any version of the Xeon series of processors;
15 (vi) any version of the Itanium series of processors; (vii) any version of the Quark series of
16 processors; (viii) any processors manufactured in a manner similar to those named in (i)–
17 (vii); (ix) any Intel Chipsets; (x) any Intel Wireless Network Adapters; and (ix) any Package
18 Substrates manufactured by Ibiden for Intel or others (collectively referred to as the “Accused
19 Instrumentalities”).

20 104. The Accused Instrumentalities comprise an article of manufacture, a multilayer
21 electrical device, a product, and/or a circuit board. *See, e.g.:*

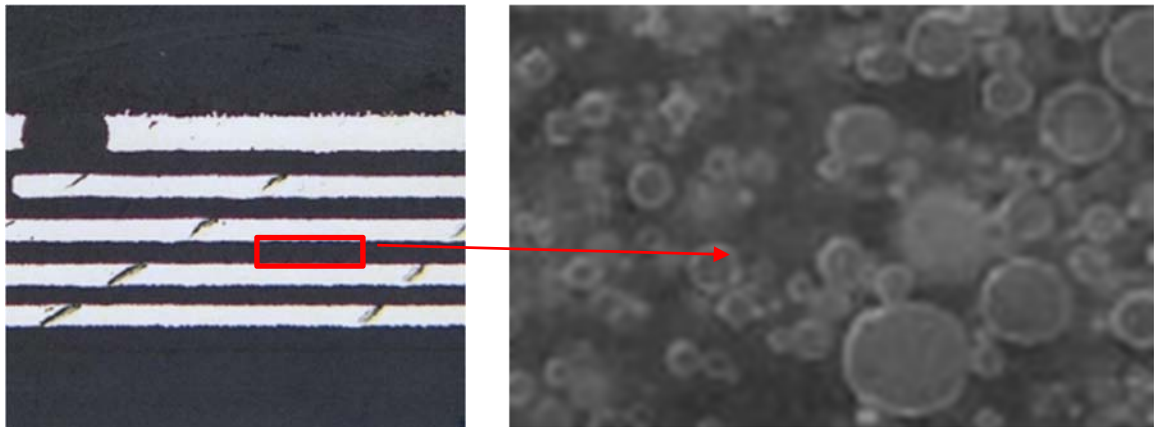
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Cross-section view of Intel Core series processor at 100x magnification.

105. The Accused Instrumentalities comprise an epoxy dielectric material (“the Epoxy”) delivered with solid content (“Solid Content”). *See, e.g.:*

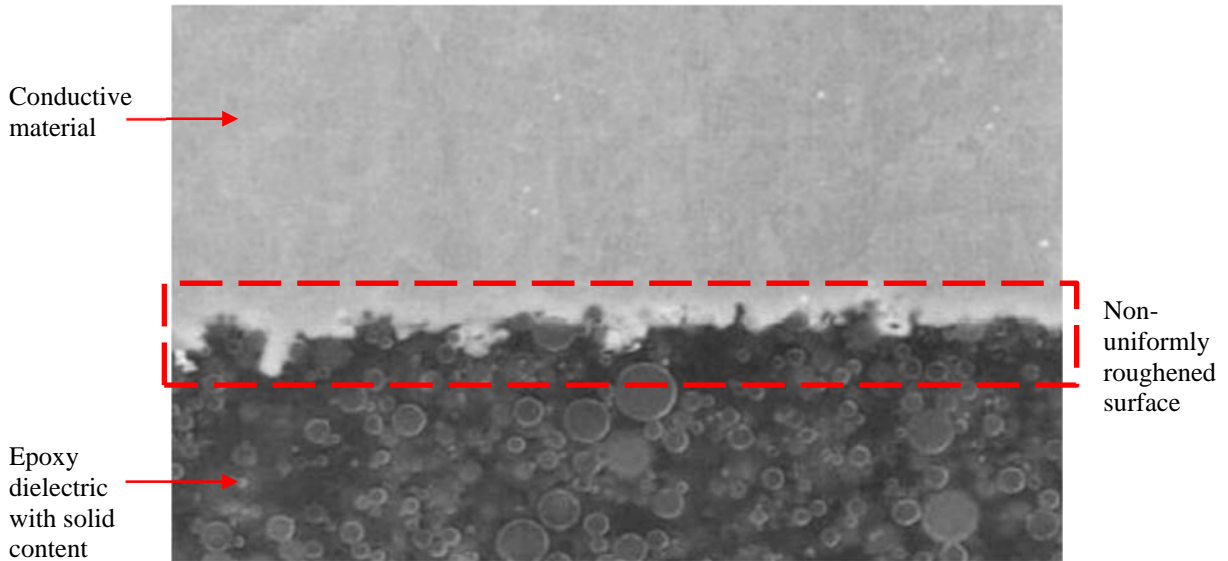


Cross-section view of Intel Core series processor at 200x (left) and >2000x (right) magnification.

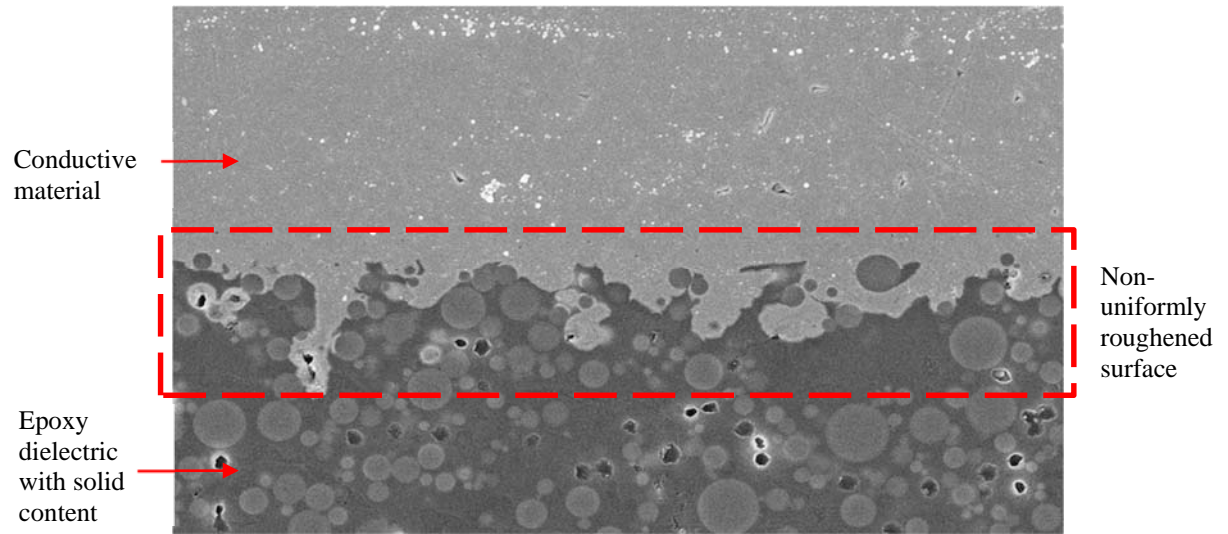
106. The Accused Instrumentalities comprise the Epoxy having been etched (“the Etching”).

107. The Accused Instrumentalities comprise the Epoxy having a non-uniformly roughened surface (“Roughened Surface”) of cavities (“the Cavities”). *See, e.g.:*

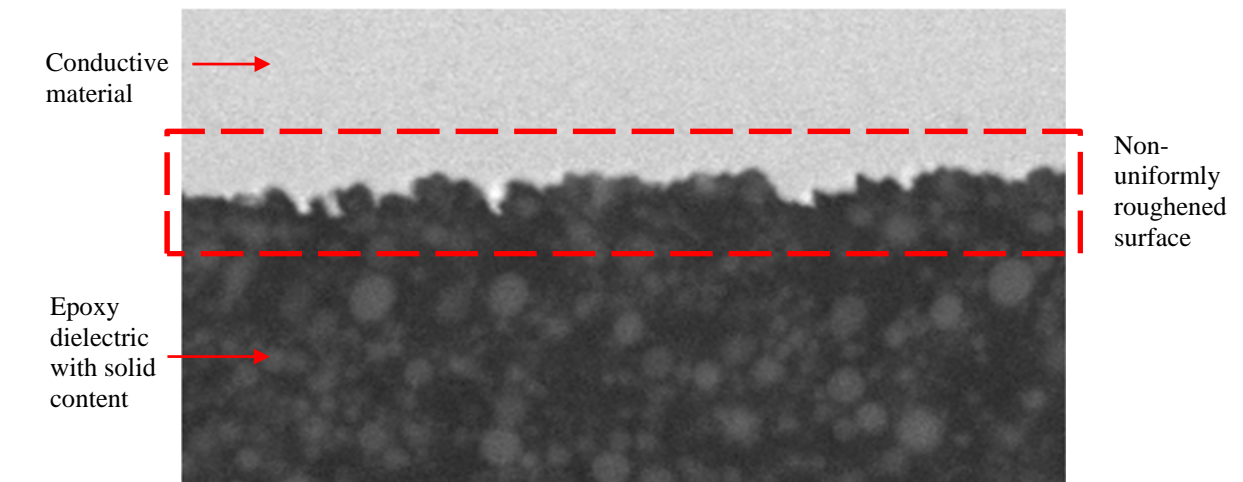
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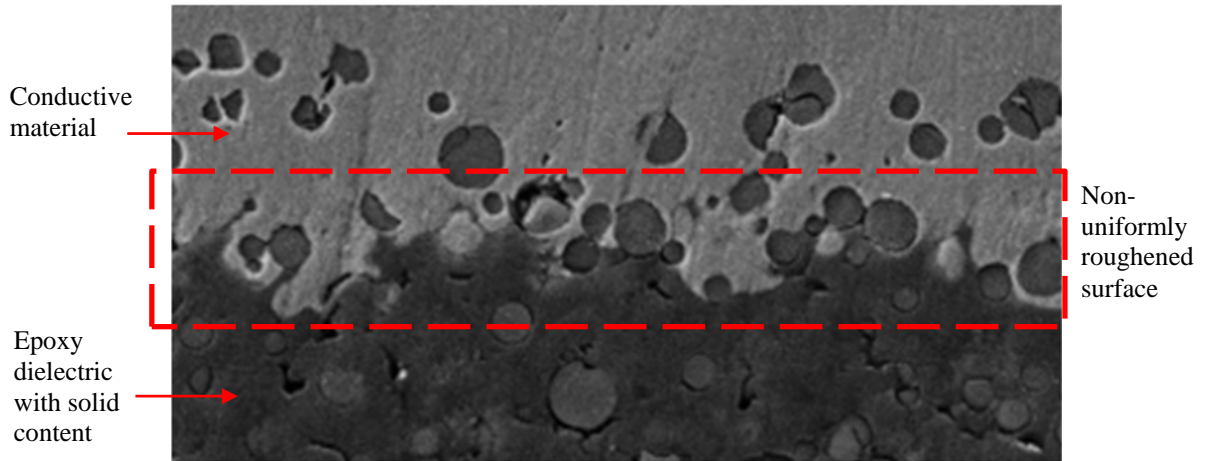
Cross-section of Intel Core series processor at > 2000x magnification.



Cross-section of Intel Atom series processor at >2000x magnification.

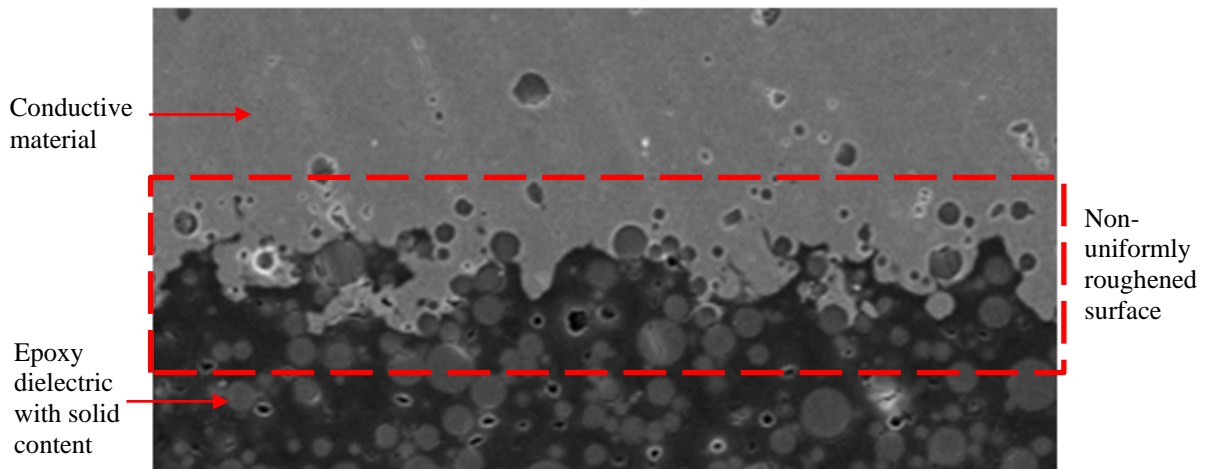


Cross-section of Intel Pentium series processor at 1500x magnification.



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10

Cross-section of Intel Celeron series processor at >2000x magnification.



18

Cross-section of Intel Xeon series processor at >2000x magnification.

19
20

108. The images in Paragraph 102 are representative of the cross-sections of a substrate layer within each of the Accused Instrumentalities.

21
22

109. There is no material difference in the substrate layers between any versions of the Accused Instrumentalities.

23

110. The Roughened Surface is formed by the Etching.

24
25

111. The Etching uses non-homogeneity with the Solid Content to bring about formation of the Roughened Surface.

26

112. The Accused Instrumentalities comprise a conductive material.

27

113. The Accused Instrumentalities comprise a conductive material in the Cavities.

28

114. The Etching includes a first etching and a second etching.

1 115. The Accused Instrumentalities constitute or contain products made by the
2 processes claimed in at least one claim of each of the patents-in-suit.

3 116. The Accused Instrumentalities and/or the portions of the Accused
4 Instrumentalities made by the processes claimed in the patents-in-suit are not materially
5 changed by subsequent processes, and do not become a trivial and nonessential component
6 of another product.

7 117. The Accused Instrumentalities remain essential to the operation and
8 functionality of any product into which they are incorporated.

9 118. In committing the above described acts of infringement under 35 U.S.C. § 271,
10 Defendants acted despite an objectively high likelihood that their actions constituted
11 infringement of at least one valid patent owned by Plaintiff, and Defendants knew or should
12 have known that their actions constituted an unjustifiably high risk of infringement of a valid
13 and enforceable patent.

14 119. Questions of fact common to Defendants exist and will arise in this action.

15 **COUNT ONE: PATENT INFRINGEMENT BY INTEL**

16 120. Plaintiff incorporates by reference the preceding paragraphs as if fully set forth
17 herein.

18 121. Intel has infringed and continues to infringe the patents-in-suit.

19 122. The Accused Instrumentalities meet claims of the patents-in-suit, including, by
20 way of example and not limitation, claim 14 of the '560 patent.

21 123. Intel makes, uses, offers to sell, sells and/or imports the Accused
22 Instrumentalities within the United States or into the United States without authority from
23 Plaintiff.

24 124. Intel therefore infringes the patents-in-suit under 35 U.S.C. § 271(a).

25 125. The Accused Instrumentalities which Intel imports, offers to sell, sells, and/or
26 uses within the United States or into the United States without authority constitute products
27 made by a process claimed in the patents-in-suit, which products are not materially changed
28

1 by a subsequent process and do not become a trivial and nonessential component of another
2 product.

3 126. Intel therefore infringes the patents-in-suit under 35 U.S.C. § 271(g).

4 127. Intel has actual knowledge of the patents-in-suit.

5 128. Intel indirectly infringes the patents-in-suit by inducing infringement by others,
6 such as its suppliers, by, for example, providing design and technical specifications for
7 substrate packages to its suppliers, including Ividen, and requiring its suppliers to meet those
8 specifications. Such design and technical specifications include benchmarks directly related
9 to the invention claimed in the patents-in-suit, including signal integrity, thermal integrity,
10 mechanical integrity, and delamination. Intel also actively markets to, encourages use by,
11 and instructs consumers, businesses, distributors, resellers, computer equipment
12 manufacturers, and sales representatives, to use, promote, market, distribute, and/or sell the
13 Accused Instrumentalities.

14 129. Intel took the above actions intending to cause infringing acts by others.

15 130. Intel was aware of the patents-in-suit and knew that the others' actions, if taken,
16 would constitute infringement of the patents-in-suit. Alternatively, Intel believed there was
17 a high probability that others would infringe the patents-in-suit but remained willfully blind
18 to the infringing nature of others' actions.

19 131. Intel therefore infringes the patents-in-suit under 35 U.S.C. § 271(b).

20 132. Intel indirectly infringes the patents-in-suit by contributing to infringement by
21 others, such as package substrate suppliers, product assemblers, resellers including computer
22 equipment manufacturers, and end-user customers by providing, offering to sell, and/or
23 selling within the United States the Accused Instrumentalities, including the package
24 substrates supplied by Ividen. These Accused Instrumentalities constitute a material part of
25 the inventions claimed in the patents-in-suit, and are used to practice one or more
26 processes/methods covered by the claims of the patents-in-suit. Not only do the Accused
27 Instrumentalities constitute a material part of the invention, but the Accused Instrumentalities
28 are manufactured in a manner which fundamentally encompasses the entire invention.

1 133. In the above offerings to sell and/or selling, Intel has known the package
2 substrates to be especially made or especially adapted for use in an infringement of the
3 patents-in-suit and are not a staple article or commodity of commerce suitable for substantial
4 non-infringing use. The only practical use of the Accused Instrumentalities constitutes
5 infringement of the patents-in-suit. Alternatively, Intel believed that there was a high
6 probability that others would infringe the patents-in-suit but remained willfully blind to the
7 infringing nature of others' actions.

8 134. Intel therefore infringes the patents-in-suit under 35 U.S.C. § 271(c).

9 135. Intel has committed and continues to commit acts of infringement under
10 35 U.S.C. § 271. In committing these acts of infringement, Intel's behavior has been
11 egregious.

12 136. Intel's infringement of the patents-in-suit has been and continues to be willful,
13 wanton, malicious, in bad-faith, deliberate, consciously wrongful, and/or flagrant.

14 137. Intel's acts of infringement have caused damage to Plaintiff. Plaintiff is
15 entitled to recover from Intel the damages sustained by Plaintiff as a result of Intel's wrongful
16 acts in an amount subject to proof at trial. In addition, the infringing acts and practices of
17 Intel have caused, are causing, and unless such acts and practices are enjoined by the Court,
18 will continue to cause immediate and irreparable harm to Plaintiff for which there is no
19 adequate remedy at law, and for which Plaintiff is entitled to injunctive relief under 35 U.S.C.
20 § 283.

21 138. To the extent Intel releases any new version of the Accused Instrumentalities,
22 such instrumentalities will meet the claims of the patents-in-suit and infringe the patents-in-
23 suit under 35 U.S.C. §§ 271(a)–(c) and (g) in ways analogous to Intel's current infringement
24 described above.

25 **COUNT TWO: PATENT INFRINGEMENT BY IBIDEN**

26 139. Plaintiff incorporates by reference the preceding paragraphs as if fully set forth
27 herein.

28 140. I Biden has infringed and continues to infringe the patents-in-suit.

1 141. The Accused Instrumentalities meet claims of the patents-in-suit, including, by
2 way of example and not limitation, claim 14 of the '560 patent.

3 142. Ividen offers to sell and sells the Accused Instrumentalities within the United
4 States without authority from Plaintiff.

5 143. Ividen therefore infringes the patents-in-suit under 35 U.S.C. § 271(a).

6 144. The Ividen Package Substrates which Ividen offers to sell and sells within the
7 United States without authority constitute products made by a process claimed in the patents-
8 in-suit, which products are not materially changed by a subsequent process and do not
9 become a trivial and nonessential component of another product.

10 145. Ividen therefore infringes the patents-in-suit under 35 U.S.C. § 271(g).

11 146. Ividen has actual knowledge of the patents-in-suit.

12 147. Ividen indirectly infringes the patents-in-suit by inducing infringement by
13 others, such as Intel and other companies supplied by Ividen, by, for example, collaborating
14 on design and technical specifications for the Accused Instrumentalities, and by providing
15 technical instruction for use of package substrates in the assembly and manufacture of the
16 Accused Instrumentalities.

17 148. Ividen took the above actions intending to cause infringing acts by others.

18 149. Ividen was aware of the patents-in-suit and knew that the others' actions, if
19 taken, would constitute infringement of the patents-in-suit. Alternatively, Ividen believed
20 there was a high probability that others would infringe the patents-in-suit but remained
21 willfully blind to the infringing nature of others' actions.

22 150. Ividen therefore infringes the patents-in-suit under 35 U.S.C. § 271(b).

23 151. Ividen indirectly infringes the patents-in-suit by contributing to infringement
24 by others, such as Intel and other companies supplied by Ividen by offering to sell and/or
25 selling within the United States products that contain components that constitute a material
26 part of the inventions claimed in the patents-in-suit. Such components are, for example,
27 package substrates used in the manufacturing of the Accused Instrumentalities. The only
28 substantial use is to be incorporated into the Accused Instrumentalities. Not only do the

1 Accused Instrumentalities constitute a material part of the invention, but the Accused
2 Instrumentalities are manufactured in a manner which fundamentally encompasses the entire
3 invention.

4 152. In the above offering to sell and/or selling, Ividen has known these components
5 to be especially made or especially adapted for use in an infringement of the patents-in-suit
6 and that these components are not a staple article or commodity of commerce suitable for
7 substantial non-infringing use. The only practical use of the Accused Instrumentalities
8 constitutes infringement of the patents-in-suit. Alternatively, Ividen believed there was a
9 high probability that others would infringe the patents-in-suit but remained willfully blind to
10 the infringing nature of others' actions. Ividen therefore infringes the patents-in-suit under
11 35 U.S.C. § 271(c).

12 153. Ividen has committed and continues to commit acts of infringement under
13 35 U.S.C. § 271. In committing these acts of infringement, Ividen's behavior has been
14 egregious.

15 154. Ividen's infringement of the patents-in-suit has been and continues to be
16 willful, wanton, malicious, in bad-faith, deliberate, consciously wrongful, and/or flagrant.

17 155. Ividen's acts of infringement have caused damage to Plaintiff. Plaintiff is
18 entitled to recover from Ividen the damages sustained by Plaintiff as a result of Ividen's
19 wrongful acts in an amount subject to proof at trial. In addition, the infringing acts and
20 practices of Ividen have caused, are causing, and unless such acts and practices are enjoined
21 by the Court, will continue to cause immediate and irreparable harm to Plaintiff for which
22 there is no adequate remedy at law, and for which Plaintiff is entitled to injunctive relief
23 under 35 U.S.C. § 283.

24 **DEMAND FOR JURY TRIAL**

25 Pursuant to Rule 38 of the Federal Rules of Civil Procedure, Plaintiff hereby demands
26 a jury for all issues so triable.

27 **PRAYER FOR RELIEF**

28 1. A judgment that Defendants have directly infringed the patents-in-suit;

- 1 2. A judgment that Defendants have induced infringement of the patents-in-suit;
- 2 3. A judgment that Defendants have contributorily infringed the patents-in-suit;
- 3 4. A preliminary and permanent injunction preventing Defendants and their
4 officers, directors, agents, servants, employees, attorneys, licensees, successors, and assigns,
5 and those in active concert or participation with any of them, from directly infringing,
6 contributorily infringing, and/or inducing infringement of the patents-in-suit;
- 7 5. A judgment that Defendants' infringement of the patents-in-suit has been
8 willful, wanton, malicious, in bad-faith, deliberate, consciously wrongful, and/or flagrant;
- 9 6. A ruling that this case is exceptional under 35 U.S.C. § 285, and a judgment
10 awarding to Plaintiff its attorneys' fees incurred in prosecuting this action;
- 11 7. A judgment and order requiring Defendants to pay Plaintiff damages under 35
12 U.S.C. § 284, including supplemental damages for any continuing post-verdict infringement
13 up until entry of the final judgment, with an accounting, as needed, and enhanced damages
14 as provided by 35 U.S.C. § 284;
- 15 8. A judgment and order requiring Defendants to pay Plaintiff the costs of this
16 action (including all disbursements);
- 17 9. A judgment and order requiring Defendants to pay Plaintiff pre-judgment and
18 post-judgment interest on the damages award;
- 19 10. In the event a permanent injunction preventing future acts of infringement is
20 not granted, a judgment and order awarding Plaintiff a compulsory ongoing licensing fee;
21 and
- 22 11. Such other and further relief as the Court may deem just a proper.

23
24 DATED this 26th day of January, 2017.

25 **CALDWELL CASSADY CURRY P.C**

26 By /s/ Bradley W. Caldwell (pro hac vice)

27 Bradley W. Caldwell (*pro hac vice*)

 Jason D. Cassady (*pro hac vice*)

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8 **ATTORNEYS FOR PLAINTIFF**
9 **CONTINENTAL CIRCUITS, LLC**

10 **CERTIFICATE OF SERVICE**

11 The undersigned certifies that all counsel of record who have consented to electronic
12 service are being served with a copy of the foregoing document via the Court's CM/ECF
13 system on this 26th day of January, 2017.

14 /s/ Bradley W. Caldwell (*pro hac vice*)
15 Bradley W. Caldwell
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