

**IN THE UNITED STATES DISTRICT COURT
FOR THE EASTERN DISTRICT OF TEXAS
TYLER DIVISION**

CODEC TECHNOLOGIES LLC,

Plaintiff,

v.

TEXAS INSTRUMENTS INCORPORATED,

Defendant.

CIVIL ACTION NO 6:18-cv-54

JURY TRIAL DEMANDED

ORIGINAL COMPLAINT FOR PATENT INFRINGEMENT

1. This is an action for patent infringement in which Codec Technologies LLC makes the following allegations against Texas Instruments Incorporated.

PARTIES

2. Plaintiff Codec Technologies LLC (“Plaintiff” or “Codec Technologies”) is a Texas limited liability company with its principal place of business at 903 E. 18th Street, Suite 224, Plano, Texas 75074.

3. On information and belief, Texas Instruments Incorporated (“Defendant”) is a corporation organized and existing under the laws of the State of Delaware, with its principal place of business at 12500 TI Blvd, Dallas, TX 75243.

JURISDICTION AND VENUE

4. This action arises under the patent laws of the United States, Title 35 of the United States Code. This Court has subject matter jurisdiction pursuant to 28 U.S.C. §§ 1331 and 1338(a).

5. Venue is proper in this district under 28 U.S.C. §§ 1391(c) and 1400(b). On information and belief, acts of infringement have been committed in this District. Additionally, Defendant has a regular and established place of business in this District, including, without limitation, its Richardson fab located at 300 W Renner Rd, Richardson, TX 75080.

6. On information and belief, Defendant is subject to this Court's specific and general personal jurisdiction pursuant to due process and/or the Texas Long Arm Statute, due at least to its substantial business in this forum, including: (i) at least a portion of the infringements alleged herein; and (ii) regularly doing or soliciting business, engaging in other persistent courses of conduct, and/or deriving substantial revenue from goods and services provided to individuals in Texas and in this Judicial District.

COUNT I
INFRINGEMENT OF U.S. PATENT NO. 6,825,780

7. Plaintiff is the owner of United States Patent No. 6,825,780 ("the '780 patent") entitled "Multiple codec-imager system and method." The '780 Patent issued on November 30, 2004. A true and correct copy of the '780 Patent is attached as Exhibit A.

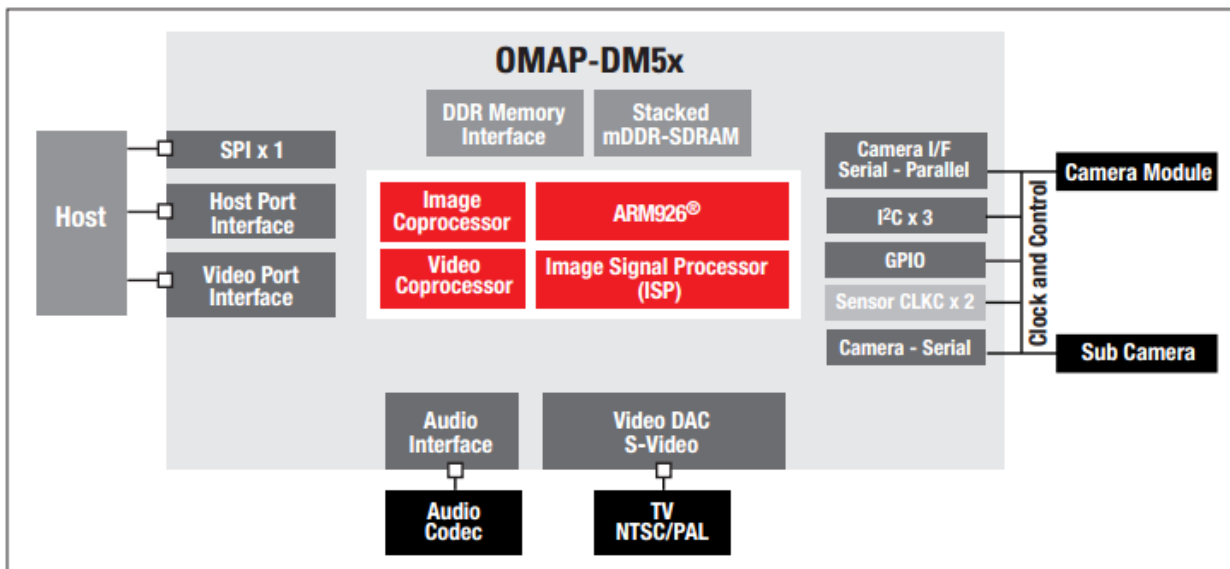
8. Defendant owns, uses, operates, advertises, controls, sells, and otherwise provides products and/or services that infringe the '780 patent. The '780 patent provides, among other things, "[a] system and method are provided for compressing data utilizing multiple encoders on a single integrated circuit. Initially, data is received in a single integrated circuit. The data is then encoded utilizing a plurality of encoders incorporated on the single integrated circuit. Another single module system and method are provided for compressing data. In use, photons are received utilizing a single module. Thereafter, compressed data representative of the photons is outputted utilizing the single module."

9. Defendant directly and/or through intermediaries, made, has made, used, imported, provided, supplied, distributed, sold, and/or offered for sale products and/or services that infringed one or more claims of the '780 patent, including at least Claim 9, in this district and elsewhere in the United States. By making, using, importing, offering for sale, and/or selling such products and services, and all like products and services, Defendant has injured Plaintiff and is thus liable for infringement of the '780 patent pursuant to 35 U.S.C. § 271.

10. Claim 9 reads, "[a] single integrated circuit for compressing data, comprising: a first encoder embodied on the single integrated circuit including circuitry for electronically encoding a first Set of data; and a second encoder embodied on the same single integrated circuit as the first encoder for electronically encoding a second set of data; wherein the data is compressed utilizing the encoders."

11. Based on present information and belief, by way of an illustrative example, the Texas Instruments OMAP-DM5x family of multimedia coprocessors (“OMAP-DM5x”), comprises an integrated circuit.

12. Based on present information and belief, the OMAP-DM5x has multiple encoders capable of encoding at least video data.



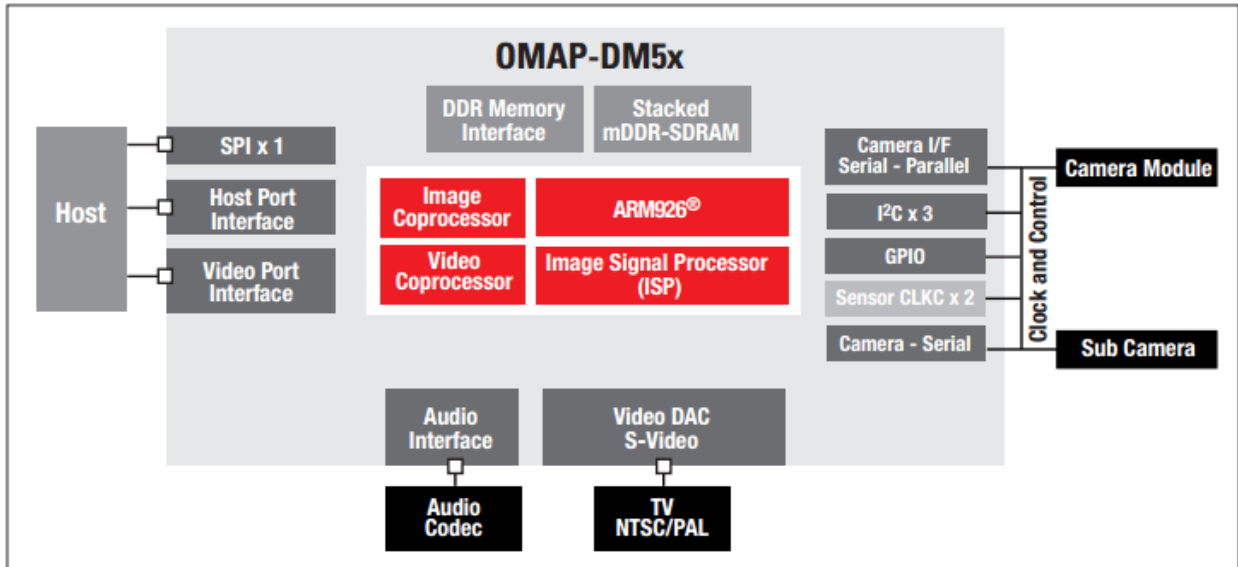
▲ OMAP-DM5x coprocessor family block diagram

	OMAP-DM510	OMAP-DM515	OMAP-DM525
Still image support	Up to 8 megapixels	Up to 12 megapixels	Up to 20 megapixels
H.264 camcorder performance (encode and decode)	720p @ 24 fps		
Universal MPEG-4 decode	WVGA @ 25 fps		
Universal H.264 decode	WVGA @ 25 fps		
H.264 encode	WVGA @ 25 fps		
JPEG capability	Up to 90 megapixels per second		
Host interfaces	SPI, HPI, I ² C, parallel		
Camera interfaces	Serial parallel		
Memory	Includes 128-Mb stacked mDDR	Includes 256-Mb stacked mDDR	Includes 256-Mb stacked mDDR
Display/TV-out	Composite/S-video		
Package	8 x 8 mm ² BGA, 0.5-mm ball pitch		

▲ *OMAP-DM5x coprocessor family features table*

<http://www.ti.com/lit/ml/swpt036/swpt036.pdf>

13. Based on present information and belief, the OMAP-DM5x has multiple encoders capable of encoding at least image data.



▲ OMAP-DM5x coprocessor family block diagram

	OMAP-DM510	OMAP-DM515	OMAP-DM525
Still image support	Up to 8 megapixels	Up to 12 megapixels	Up to 20 megapixels
H.264 camcorder performance (encode and decode)	720p @ 24 fps		
Universal MPEG-4 decode	WVGA @ 25 fps		
Universal H.264 decode	WVGA @ 25 fps		
H.264 encode	WVGA @ 25 fps		
JPEG capability	Up to 90 megapixels per second		
Host interfaces	SPI, HPI, I²C, parallel		
Camera interfaces	Serial parallel		
Memory	Includes 128-Mb stacked mDDR	Includes 256-Mb stacked mDDR	Includes 256-Mb stacked mDDR
Display/TV-out	Composite/S-video		
Package	8 x 8 mm² BGA, 0.5-mm ball pitch		

▲ OMAP-DM5x coprocessor family features table

<http://www.ti.com/lit/ml/swpt036/swpt036.pdf>

14. Based on information and belief, the OMAP-DM5x employs video HW codecs for video data and image HW codecs for image data. The data is necessarily compressed through the respective encoders.

15. Based on present information and belief, Defendant directly and/or through intermediaries, made, has made, used, imported, provided, supplied, distributed, sold, and/or offered for sale other infringing devices, including but not limited to, processors, coprocessors, and Systems on a chip (“SOC’s”). In the alternative, because the manner of use by Defendant differs in no substantial way from the language of the claims, if Defendant is not found to literally infringe, Defendant infringes under the doctrine of equivalents.

16. Defendant’s aforesaid activities have been without authority and/or license from Plaintiff.

17. Plaintiff is entitled to recover from Defendant the damages sustained by Plaintiff as a result of the Defendant’s wrongful acts in an amount subject to proof at trial, which, by law, cannot be less than a reasonable royalty, together with interest and costs as fixed by this Court under 35 U.S.C. § 284.

PRAYER FOR RELIEF

WHEREFORE, Plaintiff respectfully requests that this Court enter:

1. A judgment in favor of Plaintiff that Defendant has infringed the ‘780 Patent;
2. A judgment and order requiring Defendant to pay Plaintiff its damages, costs, expenses, and prejudgment and post-judgment interest for Defendant’s infringement of the ‘780 Patent as provided under 35 U.S.C. § 284;
3. An award to Plaintiff for enhanced damages resulting from the knowing, deliberate, and willful nature of Defendant’s prohibited conduct with notice being made at least as early as the date of the filing of this Complaint, as provided under 35 U.S.C. § 284;
4. A judgment and order finding that this is an exceptional case within the meaning of 35 U.S.C. § 285 and awarding to Plaintiff its reasonable attorneys’ fees; and
5. Any and all other relief to which Plaintiff may show itself to be entitled.

DEMAND FOR JURY TRIAL

Plaintiff, under Rule 38 of the Federal Rules of Civil Procedure, requests a trial by jury of any issues so triable by right.

Respectfully Submitted,

CODEC TECHNOLOGIES LLC

/s/ Papool S. Chaudhari

Dated: February 5, 2018

By: _____

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