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	Attorneys for Plaintiff			
12	AU Optronics Corporation America			
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	UNITED STATES DIS	TDICT COLIDT		
14	UNITED STATES DIS	TRICI COURT		
15	NORTHERN DISTRICT	OF CALIFORNIA		
13				
16	AU OPTRONICS CORPORATION AMERICA,) Case No.		
	a California corporation,)		
17		COMPLAINT FOR DECLARATORY		
18	Plaintiff,	JUDGMENT AND EQUITABLE		
) RELIEF		
19	VS.			
20	VISTA PEAK VENTURES, LLC	DEMAND FOR JURY TRIAL		
20	I VISTATEAN VENTUNES, LLC	,		

Defendant.

Plaintiff AU OPTRONICS CORPORATION AMERICA, for its Complaint for Declaratory Judgment and Equitable Relief, avers and alleges as follows:

THE PARTIES, JURISDICTION, AND VENUE

1. Plaintiff AU OPTRONICS CORPORATION AMERICA ("AUO USA") is a corporation organized and existing under the laws of the state of California with its principal place of

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business and center of operations located at 1525 McCarthy Blvd., Suite 218, Milpitas, California 95035.

- 2. Defendant VISTA PEAK VENTURES, LLC ("Defendant") is a limited liability company that purports to be organized and existing under the laws of the state of Texas with its business address located at 1400 Preston Rd, Suite 472, Plano, TX 75093.
- 3. Defendant has filed three separate actions in the Marshall Division of the District Court for the Eastern District of Texas accusing AUO USA's parent company, AU Optronics Corporation ("AUO"), of infringing the following United States Patents: U.S. Patent No. 5,929,947 ("the '947 patent"), U.S. Patent No. 6,579,749 ("the '749 patent"), U.S. Patent No. 6,674,093 ("the '093 patent"), U.S. Patent No. 6,800,872 ("the '872 patent"), U.S. Patent No. 6,891,196 ("the '196 patent"), U.S. Patent No. 7,088,401 ("the '401 patent"), U.S. Patent No. 6,404,474 ("the '474 patent"), U.S. Patent No. 6,657,699 ("the '699 patent"), U.S. Patent No. 7,009,673 ("the '673 patent"), U.S. Patent No. 7,499,119 ("the '119 patent"), U.S. Patent No. 6,781,643 ("the '643 patent"), U.S. Patent No. 7,046,327 ("the '327 patent"), and U.S. Patent No. 6,730,970 ("the '970) patent"), collectively, the "Asserted Patents." See Vista Peak Ventures, LLC v. AU Optronics Corp., Case Nos. 2:18-cv-00276-JRG, ¶¶ 10, 22, 34, 46, 57, 68, 80; 2:18-cv-00278-JRG, ¶¶ 10, 33, 44, 45, 56; and 2:18-cv-00279-JRG, ¶¶ 10, 34, 45.
- 4. In an effort to manufacture personal jurisdiction over AUO, Defendant further alleged in each of these complaints that AUO controls AUO USA and that AUO USA "has committed acts within Texas giving rise to this action and/or has established minimum contacts with Texas such that personal jurisdiction over AUO would not offend traditional notions of fair play and substantial justice." See Vista Peak Ventures, LLC v. AU Optronics Corp., Case Nos. 2:18-cv-00276-JRG, ¶¶ 8-9; 2:18-cv-00278-JRG, ¶¶ 8-9; and 2:18-cv-00279-JRG, ¶¶ 8-9.
- 5. In ad damnum clause in each of its complaints, Defendant requests the court, inter alia, to enjoin AUO USA from "making, using, selling, offering to sell, or importing any products that infringe the Asserted Patents, and any other injunctive relief the Court deems just and equitable." See Vista Peak Ventures, LLC v. AU Optronics Corp., Case Nos. 2:18-cv-00276-JRG, pp. 28-29; 2:18-cv-00278-JRG, pp. 22; and 2:18-cv-00279-JRG, pp. 19-20.

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	6.	Consequently, this action arises under the patent laws of the United States, namely 35
U.S.C.	88 271.	. 281, and 284-285, among others.

- 7. Jurisdiction: This Court has subject matter jurisdiction pursuant to 28 U.S.C. §§ 1331 and 1338(a).
- 8. Intradistrict Assignment: Pursuant to Civil Local Rule 3-5(b), this case is properly assigned to the San Jose division.
- 9. Defendant is subject to this Court's specific personal jurisdiction pursuant to due process and/or the California Long Arm Statute, due at least to Defendant's substantial business in this State and judicial district, including its engagement of Ascenda Law Group of 333 W. San Carlos Street, Suite 200, San Jose, California 95110 as counsel to acquire the Asserted Patents, see Assignment Reel/Frame No. 045469/0023 from Getner Foundation LLC to Vista Peak Ventures, LLC (attached hereto as Exhibit A), and, upon information and belief, authorizing that Firm's filing of powers of attorney to act on behalf of Defendant before the United States Patent and Trademark Office with respect to at least some of the Asserted Patents after their acquisition.
- 10. Defendant is also subject to this Court's specific personal jurisdiction pursuant to due process and/or the California Long Arm Statute due at least to Defendant's targeting of specific residents of this State and judicial district, including AUO USA, against whom Defendant seeks injunctive relief in its complaints filed in the Eastern District of Texas based upon its assertions that AUO USA both infringes the Asserted Patents and aids in AUO's alleged infringement of the Asserted Patents. See Vista Peak Ventures, LLC v. AU Optronics Corp., Case Nos. 2:18-cv-00276-JRG, ¶¶ 8-9; 2:18-cv-00278-JRG, ¶¶ 8-9; and 2:18-cv-00279-JRG, ¶¶ 8-9. Had Defendant included AUO USA as a party defendant in its complaints against AUO, 28 U.S.C. §§ 1391(c)(3) and 1400(b) would require those complaints to have been filed in this District. Defendant's failure to name AUO USA as a party defendant in its complaints notwithstanding Defendant's claim for injunctive relief against AUO USA for alleged infringement of the Asserted Patents is tantamount to depriving AUO USA of its procedural rights to defend such claims in the legally appropriate venue, namely, this District.
 - 11. Venue is appropriate pursuant to 28 U.S.C. § 1391(b) because this District is the

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location where a substantial portion of the events at issue in this suit occurred.

FACTUAL BACKGROUND

- 12. Each of the Asserted Patents was issued between July 27, 1999, and March 3, 2009, to NEC Corporation or its affiliates (collectively, "NEC").
- 13. Since at least 2001, AUO and AUO USA have been and continue to be engaged in the business of thin film transistor liquid crystal displays ("TFT-LCDs") and other flat panel displays used in a wide variety of applications.
- 14. Since at least 2003, NEC has been a customer of AUO, purchasing its TFT-LCD display products. See https://www.auo.com/en-global/New Archive/detail/news Product 20030120.
- 15. Upon information and belief, NEC has been aware of the TFT-LCD technology of AUO that Defendant accuses of infringement in its complaints since at least 2003.
- 16. NEC assigned the Asserted Patents to Getner Foundation LLC ("Getner") in April 2011. See Assignment Reel Frame No. 026312/0213 from NEC Corporation to Getner Foundation LLC (attached hereto as Exhibit B) and Assignment Reel Frame No. 026254/0400 from NEC Corporation to Getner Foundation LLC (attached hereto as Exhibit C). Getner assigned the Asserted Patents to Defendant in February 2018. See Exhibit A.
- 17. For each Asserted Patent, Defendant has accused AUO of patent infringement under 35 U.S.C. § 271(a) by "making, offering for sale, selling, and/or importing those TFT-LCD panels, their components, and/or products containing same that incorporate the fundamental technologies covered by [one of the Asserted Patents], or by having [AUO USA] do the same." See Vista Peak *Ventures, LLC v. AU Optronics Corp.*, Case Nos. 2:18-cv-00276-JRG, ¶¶ 24, 36, 48, 59, 70, 82; 2:18-cv-00278-JRG, ¶¶ 24, 35, 47, 58; and 2:18-cv-00279-JRG, ¶¶ 25, 36, 47.
- 18. Defendant is seeking from the District Court in the Eastern District of Texas: (i) damages sustained as a result of the alleged infringements, including up to treble damages; and (ii) "[a] preliminary and permanent injunction against AUO, [AUO USA], or anyone acting on its behalf from making, using, selling, offering to sell, or importing any products that infringe the Asserted Patents, and any other injunctive relief the Court deems just and equitable...." See Vista

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Peak Ventures,	LLC v. AU Optronic	es Corp., Case	Nos. 2:18-c	v-00276-JRG, ¶	93; 2:18-cv-00278
JRG, ¶69; and 2	2:18-cv-00279-JRG,	57.			

FIRST CAUSE OF ACTION:

DECLARATORY JUDGMENT OF

NON-INFRINGEMENT OF EACH OF THE ASSERTED PATENTS

- 19. AUO USA hereby incorporates by reference Paragraphs 1 through 18, above, as if fully set forth herein.
- 20. On July 10, 2018, AUO USA received notice of three separate lawsuits Defendant filed against AUO in which Defendant, inter alia, also accused AUO USA of engaging in conduct constituting direct and indirect infringement of the Asserted Patents in the United States and aiding AUO in such infringement.
- 21. AUO USA denies that it has directly infringed any valid claim of the Asserted Patents and further denies that it has induced the infringement or contributed to the infringement of any valid claim of the Asserted Patents, either individually or in concert with AUO.
- 22. By virtue of the foregoing, an actual and justifiable controversy has arisen and now exists between AUO USA and Defendant, within the jurisdiction of the Court, regarding whether AUO USA infringes one or more of the Asserted Patents.
- Declaratory relief is both appropriate and necessary in light of the conflicting 23. positions of the parties. AUO USA desires a judicial determination of the parties' respective rights and obligations in connection with each of the Asserted Patents.
- For the reasons set forth above, AUO USA respectfully requests that this Court 24. declare that, for each claim in the Asserted Patents for which Defendant claims infringement by AUO USA, AUO USA has not directly infringed each one of the Asserted Patents, either literally or under the doctrine of equivalents, and has neither induced the infringement nor contributed to the infringement, of such claims either individually or in concert with AUO. Specifically, AUO USA respectfully requests that this Court issue a judicial declaration to the effect that (inter alia, without limitation) AUO USA's:

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•	Use, importation, sales, and offers to sell TFT-LCD panels does not directly or
	indirectly infringe the '947 patent, either literally or under the doctrine of equivalents;
•	Use, importation, sales, and offers to sell TFT-LCD panels does not directly or
	indirectly infringe the '749 patent, either literally or under the doctrine of equivalents;
•	Use, importation, sales, and offers to sell TFT-LCD panels does not directly or

Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '872 patent, either literally or under the doctrine of equivalents;

indirectly infringe the '093 patent, either literally or under the doctrine of equivalents;

- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '196 patent, either literally or under the doctrine of equivalents;
- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '401 patent, either literally or under the doctrine of equivalents;
- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '474 patent, either literally or under the doctrine of equivalents;
- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '699 patent, either literally or under the doctrine of equivalents;
- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '673 patent, either literally or under the doctrine of equivalents;
- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '119 patent, either literally or under the doctrine of equivalents;
- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '643 patent, either literally or under the doctrine of equivalents;
- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '327 patent, either literally or under the doctrine of equivalents;
- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '970 patent, either literally or under the doctrine of equivalents; and

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Actions do not constitute inducement of infringement or contributory infringement of any claim in the Asserted Patents.

SECOND CAUSE OF ACTION:

DECLARATORY JUDGMENT OF

NO WILLFUL INFRINGEMENT OF EACH OF THE ASSERTED PATENTS

- 25. AUO USA hereby incorporates by reference Paragraphs 1 through 18, above, as if fully set forth herein.
- 26. On July 10, 2018, AUO USA received notice of three separate lawsuits Defendant filed against AUO in which Defendant, inter alia, accused AUO USA of engaging in alleged willful infringement of the Asserted Patents in the United States. Vista Peak Ventures, LLC v. AU Optronics Corp., Case Nos. 2:18-cv-00276-JRG, ¶¶ 29, 41, 52, 63, 75, 87; 2:18-cv-00278-JRG, ¶¶ 28, 40, 51, 63; and 2:18-cv-00279-JRG, ¶¶ 29, 40, 51.
- 27. AUO USA denies that it infringes, directly or indirectly, any valid claim of the Asserted Patents.
- 28. By virtue of the foregoing, an actual and justifiable controversy has arisen and now exists between AUO USA and Defendant, within the jurisdiction of the Court, regarding whether AUO USA engaged in any course of conduct or possessed the requisite intent to willfully infringe one or more of the Asserted Patents.
- 29. Declaratory relief is both appropriate and necessary in light of the conflicting positions of the parties. AUO USA desires a judicial determination of the parties' respective rights and obligations in connection with each of the Asserted Patents.
- 30. For the reasons set forth above, AUO USA respectfully requests that this Court declare that, for each claim in the Asserted Patents for which Defendant claims infringement by AUO USA, AUO USA does not willfully infringe such claims. Specifically, AUO USA respectfully requests that this Court issue a judicial declaration to the effect that (*inter alia*, without limitation) AUO USA's:
 - Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '947 patent;

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- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '749 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '093 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '872 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '196 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '401 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '474 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '699 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '673 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '119 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '643 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '327 patent; and
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '970 patent.

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THIRD CAUSE OF ACTION:

DECLARATORY JUDGMENT OF

INVALIDITY OF EACH OF THE ASSERTED PATENTS

- 31. AUO USA hereby incorporates by reference Paragraphs 1 through 18, above, as if fully set forth herein.
- 32. On July 10, 2018, AUO USA received notice of three separate lawsuits Defendant filed against AUO in which Defendant accused AUO USA of engaging in alleged infringement of the Asserted Patents. Vista Peak Ventures, LLC v. AU Optronics Corp., Case Nos. 2:18-cv-00276-JRG, ¶¶ 29, 41, 52, 63, 75, 87; 2:18-cv-00278-JRG, ¶¶ 28, 40, 51, 63; and 2:18-cv-00279-JRG, ¶¶ 29, 40, 51.
- 33. AUO USA denies that it infringes, directly or indirectly, any valid claim of the Asserted Patents.
- 34. By virtue of the foregoing, an actual and justifiable controversy has arisen and now exists between AUO USA and Defendant, within the jurisdiction of the Court, regarding whether each claim of the Asserted Patents is valid.
- 35. Declaratory relief is both appropriate and necessary in light of the conflicting positions of the parties. AUO USA desires a judicial determination of the parties' respective rights and obligations in connection with the validity of every claim in each of the Asserted Patents.
- For the reasons set forth above, AUO USA respectfully requests that this Court 36. declare that each claim in the Asserted Patents for which Defendant claims infringement by AUO USA is invalid for failing to satisfy all of the requirements and conditions for patentability specified in 35 U.S.C. §§ 101, 102, 103, and 112. AUO USA respectfully requests that this Court issue a judicial declaration to the effect that (*inter alia*, without limitation):
 - Any claim of the '947 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;
 - Any claim of the '749 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;

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•	Any claim of the '093 patent that Defendant asserts AUO USA infringes in its
	complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;
•	Any claim of the '872 patent that Defendant asserts AUO USA infringes in its
	complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;

- Any claim of the '196 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;
- Any claim of the '401 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;
- Any claim of the '474 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;
- Any claim of the '699 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;
- Any claim of the '673 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;
- Any claim of the '119 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;
- Any claim of the '643 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112;
- Any claim of the '327 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112; and
- Any claim of the '970 patent that Defendant asserts AUO USA infringes in its complaints is invalid under one or more of 35 U.S.C. §§ 101, 102, 103, and 112.

FOURTH CAUSE OF ACTION:

DEFENDANTS' RELIEF UNDER THE ASSERTED PATENTS IS BARRED BY EQUITABLE ESTOPPEL

- 37. AUO USA hereby incorporates by reference Paragraphs 1 through 18, above, as if fully set forth herein.
 - 38. The Asserted Patents issued to NEC before and after 2003.

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39.	Upon	information	and	belief,	NEC	has	been	aware	of	various	AUO	display
technologies	since at	least 2003 ar	nd aw	are of t	he TF7	-LC	D tech	nology	accı	used of in	nfringe	ment by
Defendant in	its com	plaints since a	roun	d that sa	me tim	ne.						

- 40. Neither AUO nor AUO USA has been accused of infringing the Asserted Patents until February 2018, years after the time they began selling the products accused of infringing those patents and years after NEC, the original owner of the Asserted Patents, had been made aware of the accused AUO display technologies.
- 41. As a result of the inexcusable delay in bringing suit against AUO and AUO USA for alleged infringement of the Asserted Patents, and AUO and AUO USA's detrimental reliance on NEC having not brought any infringement action for the Asserted Patents, Defendant should be estopped from obtaining relief for any alleged patent infringement.
- 42. Under the circumstances, it would be against equity and good conscience to permit Defendant to obtain any relief under the Asserted Patents, including any damages, including enhanced damages, or injunctions based on alleged infringements of the same.

PRAYER FOR RELIEF

WHEREFORE, AUO USA prays that the Court:

- 1. Determine and declare the parties' respective rights and obligations under the Asserted Patents.
- 2. Find and declare that AUO USA does not infringe each of the Asserted Patents, directly or indirectly, literally or under the doctrine of equivalents, and in no event willfully.
- 3. Find and declare that each claim asserted by Defendant for each Asserted Patent is invalid.
- 4. Find and declare that Defendant is equitably estopped from any relief under the Asserted Patents.
 - 5. Award AUO USA its reasonable attorney fees and costs incurred in this matter.
- 6. Award AUO USA such other and further relief as the Court deems just and proper, premises considered.

Case 3:18-cv-04574-SK Document 1 Filed 07/27/18 Page 12 of 134

Dated: July 27, 2018 Respectfully submitted, LOCKE LORD LLP By: /s/ Regina J. McClendon Regina J. McClendon Attorneys for Plaintiff AU OPTRONICS CORPORATION AMERICA **DEMAND FOR JURY TRIAL** Pursuant to Fed. R. Civ. P. 38, Plaintiff AU Optronics Corporation America hereby demands a trial by jury. Dated: July 27, 2018 LOCKE LORD LLP 101 Montgomery Street, Suite 1950 San Francisco, CA 94104 By: /s/ Regina J. McClendon Attorneys for Plaintiff AU OPTRONICS CORPORATION AMERICA

Exhibit A

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1 Stylesheet Version v1.2 EPAS ID: PAT4843891

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
GETNER FOUNDATION LLC	02/13/2018

RECEIVING PARTY DATA

Name: VISTA PEAK VENTURES, LLC	
Street Address: 1400 PRESTON ROAD	
Internal Address: SUITE 472	
City: PLANO	
State/Country:	TEXAS
Postal Code:	75201

PROPERTY NUMBERS Total: 250

Patent Number: 6566154 Patent Number: 6756187 Patent Number: 7214473 Patent Number: 6704076 Patent Number: 7006065 Patent Number: 7598939 Patent Number: 8599125 Patent Number: 9111495 Patent Number: 7589383 Patent Number: 7586504 Patent Number: 8471794 Patent Number: 6023314 Patent Number: 6055324 Patent Number: 6310668 Patent Number: 6310668	Property Type	Number
Patent Number: 7214473 Patent Number: 6704076 Patent Number: 7006065 Patent Number: 7671829 Patent Number: 7598939 Patent Number: 9111495 Patent Number: 7589383 Patent Number: 7005916 Patent Number: 7586504 Patent Number: 6023314 Patent Number: 6055324 Patent Number: 6005653 Patent Number: 6310668	Patent Number:	6566154
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Patent Number: 6023314 Patent Number: 6055324 Patent Number: 6005653 Patent Number: 6310668	Patent Number:	7586504
Patent Number: 6055324 Patent Number: 6005653 Patent Number: 6310668	Patent Number:	8471794
Patent Number: 6005653 Patent Number: 6310668	Patent Number:	6023314
Patent Number: 6310668	Patent Number:	6055324
	Patent Number:	6005653
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PATENT REEL: 045469 FRAME: 0023

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Patent Number:	7145628
Patent Number:	7123314

Property Type	Number
Patent Number:	6791656
Patent Number:	6573972
Patent Number:	6784456

CORRESPONDENCE DATA

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ATTORNEY DOCKET NUMBER:	5109-VPV
NAME OF SUBMITTER:	TAREK N. FAHMI
SIGNATURE:	/Tarek N. Fahmi/
DATE SIGNED:	02/28/2018

Total Attachments: 77

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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Getner Foundation LLC, a Delaware limited liability company with an address at 251 Little Falls Drive, Wilmington, Delaware 19808("Assignor"), does hereby sell, assign, transfer, and convey unto Vista Peak Ventures, LLC, a Texas limited liability company with an address at 1400 Preston Road, Suite 472, Plano, TX 75201 ("Assignee"), all of Assignor's right, title, and interest in and to the following (collectively, the "Assigned Patent Rights"):

(a) the patents and patent applications listed in the table below (the "*Patents*");

•		<u>Issue Date/</u>	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
TWI304501	TW	12/21/2008	Method for manufacturing
(TW091111212)		(5/27/2002)	liquid crystal display device
			Yamamoto, Yuji
6566154	US	5/20/2003	Method for manufacturing
(10/160071)		(6/4/2002)	liquid crystal display device
			Yamamoto, Yuji
JP3522249	JР	2/20/2004	Method for removing organic
(JP2001-326584)		(10/24/2001)	film
			Kido, Shusaku
6756187	US	6/29/2004	Method for removing
(10/035222)		(1/4/2002)	patterned layer from lower
(10,030222)		(1, 1, 2002)	layer through reflow
			with all 1
7014470	TIC	5/0/2007	Kido, Shusaku
7214473	US	5/8/2007	Method for removing
(10/773272)		(2/9/2004)	patterned layer from lower
			layer through reflow
			Kido, Shusaku
TWI287178	TW	9/21/2007	Method for removing
(TW090133502)		(12/31/2001)	patterned layer from lower
			layer through reflow
			Kido, Shusaku
6704076	US	3/9/2004	Method for fabricating a
(10/173518)		(6/17/2002)	liquid crystal display device
			Okamoto, Mamoru;
			Sakamoto, Michiaki;
			Kikkawa, Hironori
			Tarkawa, Illionon

		Issue Date/	•
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
CNZL02141283.9	CN	6/21/2006	Method for making a LCD
(CN02141283.9)		(7/5/2002)	device
			Okamoto, Mamoru;
			Sakamoto, Michiaki;
			Kikkawa, Hironori
JP3412583	JP	3/28/2003	Method and circuit for
(JP11-316873)		(11/8/1999)	driving color liquid crystal
			display
			Sugawara, Noriaki; Koga,
			Kouichi
7006065	US	2/28/2006	Gamma compensation
(09/707816)		(11/7/2000)	method and circuit for color
			liquid crystal display
			Sugawara, Noriaki; Koga,
			Kouichi
7671829	US	3/2/2010	Driving method and driving
(11/243626)		(10/5/2005)	circuit for color liquid crystal
,			display
-			
			Sugawara, Noriaki; Koga,
VD10 0267418	KR	12/24/2002	Kouichi Method and circuit for
KR10-0367418 (KR10-2000-0066269)	KK	12/24/2002	
(KK10-2000-0000209)		(11/8/2000)	driving color liquid crystal display
			Koga, Kouichi; Sugawara,
			Noriaki
TWI156469	TW	5/21/2002	Driving method and driving
(TW089123670)	1	(11/8/2000)	circuit for color liquid crystal
			Sugawara, Noriaki; Koga,
			Kouichi
JP4127305	JP	5/23/2008	Liquid crystal display device,
(JP2006-259148)		(9/25/2006)	backlight, and driving method
			and manufacturing method
			thereof
			Sumiyoshi, Ken; Yoshioka,
-			Toshihiro; Hayama, Hiroshi;
			Matsushima, Hiroshi

- 2 -

		<u> Issue Date/</u>	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
JP4870652	JP	11/25/2011	Liquid crystal display device,
(JP2007-312459)		(3/12/2007)	backlight and its driving
			method and manufacturing
			method
			Combined War Washinton
			Sumiyoshi, Ken; Yoshioka, Toshihiro; Hayama, Hiroshi;
			Matsushima, Hiroshi
7598939	US	10/6/2009	Liquid crystal display device,
(11/272663)	05	(11/14/2005)	backlight used for same
(11/2/2003)		(11/14/2003)	display device, method for
			driving same backlight and
			method for manufacturing
			same backlight
			Sumiyoshi, Ken; Yoshioka,
			Toshihiro; Hayama, Hiroshi;
			Matsushima, Jin
8599125	US	12/3/2013	Liquid crystal display device,
(12/381737)		(3/16/2009)	backlight used for same
			display device, method for
			driving same backlight and
			method for manufacturing
			same backlight
			Sumiyoshi, Ken; Yoshioka,
			Toshihiro; Hayama, Hiroshi;
			Matsushima, Jin
9111495	US	8/18/2015	Liquid crystal display device,
(14/066560)		(10/29/2013)	backlight used for same
	ļ		display device, method for
			driving same backlight and
			method for manufacturing
			same backlight
		Í	Sumiyoshi, Ken; Yoshioka,
			Toshihiro; Hayama, Hiroshi;
			Matsushima, Jin
7589383	US	9/15/2009	Thin film semiconductor
(11/441213)		(5/26/2006)	device and method of
			manufacturing the same
			Yanase, Jirou
			ranase, snou

- 3 -

Patent/Application Number	Country	Issue Date/	Tide of Dedend and Language
JP5559244	<u>Country</u> JP	Filing Date 6/13/2014	Title of Patent and Inventors Thin-film semiconductor
(JP2012-119817)	\ \frac{1}{2}	(5/25/2012)	device and manufacturing
			method thereof
			Kanji Yanase
JP4487488	JP	4/9/2010	Circuit of driving display
(JP2003-033071)		(2/12/2003)	device, its control method,
			portable telephone and portable electronic apparatus
			Nakahira, Yoshihiko; Tsuchi, Hiroshi
7005916	US	2/28/2006	Amplifier circuit, driving
(10/358245)		(2/5/2003)	circuit of display apparatus,
			portable telephone and portable electronic apparatus
			portable electronic apparatus
			Nakahira, Yoshihiko; Tsuchi, Hiroshi
7586504	US	9/8/2009	Amplifier circuit, driving
(11/294605)		(12/6/2005)	circuit of display apparatus, portable telephone and
		·	portable electronic apparatus
			portuoie electronic apparatus
			Nakahira, Yoshihiko; Tsuchi, Hiroshi
8471794	US	6/25/2013	Driving circuit for display
(12/504981)		(7/17/2009)	apparatus, and method for controlling same
			Nakahira, Yoshihiko; Tsuchi,
			Hiroshi
CNZL03103193.5	CN	4/26/2006	Amplifier of display device,
(CN03103193.5)		(2/8/2003)	drive circuit, mobile phone
			and portable electronic
			apparatus
			Nakadaira, Yoshihiko;
C00004			Tsuchi, Hiroo
6023314	US	2/8/2000	Elliptically polarizing plate
(09/088731)		(6/2/1998)	and liquid crystal display
			device using same
			Sumiyoshi, Ken

- 4 -

The deceal of the second of	G .	Issue Date/	
Patent/Application Number 6055324	Country US	Filing Date 4/25/2000	Title of Patent and Inventors
(09/103502)	US	(6/24/1998)	Fingerprint image entry device of electrostatic
(09/103302)		(0/24/1998)	capacitance sensing type
			capacitance sensing type
			Fujieda, Ichiro
6005653	US	12/21/1999	Method and apparatus for
(09/169134)		(10/9/1998)	sealing liquid crystal display
			element cell employing seal
			members with Z-shaped cross
			sections that form pressure
			regions
			Matsuzawa, Tadashi
JP3036513	JP	2/25/2000	Liquid crystal display device
(JP10-162691)		(6/10/1998)	
			Ukita, Toru
6310668	US	10/30/2001	LCD wherein opening in
(09/329559)		(6/10/1999)	source electrode overlaps gate
			electrode to compensate
			variations in parasitic
	<u> </u>		capacitance
			Ukita, Tooru
6022110	US	2/8/2000	Projection color liquid crystal
(09/115669)		(7/14/1998)	display apparatus
40.04.07.0			Tsujikawa, Susumu
6031513	US	2/29/2000	Liquid crystal display
(09/019162)		(2/6/1998)	71 1 37
6073369	TYC	6/12/2000	Ikeda, Naoyasu
(09/049150)	US .	6/13/2000	Substrate drying apparatus
(09/049130)		(3/27/1998)	and method
			Yasuyuki, Satou
JP3125766	JP	11/2/2000	Liquid crystal display device
(JP10-271140)		(9/25/1998)	and its manufacture
6717620	TIG	1/6/0000	Ikeda, Munehiro
6717630	US	4/6/2004	Liquid crystal display device
(09/404705)		(9/23/1999)	and method of fabricating the
			same
			Ikeda, Munehiro
		_	

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Detect and Investors
6750895	US	6/15/2004	Title of Patent and Inventors Optical printer head with
(09/670596)		(9/29/2000)	integrated drive circuitry
			Otose, Tomohiko; Asada, Hideki; Uezono, Tsutomu; Oda, Atsushi; Toguchi, Satoru
JP3801032 (JP2001-365327)	JP	5/12/2006 (11/29/2001)	Light source and liquid crystal display using the light source
			Saito, Goro; Yagi, Toshie; Mimura, Hiroji; Sumiyoshi, Ken
6824285 (10/277930)	US	11/30/2004 (10/23/2002)	Light source and liquid crystal display device using this light source
			Saitoh, Goroh; Yagi, Yoshie; Mimura, Koji; Sumiyoshi, Ken
6930665 (10/353951)	US	8/16/2005 (1/30/2003)	Display device for D/A conversion using load capacitances of two signal lines
			Sekine, Hiroyuki
JP3937304 (JP2002-003920)	JP	4/6/2007 (1/10/2002)	Liquid crystal display panel and its manufacturing method
(50.10.10	1		Noiri, Yoshikazu
6734943 (10/338829)	US	5/11/2004 (1/9/2003)	Liquid crystal display panel with sealing member on second partition wall and manufacturing method thereof
			Noiri, Yoshikazu

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
7027121	US	4/11/2006	Method for manufacturing
(10/620125)		(7/15/2003)	liquid crystal display device
(10/020123)		(111312003)	having the first substrate and
			support substrate pressed
			against each other under
			vacuum condition without use
			of an adhesive
			of an adnesive
			Horiuchi, Toshihiro; Noiri,
			Yoshikazu; Yoshizoe,
			Hidefumi; Ueebisu, Shinji;
			Mukai, Toshiaki
7218368	US	5/15/2007	Rubbing roll orientation
(10/990892)		(11/17/2004)	processor for liquid crystal
			display manufacturing
			Mori, Makoto
6011860	US	1/4/2000	Small reliable image input
(09/020908)		(2/9/1998)	apparatus incorporated in
			finger-print collation system
			of personal identification
			Fujieda, Ichiro; Suga,
			Michihisa
6573970	US	6/3/2003	Liquid crystal display device
(09/617040)		(7/14/2000)	Esquid expend display device
<u> </u>		(1.1.1.2000)	Saitoh, Goroh; Takatori, Ken-
			ichi
JP4442061	JP	1/22/2010	Diffractive optical element
(JP2001-229082)		(7/30/2001)	and optical device using the
			same
			Fujieda, Ichiro
7146076	US	12/5/2006	Diffractive optical element
(10/201133)		(7/24/2002)	and optical device using the
,		(= 2002)	same
			Fujieda, Ichiro
JP5318308	JР	7/19/2013	Semiconductor substrate
(JP2001-247238)		(8/16/2001)	production system
()		(0/10/2001)	production system
			Tanabe, Hiroshi

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		<u>Issue Date/</u>		
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors	
7354810	US	4/8/2008	Method of and apparatus for	
(10/856275)		(5/28/2004)	manufacturing semiconductor	
			thin film, and method of	
			manufacturing thin film	
			transistor	
			Takaoka, Hiromichi; Nakata,	
			Mitsuru	
JP4221270	JP	11/21/2008	Liquid crystal display device	
(JP2003-364836)		(10/24/2003)	and method for	
		(10/2 112000)	manufacturing the same	
·			The state of the s	
			Ueyama, Hiroshi	
7193678	US	3/20/2007	Liquid crystal display device	
(10/948758)		(9/24/2004)	and method for	
			manufacturing the same	
			Ueyama, Hiroshi	
7468765	US	12/23/2008	Electro-optical display device	
(11/001240)		(12/2/2004)	and image projection unit	
			Shiota, Kunihiro; Tanabe,	
			Hiroshi	
JP4771042	JP	7/1/2011	Piezoelectric element	
(JP2004-216325)		(7/23/2004)	mounted device, liquid-drop	
			discharging device using	
			same, and image output	
			device	
			Otose, Tomohiko	
7355323	US	4/8/2008	Piezoelectric device, droplet	
(11/187964)		(7/25/2005)	jetting device using the same,	
		(and image output device	
			Otose, Tomohiko	

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Th 4 4/4 P 42 - NT 1 -	C 4	Issue Date/	TOTAL CODE A SECOND
Patent/Application Number 7626648	Country US	Filing Date 12/1/2009	Title of Patent and Inventors Pixel circuit substrate, LCD
(11/242935)	US	(10/5/2005)	apparatus and projection
(11/242933)		(10/3/2003)	
			display apparatus having
			interlayer insulating film
			comprising a laminate of
			inorganic insulating material
			having a uniformly flat
			surface over TFT
			Shiota, Kunihiro; Tanabe,
			Hiroshi
7388625	US	6/17/2008	Thin-film transistor array
(11/313955)		(12/22/2005)	substrate and liquid crystal
			display device
			Matsuzaki, Tadahiro; Sera,
			Kenji
6781643	US	8/24/2004	Active matrix liquid crystal
(09/573185)		(5/18/2000)	display device
(, , , , , , , , , , , , , , , , , , ,		(5/15/2000)	and the state of t
			Watanabe, Makoto;
			Watanabe, Takahiko
6445431	US	9/3/2002	Liquid crystal display with
(09/732920)		(12/11/2000)	polarization layer interior to
		,	substrates
			Nose, Takashi; Kaneko,
			Setsuo; Suzuki, Shigeyoshi
6501520	US	12/31/2002	Liquid crystal display with
(10/112947)		(4/2/2002)	polarization layer interior to
			substrates
			Nose, Takashi; Kaneko,
			Setsuo; Suzuki, Shigeyoshi
6822263	US	11/23/2004	Thin film transistor formed
(10/457928)		(6/9/2003)	on a transparent substrate
			Sato, Yoshinobu; Yuda,
			Katsuhisa; Tanabe, Hiroshi
7119363	US	10/10/2006	Thin film transistor formed
(10/911833)		(8/5/2004)	on a transparent substrate
,		(======================================	
			Sato, Yoshinobu; Yuda,
			Katsuhisa; Tanabe, Hiroshi

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Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
7585708	US	9/8/2009	Method for manufacturing a
(11/512596)		(8/30/2006)	thin-film transistor
			Sato, Yoshinobu; Yuda,
C0005.41	TIC	10/5/0004	Katsuhisa; Tanabe, Hiroshi
6800541	US	10/5/2004	Pulse laser irradiation method
(10/263771)		(10/4/2002)	for forming a semiconductor thin film
			tiitii iiiiti
	,		Okumura, Hiroshi
JP3391343	JP	1/24/2003	Active matrix substrate and
(JP2000-308262)		(10/6/2000)	manufacturing method
			therefore
			Tanaka, Hiroaki; Yamaguchi,
			Hirotaka; Kaneko, Wakahiko;
			Sakamoto, Michiaki; Itoida,
			Satoshi; Hayase, Takasuke;
			Yoshikawa, Tae; Kano,
			Hiroshi
TWI151519	TW	3/1/2002	Active matrix substrate and
(TW089121918)		(10/19/2000)	the manufacturing method
			therefore
			Tanaka, Hiroaki; Yamaguchi,
			Hirotaka; Kaneko, Wakahiko;
			Sakamoto, Michiaki; Itoida,
			Satoshi; Hayase, Takasuke;
			Yoshikawa, Tae; Kano,
			Hiroshi
KR10-0393293	KR	7/21/2003	Active matrix substrate and
(KR10-2000-0062610)		(10/24/2000)	manufacturing method
			therefore
			Hayase, Takasuke; Ihida,
			Satoshi; Kaneko, Wakahiko;
			Kano, Hiroshi; Sakamoto,
			Michiaki; Tanaka, Hiroaki;
			Yamaguchi, Hirotaka;
			Yoshikawa, Tae

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
6674093 (09/695321)	US	1/6/2004 (10/25/2000)	Active matrix substrate and manufacturing method therefore
			Tanaka, Hiroaki; Yamaguchi, Hirotaka; Kaneko, Wakahiko; Sakamoto, Michiaki; Ihida, Satoshi; Hayase, Takasuke; Yoshikawa, Tae; Kanou, Hiroshi
6891196 (10/617035)	US	5/10/2005 (7/11/2003)	Active matrix substrate and manufacturing method therefore
			Tanaka, Hiroaki; Yamaguchi, Hirotaka; Kaneko, Wakahiko; Sakamoto, Michiaki; Ihida, Satoshi; Hayase, Takasuke; Yoshikawa, Tae; Kanou, Hiroshi
7223622 (10/833153)	US	5/29/2007 (4/28/2004)	Active-matrix substrate and method of fabricating same
			Hirata, Kazumi
JP3645755 (JP11-263635)	JP	2/10/2005 (9/17/1999)	Thin-film transistor and its manufacturing method
			Tanabe, Hiroshi
6512246 (09/656631)	US	1/28/2003 (9/7/2000)	Thin film transistor
6667188 (10/272071)	US	12/23/2003 (10/16/2002)	Tanabe, Hiroshi Thin film transistor and method for fabricating same
650555			Tanabe, Hiroshi
6797535 (10/693395)	US	9/28/2004 (10/24/2003)	Thin film transistor and method for fabricating same
,			Tanabe, Hiroshi
KR10-0396227 (KR10-2000-0053772)	KR	8/19/2003 (9/9/2000)	Thin-film transistor and its manufacturing method
			Tanabe, Hiroshi

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		<u>Issue Date/</u>		
Patent/Application Number	<u>Country</u>	Filing Date	Title of Patent and Inventors	
TWI248546	TW	2/1/2006	Thin film transistor and	
(TW089119085)		(9/15/2000)	method for fabricating same	
			Tanabe, Hiroshi	
JP4525451	JP	6/11/2010	Semiconductor device and	
(JP2005-129251)		(4/27/2005)	image sensor device using same	
			Haga, Hiroshi; Okumura, Fujio	
6583456	US	6/24/2003	Image sensor with light	
(09/956669)		(9/20/2001)	receiving elements of	
			differing areas and image	
			reader both having	
			semiconductor device	
			Haga, Hiroshi; Fujieda,	
			Ichiro; Okumura, Fujio	
JP3267271	JP	1/11/2002	Liquid crystal display device	
(JP11-197741)		(7/12/1999)	and its production	
			Sakamoto, Michiaki;	
			Okamoto, Mamoru; Nakada,	
	i		Shinichi; Yamamoto, Yuji;	
			Watanabe, Takahiko;	
			Yoshikawa, Shuken;	
			Maruyama, Muneo	
TWI184524	TW	8/21/2003	Liquid crystal display device	
(TW088121147)		(12/3/1999)	and the manufacturing	
			method thereof	
			Nakata, Shinichi; Yamamoto,	
			Yuji; Okamoto, Mamoru;	
•			Sakamoto, Michiaki;	
			Watanabe, Takahiko; Ihara,	
			Hirofumi; Kikkawa, Hironori;	
			Maruyama, Muneo	

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D-44/4	G	Issue Date/	TOTAL OTD A A NY
Patent/Application Number KR10-0376355	Country KR	Filing Date 3/5/2003	Title of Patent and Inventors
(KR10-1999-0056047)	KK	(12/9/1999)	LIQUID CRYSTAL DISPLAY DEVICE AND
(KK10-1999-0030047)		(12/9/1999)	MANUFACTURING
			ř
			METHOD THEREOF
			Nakata, Sinitzi; Yamamoto,
			Yuzi; Okamoto, Mamoru;
			Sakamoto, Mitziaki;
			Watanabe, Takahiko; Ihara,
			Hirohumi; Kikawa, Hironori;
			Maruyama, Muneo
6429916	US	8/6/2002	Liquid crystal display with
(09/459010)		(12/10/1999)	filter and light shield
		(12,10,1333)	separated from contact hole
			arparation contact note
			Nakata, Shinichi; Yamamoto,
			Yuji; Okamoto, Mamoru;
			Sakamoto, Michiaki;
			Watanabe, Takahiko; Ihara,
			Hirofumi; Kikkawa, Hironori;
			Maruyama, Muneo
6398610	US	6/4/2002	Display panel production line
(09/470398)		(12/22/1999)	
			Inoue, Yoshiyasu
JP3730958	JP	9/5/2005	Method of patterning
(JP2002-375607)		(12/25/2002)	multilayer film and multilayer
			wiring electrode
			Kato, Takeshi; Kido,
			Shusaku; Maeda, Akiyoshi
7012029	US	3/14/2006	Method of forming a
(10/745636)		(12/29/2003)	lamination film pattern and
,		(44.23,2333)	improved lamination film
			pattern
			F
			Katoh, Tsuyoshi; Kido,
			Syuusaku; Maeda, Akitoshi
7554207	US	6/30/2009	Method of forming a
(11/295462)		(12/7/2005)	lamination film pattern and
•			improved lamination film
			pattern
		,	_
			Katoh, Tsuyoshi; Kido,
			Syuusaku; Maeda, Akitoshi

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
6734460	US	5/11/2004	Active matrix substrate and
(10/114093)		(4/2/2002)	method of fabricating the
(-3, -1, 10, -1)	i	()	same
			Okumura, Hiroshi;
			Sukegawa, Osamu
KR10-0502685	KR	7/12/2005	Active matrix substrate and
(KR10-2002-0018192)		(4/3/2002)	manufacturing method therefore
			Okumura, Hiroshi;
			Sukegawa, Osamu
TWI181982	TW	7/11/2003	Active matrix substrate and
(TW091106803)		(4/3/2002)	method of fabricating the same
			Okumura, Hiroshi;
			Sukegawa, Osamu
CNZL02127312.X	CN	10/5/2005	Active matrix substrate and
(CN02127312.X)		(7/31/2002)	manufacture thereof
			Okumura, Nobu; Tasuku,
IB4000140	TD.	0/10/0010	Kawanobu
JP4920140	JP	2/10/2012	Liquid crystal display device
(JP2001-150092)		(5/18/2001)	and its manufacturing method
		-	Maeda, Akiyoshi; Tanaka,
			Hiroaki; Kimura, Shigeru;
			Kimura, Satoshi
KR10-0531094	KR	11/18/2005	Liquid crystal display device
(KR10-2002-0027406)		(5/17/2002)	and its manufacturing method
			Kimura, Satoshi; Kimura,
			Shigeru; Maeda, Akitoshi;
			Tanaka, Hiroaki
TWI189785	TW	11/1/2003	Active matrix substrate for
(TW091110359)		(5/17/2002)	liquid crystal display and its
		·	fabrication
			Maeda, Akitoshi; Tanaka,
			Hiroaki; Kimura, Shigeru;
			Kimura, Satoshi

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Patent/Application Number	Country	<u>Issue Date/</u> <u>Filing Date</u>	Title of Patent and Inventors
6894311	US	5/17/2005	Active matrix substrate for
(10/151544)		(5/20/2002)	liquid crystal display utilizing
			interconnection lines formed
			from multilayered films that
	!		include an aluminum-
			neodymium alloy layer
			Maeda, Akitoshi; Tanaka,
			Hiroaki; Kimura, Shigeru;
			Kimura, Satoshi
CNZL02140183.7	CN	10/25/2006	tive matrix substrate for LCD
(CN02140183.7)		(7/3/2002)	and mfg. method thereof
			Maeda, Akitoshi; Tanaka,
			Hiroaki; Kimura, Shigeru;
			Kimura, Satoshi
7037766	US	5/2/2006	Method of fabricating an
(10/793086)		(3/4/2004)	active matrix substrate for
			liquid crystal display utilizing
			interconnection lines formed
			from multilayered films that
			include an aluminum-
			neodymium alloy layer
			Maeda, Akitoshi; Tanaka,
			Hiroaki; Kimura, Shigeru;
			Kimura, Satoshi
JP5411236	JP	11/15/2013	Liquid crystal display device
(JP2011-249576)		(11/15/2011)	and its manufacturing method
			Maeda, Akiyoshi; Tanaka,
			Hiroaki; Kimura, Shigeru;
			Kimura, Satoshi

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
JP5408829	JP	11/15/2013	Active matrix substrate and
(JP2000-252076)		(8/23/2000)	method of manufacturing the same
			Kimura, Shigeru; Watanabe, Takahiko; Yoshikawa, Tae; Uchida, Hiroyuki; Kido, Shusaku; Nakada, Shinichi; Hamada, Tsutomu; Shimodousono, Hisashi; Doi, Satoshi; Harano, Toshihiko; Maeda, Akiyoshi; Itoida, Satoshi; Tanaka, Hiroaki; Hayase, Takasuke; Kuroba, Shoichi; Ihara, Hiroshi;
6632696 (09/745657)	US	10/14/2003 (12/20/2000)	Takechi, Kazue Manufacturing method of active matrix substrate plate and manufacturing method therefor Kimura, Shigeru; Watanabe, Takahiko; Yoshikawa, Tae; Uchida, Hiroyuki; Kido, Shusaku; Nakata, Shinichi; Hamada, Tsutomu; Shimodouzono, Hisanobu; Doi, Satoshi; Harano, Toshihiko; Maeda, Akitoshi; Ihida, Satoshi; Tanaka, Hiroaki; Hayase, Takasuke; Kuroha, Shouichi; Ihara, Hirofumi; Takechi,

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Potent and Inventors
KR10-0463410	KR	12/15/2004	Title of Patent and Inventors Active matrix substrate plate
(KR10-2000-0083299)		(12/27/2000)	and manufacturing method
			therefor
			Doi, Satoshi; Hamada,
			Tsutomu; Harano, Toshihiko;
			Hayase, Takasuke; Ihara,
			Hirofumi; Ihida, Satoshi;
			Kido, Shusaku; Kimura, Shigeru; Kuroha, Shoichi;
			Maeda, Akitoshi; Nakata,
			Shinichi; Shimodouzono,
			Hisanobu; Takechi,
			Kazushige; Tanaka, Hiroaki;
,			Uchida, Hiroyuki; Watanabe,
TDV 111 6 5 5 5 5		40/44/2002	Takahiko; Yoshikawa, Tae
TWI165555 (TW089127961)	TW	10/11/2002	Active matrix substrate plate
(1 w08912/901)		(12/27/2000)	and manufacturing method therefor
\$			therefor
			Kimura, Shigeru; Watanabe,
			Takahiko; Yoshikawa, Tae;
			Uchida, Hiroyuki; Kido,
			Shusaku; Nakata, Shinichi;
			Hamada, Tsutomu;
			Shimodouzono, Hisanobu; Doi, Satoshi; Harano,
			Toshihiko; Maeda, Akitoshi;
			Ihida, Satoshi; Tanaka,
			Hiroaki; Hayase, Takasuke;
			Kuroha, Shouichi; Ihara,
			Hirofumi; Takechi,
IB2652007	TD	2/4/2005	Kazushige
JP3652997 (JP2001-085793)	JP	3/4/2005	Liquid crystal material and
(01 2001-003/93)		(3/23/2001)	liquid crystal display device
6640006			Matsumoto, Koichi
6649226	US	11/18/2003	Liquid crystal material and
(09/877180)		(6/11/2001)	liquid crystal display
			Matsumoto, Kimikazu

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D-44/4P4PN1	G 4	Issue Date/	
Patent/Application Number KR10-0437141	Country	Filing Date	Title of Patent and Inventors
	KR	6/14/2004	Liquid crystal material and
(KR10-2001-0033060)		(6/13/2001)	liquid crystal display
			Matsumoto, Kimikazu
TWI206385	TW	6/21/2004	Liquid crystal material and
(TW090114361)		(6/13/2001)	liquid crystal display
			Matsumoto, Kimikazu
6781647	US	8/24/2004	Liquid crystal display device
(09/842580)		(4/26/2001)	having a front light unit
(427.4.2247)		("20,2001)	naving a now light and
YDOSTOOTA		7.0.000	Fujieda, Ichiro
JP3570974	JP	7/2/2004	Active matrix type liquid
(JP2000-216202)		(7/17/2000)	crystal display device
			Matsumoto, Koichi; Nishida,
			Shinichi
KR10-0396823	KR	8/21/2003	Active matrix type liquid
(KR10-2001-0042383)		(7/13/2001)	crystal display device
			Materia to Kindle
			Matsumoto, Kimikazu; Nishida, Shinichi
TWI238915	TW	9/1/2005	Active matrix liquid crystal
(TW090117502)	1 "	(7/17/2001)	display device
(4 11 02 022)		(11112001)	display device
			Matsumoto, Kimikazu;
			Nishida, Shinichi
7009673	US	3/7/2006	Active matrix liquid crystal
(10/656138)		(9/8/2003)	display having a thin film
			transistor over which
			alignment of liquid crystal
			molecules does not change
			Matsumoto, Kimikazu;
			Nishida, Shinichi
7492431	US	2/17/2009	Active matrix liquid crystal
(11/291997)		(12/2/2005)	display having a thin film
			transistor over which
			alignment of liquid crystal
			molecules does not change
			Matsumoto, Kimikazu;
			Nishida, Shinichi
		i	i moma, omilicili

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	~ .	Issue Date/	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
6881894	US	4/19/2005	EMI shielding structures
(09/975791)		(10/12/2001)	
			Kai, Takanobu
7271041	US	9/18/2007	Method for manufacturing
(11/102765)		(4/11/2005)	thin film transistor
			Takahashi, Mitsuasa
7012849	US	3/14/2006	Semiconductor, image output
(10/619594)		(7/16/2003)	device, and driving method of
			a functional device
			Otose, Tomohiko
7665816	US	2/23/2010	Image output device with an
(11/330968)		(1/13/2006)	injector
		(======================================	
			Otose, Tomohiko
CNZL03132804.0	CN	4/9/2008	Semiconductor, image output
(CN03132804.0)		(7/21/2003)	device and driving method
			for function device
			Otose, Tomohiko
TWI291225	TW	12/11/2007	Thin film semiconductor
(TW092104387)		(3/5/2003)	device and method for
,			manufacturing same
			Takahashi, Mitsuasa
7109554	US	9/19/2006	Thin film semiconductor
(10/961094)	OB	(10/12/2004)	device and method for
(10/201024)		(10/12/2004)	manufacturing same
			manufacturing same
			Takahashi, Mitsuasa
JP4423659	JP	12/18/2009	Thin film transistor, TFT
(JP2003-024473)		(1/31/2003)	substrate, and liquid crystal
			display
			Matanana Nashi Gara
			Matsunaga, Naoki; Sera,
7105905	US	9/12/2006	Kenji
(10/724040)	US		Thin film transistor, TFT
(10/724040)		(12/1/2003)	substrate and liquid crystal
			display unit
			Matsunaga, Naoki; Sera,
			Kenji
			12011)1

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	a .	Issue Date/	
Patent/Application Number CNZL200310124837.9	Country CN	Filing Date 12/6/2006	Title of Patent and Inventors
(CN200310124837.9)	CN	(12/31/2003)	Film transistor, TFT substrate and LCD
(CN200310124837.9)		(12/31/2003)	and LCD
			Matsunaga, Naoki; Sera,
			Kenji
JP4945107	JP		Light source device and its
(JP2005-268809)		(9/15/2005)	manufacturing method,
			indicator device and its
			manufacturing method, and
			drive method for indicator
			device
	•		
			Takatori, Kenichi;
			Sumiyoshi, Ken
7369725	US	5/6/2008	Light source device and
(11/521009)	05	(9/14/2006)	method for manufacturing the
(11.021003)		(5/11/2000)	same, display device and
			method for manufacturing the
			same, and method for driving
			display device
			Takatori, Kenichi;
			Sumiyoshi, Ken
7656472	US	2/2/2010	Light source device and
(12/050324)		(3/18/2008)	method for manufacturing the
			same, display device and
			method for manufacturing the
:			same, and method for driving
			display device
			Takatori, Kenichi;
			Sumiyoshi, Ken
7212722	US	5/1/2007	Backlight unit and liquid-
(11/299951)		(12/13/2005)	crystal display device using
, ·		(the same
			Shinohara, Toshiya
CNZL200610051348.9	CN	6/25/2008	Backlight unit and liquid-
(CN200610051348.9)		(1/5/2006)	crystal display device using
			the same
			Shinohara, Toshiya

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The Access As a second of the	6 4	Issue Date/	TOTAL CD / A TY
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
7122444	US	10/17/2006	Manufacturing method of thin
(10/974932)		(10/28/2004)	film device substrate
			Nakata, Mitsura; Takechi,
			Kazushige; Kanoh, Hiroshi
7579222	US	8/25/2009	Manufacturing method of thin
(11/503286)		(8/14/2006)	film device substrate
			Nakata, Mitsura; Takechi,
			Kazushige; Kanoh, Hiroshi
7373834	US	5/20/2008	Pressure sensor
(11/586512)		(10/26/2006)	11000010 0011001
(11/000012)		(10,20,2000)	Takahashi, Mitsuasa
JP3127894	JP	11/10/2000	Active matrix type liquid
(JP10-209148)		(7/24/1998)	crystal display device
			Watanabe, Makoto;
			Watanabe, Takahiko
TWI188084	TW	10/11/2003	Active matrix-type liquid
(TW088110844)	1 **	(6/28/1999)	crystal display device
(1,1000110011)		(0/20/1999)	crystal display device
			Watanabe, Makoto;
			Watanabe, Takahiko
CNZL99109819.6	CN	3/12/2003	Active matrix-type liquid
(CN99109819.6)		(7/15/1999)	crystal display device
			Watanabe, Sei; Watanabe,
			Kihiko
6404474	US	6/11/2002	Horizontal electric field LCD
(09/357060)		(7/19/1999)	with increased capacitance
			between pixel and common
			electrodes
		•	Watanabe, Makoto;
			Watanabe, Takahiko
KR10-0313726	KR	10/24/2001	Active matrix-type liquid
(KR10-1999-0030005)		(7/23/1999)	crystal display device
•			J
			Watanabe, Makoto;
			Watanabe, Takahiko

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		<u>Issue Date/</u>			
<u> F</u>	Patent/Application Number	Country	Filing Date	Title of Patent and Inventors	
` J	P3538046	JP	3/26/2004	Liquid crystal module support	
(JP10-353579)		(12/11/1998)	structure and portable	
				terminal device mounting the	
				same	
				Yano, Kiichi; Miwa, Tomoo;	
				Sato, Yasuyoshi; Ogawa,	
				Toshinao; Nishiyama,	
				Tomoaki; Baba, Masatake;	
				Okabe, Akira; Fujishiro,	
				Fumihiko; Kato, Takehiro;	
ĺ				Mikami, Kazuaki; Fukuyoshi,	
				Hirokazu	
- 1	WI151064	TW	2/21/2002	Liquid crystal module	
(TW088121453)		(12/8/1999)	installation structure and	
				portable terminal device	
Ì				having the same	
				Yano, Takakazu; Miwa,	
				Tomoo; Sato, Yasuhiro;	
				Ogwa, Toshihisa; Nishiyama,	
				Michiaki; Baba, Masatake;	
				Okabe, Hikaru; Fumihiko,	
				Fujishiro; Kato, Yudai;	
				Mikami, Kazuaki; Fukuyoshi,	
				Hirokazu	
	IR10-0419043	KR	2/4/2004	Structure for supporting	
(F	KR10-1999-0056493)		(12/10/1999)	liquid module	
				Yano, Takakazu; Miwa,	
				Tomoo; Sato, Yasuhiro;	
				Ogawa, Toshihisa;	
				Nishiyama, Michiaki; Baba,	
				Masatake; Okabe, Hikaru;	
				Fujishiro, Fumihiko, Kato,	
				Katsuhiro; Mikami, Kazuaki;	
				Fukuyoshi, Hirokazu	

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
CNZL99126036.8	CN	1/7/2004	Liquid crystal module
(CN99126036.8)		(12/13/1999)	installation structure and
			portable terminal device
			having same
			Yano, Takakazu; Miwa,
			Tomoo; Sato, Yasuhiro;
			Ogawa, Toshihisa;
			Nishiyama, Michiaki; Baba,
			Masatake; Okabe, Hikaru;
			Fujishiro, Fumihiko; Kato,
			Katsuhiro; Mikami, Kazuaki;
6594143	US	7/15/2002	Fukuyoshi, Hirokazu
(09/459280)	US	7/15/2003 (12/13/1999)	Liquid crystal module mounting structure and
(09/439280)		(12/13/1999)	mobile terminal mounted
			with the same
	-		with the same
			Yano, Takakazu; Miwa,
			Tomoo; Sato, Yasuhiro;
			Ogawa, Toshihisa;
			Nishiyama, Michiaki; Baba,
			Masatake; Okabe, Hikaru;
			Fujishiro, Fumihiko; Kato,
			Katsuhiro; Mikami, Kazuaki;
JP4375517	JP	9/18/2009	Fukuyoshi, Hirokazu Liquid crystal display device
(JP2001-220829)		(7/23/2001)	Enquid crystar display device
(0.2001.220025)		(112312001)	Sato, Tetsushi; Sekine,
			Hiroyuki; Yoshinaga,
			Kazuhide
6853407	US	2/8/2005	Liquid crystal display device
(10/196356)		(7/17/2002)	including a lightly doped
			drain region
			Sato, Tetsushi; Sekine,
			Hiroyuki; Yoshinaga,
			Kazuhide
DE60222508.6	DE	9/19/2007	Liquid crystal display device
(DE60222508.6)		(7/18/2002)	
			Sato, Tetsushi; Sekine,
			Hiroyuki; Yoshinaga,
			Kazuhide

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Datant/Application Number	Country	<u>Issue Date/</u> Filing Date	T:41CD-44 1 T 4
Patent/Application Number DE60248633.5	DE	1/18/2017	Title of Patent and Inventors Liquid crystal display device
(DE60248633.5)		(7/18/2002)	Elquid crystal display device
(DE00270033.5)	,	(1/10/2002)	Sato, Tetsushi; Sekine,
			Hiroyuki; Yoshinaga,
	i		Kazuhide
FR1279997	FR	9/19/2007	Liquid crystal display device
(FR02016030.5)		(7/18/2002)	Enquire on state display device
,			Sato, Tetsushi; Sekine,
			Hiroyuki; Yoshinaga,
			Kazuhide
FR1744206	FR	1/18/2017	Liquid crystal display device
(FR06022058.9)		(7/18/2002)	
			Sato, Tetsushi; Sekine,
			Hiroyuki; Yoshinaga,
			Kazuhide
GB1279997	GB	9/19/2007	Liquid crystal display device
(GB02016030,5)		(7/18/2002)	
			Sato, Tetsushi; Sekine,
			Hiroyuki; Yoshinaga,
CD1744006	GD	1 /1 0 /2 0 1 5	Kazuhide
GB1744206	GB	1/18/2017	Liquid crystal display device
(GB06022058.9)		(7/18/2002)	G-4- T-41: G 1:
			Sato, Tetsushi; Sekine,
			Hiroyuki; Yoshinaga, Kazuhide
CNZL02126554.2	CN	1/19/2005	Liquid crystal display
(CN02126554.2)		(7/23/2002)	Erquid Crystal display
(0110212025 112)		(112312002)	Sato, Tetsushi; Sekine,
			Hiroyuki; Yoshinaga,
			Kazuhide
TWI290247	TW	11/21/2007	Backlight unit uniformly
(TW091115733)		(7/15/2002)	illuminating object regardless
Ť		(of lapse of time and liquid
			crystal display panel device
			using the same
			Ueda, Shoichi; Sakaki,
TD 1010016			Yoshinobu
JP4819016	JP	9/9/2011	Method of manufacturing
(JP2007-258154)		(10/1/2007)	liquid crystal display device
			771.1
·			Kido, Shusaku

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		Issue Date/	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
TWI311658	TW	7/1/2009	Pattern forming method and
(TW091114593)		(7/2/2002)	method for manufacturing
			liquid crystal display device
			using the same
			Kido, Shusaku
6617263	US	9/9/2003	Pattern forming method for
(10/189571)		(7/8/2002)	flattening an organic film for
			a liquid crystal display device
			1 January Market
			Kido, Shusaku
7408135	US	8/5/2008	Imaging device having dual
(10/252407)		(9/24/2002)	opening with open/close unit
			and electronic equipment
			incorporated therein the same
			Fujieda, Ichiro
JP3858263	JP	9/29/2006	Fingerprint image input
(JP2001-344895)		(11/9/2001)	device and electronic
			equipment using the same
			Fujieda, Ichiro
6885439	US	4/26/2005	Fingerprint input devices and
(10/288925)		(11/6/2002)	electronic devices provided
·			with the same
			Fujieda, Ichiro
CNZL02150502.0	CN	8/17/2005	Fingerprint input device and
(CN02150502.0)		(11/11/2002)	electrionic equipment with
, ,		'`	the fingerprint input device
			Fujie, Ichiro
TWI286643	TW	9/11/2007	Optical unit and LCD device
(TW092104486)		(3/3/2003)	using the optical unit
			Ogawa, Toshihisa
7301588	US	11/27/2007	Optical unit and LCD device
(10/376637)		(3/3/2003)	using the optical unit
1			
7502070	TIG	0/00/000	Ogawa, Toshihisa
7593070	US	9/22/2009	Optical unit and LCD device
(11/984403)		(11/16/2007)	using the optical unit
			Ogawa, Toshihisa
			Ogawa, Toshiilisa

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
CNZL03105113.8 (CN03105113.8)	CN	12/14/2005 (3/4/2003)	Light unit element and liquid crystal display equipment
			using light unit element
		•	Ogawa, Toshinao
KR10-0543817	KR	1/10/2006	Optical unit and LCD device
(KR10-2003-0013286)		(3/4/2003)	using the optical unit
			Ogawa, Toshihisa
TWI206131	TW	6/21/2004	In-plane switching mode
(TW092107034)		(3/27/2003)	liquid crystal display device
			Matsumoto, Kimikazu;
			Itakura, Kunimasa; Nishida, Shinichi
6950165	US	9/27/2005	In-plane switching mode
(10/397256)		(3/27/2003)	liquid crystal display device
-			Matsumoto, Kimikazu;
			Itakura, Kunimasa; Nishida, Shinichi
KR10-0510566	KR	8/19/2005	In-plane switching mode
(KR10-2003-0019773)		(3/29/2003)	liquid crystal display device
			Matsumoto, Kimikazu;
			Itakura, Kunimasa; Nishida, Shinichi
CNZL03108606.3	CN	4/12/2006	In-plane switching mode
(CN03108606.3)		(3/31/2003)	liquid crystal display device
			Nishida, Shinichi; Itakura,
			Kunimasa; Matsumoto,
7476470	US	1/13/2009	Kimikazu Method for manufacturing
(10/987772)	105	(11/12/2004)	thin film semiconductor
(()		(11/15/2007)	device and method for
			forming resist pattern thereof
			Takahashi, Mitsuasa
7151516	US	12/19/2006	Liquid crystal display device
(10/632713)		(8/1/2003)	Tolvodo Ilimad ' II'
			Takeda, Hiroshi; Hirano, Youji

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		<u>Issue Date/</u>		
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors	
6840647	US	1/11/2005	Lighting device, liquid crystal	
(09/991600)		(11/23/2001)	display device including the	
			same, and method of	
			fabricating the same	
			Hayashi, Kazuhiko; Fujieda, Ichiro; Oda, Atsushi; Kaneko, Setsuo	
6816140	US	11/9/2004	Displaying device and	
(10/013263)		(12/7/2001)	displaying method and	
			manufacturing method of the	
			device	
			Fujieda, Ichiro	
JP4190798	JP	9/26/2008	Thin film transistor and	
(JP2002-132310)		(5/8/2002)	manufacturing method	
			thereof	
			Okumura, Nobu	
TWI206712	TW	6/21/2004	Thin-film transistor and	
(TW092112290)		(5/6/2003)	method for manufacturing	
			same	
			Okumura, Hiroshi	
6815269	US	11/9/2004	Thin-film transistor and	
(10/430540)		(5/6/2003)	method for manufacturing the	
			same	
			Okumura, Hiroshi	
KR10-0510934	KR.	8/22/2005	Thin-film transistor and	
(KR10-2003-0028829)		(5/7/2003)	method for manufacturing	
			same	
			Okumura, Hiroshi	
CNZL03130906.2	CN	5/30/2007	Thin film transistor and	
(CN03130906.2)		(5/8/2003)	manufacturing method	
		(thereof	
			Oku, Muranobu	

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	~ ·	Issue Date/	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
7199780	US	4/3/2007	Field sequential driving type
(10/457374)		(6/10/2003)	liquid crystal display
			apparatus capable of
			increasing brightness while
	i		suppressing irregularity, and
			its driving method
			Sekine, Hiroyuki
JP4133088	JP	6/6/2008	Liquid crystal display device
(JP2002-224997)		(8/1/2002)	Enquire crystal display de vice
(2 2 3 2 2 1 2 1 2 1 2 1 2 1 2 1 2 1 2 1		(0/1/2002)	Ishii, Toshiya; Sakamoto,
			Michiaki; Hayakawa, Kiyomi
KR10-0575034	KR	4/24/2006	Liquid crystal display device
(KR10-2003-0053185)		(7/31/2003)	Elquid orybur display device
,			Ishii, Toshiya; Sakamoto,
			Michiaki; Hayakawa, Kiyomi
CNZL03152510.5	CN	7/11/2007	LCD device
(CN03152510.5)		(8/1/2003)	
· ·			Donburi, Toshiya; Sakamoto,
			Michiaki; Sasagawa, Kiyomi
TWI225564	TW	12/21/2004	Liquid crystal display device
(TW092121105)		(8/1/2003)	
			Ishii, Toshiya; Sakamoto,
			Michiaki; Hayakawa, Kiyomi
6967702	US	11/22/2005	Liquid crystal display device
(10/633219)		(8/1/2003)	
			Ishii, Toshiya; Sakamoto,
			Michiaki; Hayakawa, Kiyomi
7071040	US	7/4/2006	Method of fabricating thin
(10/639478)		(8/13/2003)	film transistor
			Materia Na lite Cana
			Matsunaga, Naoki; Sera,
JP4371687	JP	9/11/2009	Kenji
(JP2003-087871)	JF	I	Backlight device and liquid
(31 2003-007071)		(3/27/2003)	crystal display
			Ono, Shinichirou; Fukuyoshi,
			Hirokazu
7106394	US	9/12/2006	Backlight unit in a liquid
(10/807245)		(3/24/2004)	crystal display device
•			
			Ono, Shin-Ichirou;
			Fukuyoshi, Hirokazu

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		Issue Date/	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
TWI255948	TW	6/1/2006	Backlight unit in a liquid
(TW093108056)		(3/25/2004)	crystal display device
			Ono, Shinichirou; Fukuyoshi, Hirokazu
CNZL200410031388.8	CN	4/25/2007	Poor light apparatus and
(CN200410031388.8)		(3/26/2004)	liquid crystal display
			Ono, Shin-Ichirou; Fukuyoshi, Hirokazu
KR10-0685086	KR	2/13/2007	Backlight unit of LCD for
(KR10-2004-0020688)		(3/26/2004)	improving luminance
			Ono, Shinichirou; Fukuyoshi, Hirokazu
7050020	US	5/23/2006	3D image/2D image
(10/646799)		(8/25/2003)	switching display apparatus
			and portable terminal device
			Uehara, Shin-ichi; Takanashi, Nobuaki; Hayama, Hiroshi
GB1394593	GB	6/2/2010	3D image / 2D image
(GB03019284.3)		(8/26/2003)	switching display apparatus
			Uehara, Shin-ichi; Takanashi, Nobuaki; Hayama, Hiroshi
7148871	US	12/12/2006	Liquid crystal display device,
(10/656297)		(9/8/2003)	liquid crystal display device
			driving method, and liquid
			crystal projector apparatus
			Sekine, Hiroyuki
TWI255382	TW	5/21/2006	Liquid crystal display device
(TW093111937)		(4/28/2004)	
			Okamoto, Mamoru; Nishida,
			Shinichi; Konno, Takayuki;
7046227	770		Maruyama, Muneo
7046327	US	5/16/2006	Liquid crystal display device
(10/833318)		(4/28/2004)	including columnar spacer
			above gate line
			Okamoto, Mamoru; Nishida, Shinichi; Konno, Takayuki;
			Maruyama, Muneo

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.		<u>Issue Date/</u>	
Patent/Application Number 7057340	Country	Filing Date	Title of Patent and Inventors
(10/838203)	US	6/6/2006	Organic electroluminescence
(10/838203)		(5/5/2004)	display device
			Imura, Hironori
CNZL200410043242.5	CN	7/15/2009	Organic electroluminescence
(CN200410043242.5)		(5/14/2004)	display device
			Imura, Hironori
KR10-0707544	KR	4/6/2007	Organic electroluminescence
(KR10-2004-0034135)		(5/14/2004)	display device robust to stress
			from outside
ID4125000	ID	- (112/2000	Imura, Hironori
JP4135088	JP	6/13/2008	Liquid crystal display and
(JP2003-283668)		(7/31/2003)	liquid crystal projector
			Sato, Yuko; Matsushima, Jin;
			Sugimoto, Mitsuhiro;
			Sumiyoshi, Ken
7310127	US	12/18/2007	LCD device and LCD
(10/901206)		(7/29/2004)	projector
		(projector
			Sato, Yuko; Matsushima, Jin;
			Sugimoto, Mitsuhiro;
			Sumiyoshi, Ken
CNZL200410055632.4	CN	8/29/2007	LCD device and LCD
(CN200410055632.4)		(7/30/2004)	projector
			Sato, Yuko; Matsushima, Jin;
			Sugimoto, Mitsuhiro;
			Sumiyoshi, Ken
7554162	US	6/30/2009	Thin film transistor substrate
(10/873149)		(6/23/2004)	with low reflectance upper
			electrode
			Hayashi, Kenichi;
			Shimamoto, Hirofumi;
			Matsuzaki, Tadahiro
7242450	US	7/10/2007	Orientation film rubbing
(10/931560)		(9/1/2004)	apparatus and method to
			suppress scraping-off of the
			orientation film
			Mizoguchi, Chikaaki

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Patent/Application Number	Country	<u>Issue Date/</u> <u>Filing Date</u>	Title of Patent and Inventors
JP4047796	JP	11/30/2007	Backlight device and double-
(JP2003-376749)		(11/6/2003)	faced display device
			Ogawa, Toshihisa; Ono, Shinichirou
7090365	US	8/15/2006	Backlight unit having an
(10/970737)		(10/21/2004)	inclined light emission surface
			Ogawa, Toshihisa; Ono, Shin-ichirou
CNZL200410084884.X (CN200410084884.X)	CN	11/26/2008 (11/1/2004)	Backlight unit having an inclined light emission surface
			Ogawa, Toshihisa; Ono, Shin-ichirou
KR10-0669056	KR	1/9/2007	Backlight unit having an
(KR10-2004-0090014)		(11/5/2004)	inclined light emission
			surface
			Ogawa, Toshihisa; Ono, Shinichirou
TWI293389	TW	2/11/2008	Backlight unit and double-
(TW093133818)		(11/5/2004)	sided display device
			Ogawa, Toshihisa; Ono, Shin-ichirou
TWI247431 (TW093136866)	TW	1/11/2006 (11/30/2004)	Liquid crystal display unit
(1110)3130000)		(11/30/2004)	Kimura, Shigeru
7511301 (11/000153)	US	3/31/2009 (12/1/2004)	Liquid crystal display unit
(11/000155)		(12/1/2004)	Kimura, Shigeru
JP4512976	JP	5/21/2010	Liquid crystal display device
(JP2003-411873)		(12/10/2003)	and its manufacturing method
			Hashimoto, Nobuaki
TWI269109	TW	12/21/2006	Liquid crystal display device
(TW093137736)		(12/7/2004)	and method of manufacturing the same
			Hashimoto, Yoshiaki

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
7280178 (11/007157)	US	10/9/2007 (12/9/2004)	Liquid crystal display device and method of manufacturing the same
CNZL200410100094.6 (CN200410100094.6)	CN	6/11/2008 (12/10/2004)	Hashimoto, Yoshiaki Liquid crystal display device and method of manufacturing the same
KR10-0704062 (KR10-2004-0104009)	KR	3/29/2007 (12/10/2004)	Hashimoto, Yoshiaki Liquid crystal display device and method of manufacturing the same
7294881 (11/047814)	US	11/13/2007 (2/2/2005)	Hashimoto, Yoshiaki Nonvolatile semiconductor memory having a charge accumulation layer connected to a gate
JP4438441 (JP2004-038571)	JP	1/15/2010 (2/16/2004)	Korenari, Takahiro; Sera, Kenji; Kanou, Hiroshi Lighting system and display device equipped with it
7160003 (11/052765)	US	1/9/2007 (2/9/2005)	Saito, Goro; Imai, Masao Illumination device and display device incorporating the same
CNZL200510007993.6 (CN200510007993.6)	CN	3/5/2008 (2/16/2005)	Saitoh, Goroh; Imai, Masao Illumination device and display device incorporating
JP4412027 (JP2004-096525)	JP	11/27/2009 (3/29/2004)	Saitoh, Goroh; Imai, Masao Amplifier circuit and display device
CNZL200510062558.3 (CN200510062558.3)	CN	4/29/2009 (3/29/2005)	Tsuchi, Hiroshi Amplifier circuit and display device
1			Tsuchi, Hiroshi

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Country US	Filing Date 3/4/2008	Title of Patent and Inventors
105		Amplifier circuit and display
	(3/29/2005)	device
		Tsuchi, Hiroshi
US	2/27/2007 (3/23/2005)	Semiconductor device having a low-resistance bus interconnect, method of manufacturing same, and display apparatus employing same
US	7/29/2008 (12/14/2006)	Toba, Tamaki Semiconductor device having a low-resistance bus interconnect, method of manufacturing same, and display apparatus employing same Toba, Tamaki
CN	6/11/2008 (3/31/2005)	Semiconductor device having a low-resistance bus interconnect, method of manufacturing same, and display apparatus employing same
		Toba, Tamaki
CN	(4/8/2005)	Space saving on peripheral rim outside display pixel region in display device
		Ishibashi, Osamu
US	1/27/2009 (4/11/2005)	Space saving on peripheral rim outside display pixel region in display device
		Ishibashi, Osamu
TW	6/21/2007 (4/28/2005)	Liquid crystal display device, and light source driving circuit and method to be used in same Honbo, Nobuaki
	CN	US 2/27/2007 (3/23/2005) US 7/29/2008 (12/14/2006) CN 6/11/2008 (3/31/2005) CN (4/8/2005) US 1/27/2009 (4/11/2005) TW 6/21/2007

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Patent/Application Number	Country	<u>Issue Date/</u> <u>Filing</u> Date	Title of Patent and Inventors
CNZL200510066717.7 (CN200510066717.7)	CN	1/23/2008 (4/30/2005)	Liquid crystal display device, and light source driving circuit and method to be used in same
7489295 (11/118432)	US	2/10/2009 (5/2/2005)	Honbo, Nobuaki Liquid crystal display device, and light source driving circuit and method to be used in same
JP4491638 (JP2004-150514)	JP	4/16/2010 (5/20/2004)	Honbo, Nobuaki External commutated inverter circuit for backlight and driving method
7345432 (11/131231)	US	3/18/2008 (5/18/2005)	Honpo, Nobuaki Inverter circuit for lighting backlight of liquid crystal display and method for driving the same
CNZL200610005009.7 (CN200610005009.7)	CN	9/3/2008 (1/18/2006)	Honbo, Nobuaki Liquid crystal display panel and liquid crystal display device
7525618 (11/334681)	US	4/28/2009 (1/18/2006)	Hiroya, Tsutomu; Sumiyoshi, Ken; Shigemura, Kouji Liquid crystal display panel and liquid crystal display device
JP4382681 (JP2005-027909)	JP	10/2/2009 (2/3/2005)	Hiroya, Tsutomu; Sumiyoshi, Ken; Shigemura, Kouji Liquid crystal display device Sakaki, Yoshinobu
TWI301923 (TW095103448)	TW	10/11/2008 (1/27/2006)	Backlight unit and liquid- crystal display device using the same Sakaki, Yoshinobu

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Datast and Investors
KR10-0823063	KR	4/11/2008	Title of Patent and Inventors Backlight unit and liquid-
(KR10-2006-0010090)	KK	(2/2/2006)	crystal display device using
(2000 0010000)		(2/2/2000)	the same
			Sakaki, Yoshinobu
7350956	US	4/1/2008	Backlight unit and liquid-
(11/345306)		(2/2/2006)	crystal display device using
			the same
JP5309059	JP	7/5/2012	Sakaki, Yoshinobu
(JP2010-049923)	JP	7/5/2013 (3/5/2010)	Method and apparatus for
(31 2010-049923)		(3/3/2010)	deciding occurrence of microcrystals
			increerystais
			Okumura, Nobu
7473657	US	1/6/2009	Laser irradiation method and
(11/361756)		(2/24/2006)	apparatus for forming a
			polycrystalline silicon film
707004			Okumura, Hiroshi
7372211	US	5/13/2008	Cold cathode tube lighting
(11/362839)		(2/28/2006)	device and driving method
			and integrated circuit to be used in same
			used in same
			Honbo, Nobuaki
JP4543385	JP	7/9/2010	Liquid crystal display device
(JP2005-073631)		(3/15/2005)	and itsmanufacturing method
			77 1 77 1
			Yasuda, Yukiyasu; Suzuki,
KR10-0823061	KR	4/11/2008	Seiji Liquid crystal display device
(KR10-2006-0023072)	KIK	(3/13/2006)	capable of settling
()		(37 137 2000)	disclination defect and
			manufacturing method of the
			same
			Yasuda, Kyounei; Suzuki,
			Seiji

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Patent/Application NumberCountryFiling DateTitle of Patent and 8/20/2008CNZL200610059141.6CN8/20/2008Liquid crystal dis and manufacturin the sameVasuda, Kyounei SeijiYasuda, Kyounei Seiji7564531US7/21/2009LCD device and r including a plastic with metal layer of copper surrounder metal film embed groove within the substrate	
(CN200610059141.6) (3/15/2006) and manufacturin the same Yasuda, Kyounei Seiji 7564531 (11/375112) US 7/21/2009 (3/15/2006) LCD device and r including a plastic with metal layer copper surrounder metal film embed groove within the substrate	
the same Yasuda, Kyounei Seiji 7564531 (11/375112) US 7/21/2009 (3/15/2006) LCD device and r including a plastic with metal layer of copper surrounder metal film embed groove within the substrate	
Yasuda, Kyounei Seiji 7564531 (11/375112) US 7/21/2009 (3/15/2006) LCD device and r including a plastic with metal layer of copper surrounder metal film embed groove within the substrate	g method of
7564531 US 7/21/2009 LCD device and r (11/375112) (3/15/2006) including a plastic with metal layer c copper surrounded metal film embed groove within the substrate	
7564531 US 7/21/2009 LCD device and r including a plastic with metal layer c copper surrounded metal film embed groove within the substrate	Suzuki,
(3/15/2006) including a plastic with metal layer copper surrounder metal film embed groove within the substrate	
with metal layer of copper surrounder metal film embed groove within the substrate	
copper surrounded metal film embed groove within the substrate	ontaining
metal film embed groove within the substrate	
substrate	
	plastic
Yasuda, Kyounei:	Suzuki
Seiji	
CNZL200610100954.5 CN 9/17/2008 Optical element, of	
(CN200610100954.5) (8/2/2006) device, mobile inf	
terminal, and man	ufacturing
method	
Sumiyoshi, Ken; I	Mimura.
Koji	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
7551340 US 6/23/2009 Optical element, of	
(11/461959) (8/2/2006) device, mobile inf	
terminal, and man	ufacturing
method	
Sumiyoshi, Ken; I	Aimura.
Koji	,,
JP4650624 JP 12/24/2010 Backlight unit and	- 1
(JP2005-216776) (7/27/2005) crystal display dev	rice
Shinohara, Toshiy	o: Sugitori
Choei	a, Sugnam,
TWI339761 TW 4/1/2011 Backlight unit, dis	play device
(TW095126699) (7/21/2006) provided with the	
unit, and method of	backnight
manufacturing the	of
device	of
Shinohara, Toshiy	of
Chouei	of display

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75 / //A 15 / 77 F	G .	Issue Date/	
Patent/Application Number 7446825	Country US	<u>Filing Date</u> 11/4/2008	Title of Patent and Inventors
(11/490119)	US	(7/21/2006)	Backlight unit, display device provided with the backlight unit, and method of manufacturing the display device
KR10-0777871	KR	11/13/2007	Shinohara, Toshiya; Sugitani, Chouei Backlight unit for preventing
(KR10-2006-0070122)		(7/26/2006)	deformation of a reflector sheet caused by an fpc, a display device having the backlight unit, and a method for manufacturing the display device Shinohara, Toshiya; Sugitani,
CNZL200610107589.0 (CN200610107589.0)	CN	12/31/2008 (7/27/2006)	Chouei Backlight unit, display device provided with the backlight unit, and method of manufacturing the display device
			Shinohara, Toshiya; Sugitani, Chouei
7499119 (11/582315)	US	3/3/2009 (10/18/2006)	Liquid-crystal display device with thin-film transistors and method of fabricating the same
			Yasuda, Kyounei
JP4771065 (JP2005-288352)	JP	7/1/2011 (9/30/2005)	Light source device, display device, and terminal device
			Uehara, Shinichi; Imai, Masao
CNZL200610139942.3 (CN200610139942.3)	CN	5/9/2012 (9/27/2006)	Light source device, display device, and terminal device
			Uchara, Shin-ichi; Imai, Masao

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
RE44063	US	3/12/2013	Light source device, display
(13/111815)		(5/19/2011)	device, and terminal device
			Uehara, Shin-ichi; Imai, Masao
JP4813861 (JP2005-282719)	JP	9/2/2011 (9/28/2005)	Liquid crystal module
			Morishita, Shizuo
TWI361307 (TW095134782)	TW	(9/20/2006)	Liquid crystal display module
7520/55	TIG	4/01/0000	Morishita, Shizuo
7520655 (11/526571)	US .	(9/26/2006)	Liquid crystal display module
CNIZI 200710141700 0	CN I	7/02/0000	Morishita, Shizuo
CNZL200610141508.9 (CN200610141508.9)	CN	7/22/2009 (9/28/2006)	Liquid crystal display module
77.100.1610			Morishita, Shizuo
JP4994649	JP	5/18/2012	Backlight for color liquid
(JP2005-349752)		(12/2/2005)	crystal display device
	·		Takahashi, Yoshitomo
RE45229	US	11/4/2014	Backlight for color liquid
(13/292700)		(11/9/2011)	crystal display apparatus
			Takahashi, Mitsuasa
JP4577229	JP	9/3/2010	Backlight device and liquid
(JP2006-038953)		(2/16/2006)	crystal display
			Nishigaki, Eitaro
RE44118	US	4/2/2013	Backlight unit and liquid-
(13/245678)		(9/26/2011)	crystal display device using the same
			Nishigaki, Eitaro
7568574	US	8/4/2009	Substrate transportation
(11/798090)		(5/10/2007)	method and apparatus
			Nagatomo, Kazuya
JP4939133	JP	3/2/2012	Connector
(JP2006-187865)		(7/7/2006)	
			Uchida, Toshio

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Patent/Application Number	Country	Issue Date/ Filing Date	Title of Patent and Inventors
7491079 (11/822489)	US	2/17/2009 (7/6/2007)	Connector and method of connecting the same
TD 4000100	770	10/00/0011	Uchida, Toshio
JP4890100 (JP2006-144763)	JP	12/22/2011 (5/25/2006)	Image display device
7518690 (11/802784)	US	4/14/2009 (5/24/2007)	Komahisa, Manabu Image display device Komaju, Manabu
JP4493064 (JP2000-308171)	JP	4/16/2010 (10/6/2000)	Flat-face type fluorescent lamp, its fixing structure, and liquid crystal display device Fujishiro, Fumihiko
TWI259495 (TW090124797)	TW	8/1/2006 (10/5/2001)	Flat fluorescent lamp having unique mating portion and liquid crystal display device incorporating such a flat fluorescent lamp
6611092 (09/971994)	US	8/26/2003 (10/5/2001)	Fujishiro, Fumihiko Flat fluorescent lamp having unique mating portion and liquid crystal display device incorporating such a flat fluorescent lamp
6864947 (09/774921)	US	3/8/2005 (1/31/2001)	Fujishiro, Fumihiko Method of fabricating liquid crystal display substrate and the same
TWI150201 (TW087117617)	TW	2/1/2002 (10/23/1998)	Shiraishi, Hideaki Semiconductor device and method for producing the same Fujita, Akira; Sugitani, Choei

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		<u>Issue Date/</u>	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
DE69808405.5 (DE69808405.5)	DE	10/2/2002 (10/26/1998)	Connection terminal structure for liquid crystal display and semiconductor device, and method of producing the same
			Fujita, Akira; Sugitani, Choei
FR0913719 (FR98120246.8)	FR	10/2/2002 (10/26/1998)	Connection terminal structure for liquid crystal display and semiconductor device, and method of producing the same
GB0913719 (GB98120246.8)	GB	10/2/2002 (10/26/1998)	Fujita, Akira; Sugitani, Choei Connection terminal structure for liquid crystal display and semiconductor device, and method of producing the same
			Fujita, Akira; Sugitani, Choei
6184966 (09/179654)	US	2/6/2001 (10/27/1998)	Semiconductor device and method for producing the same
			Fujita, Akira; Sugitani, Choei
6323931 (09/671414)	US	11/27/2001 (9/27/2000)	LCD with external circuit having anti-short-circuit pattern and particular structure
			Fujita, Akira; Sugitani, Choei
(10/003550)	US	1/27/2004 (10/24/2001)	Semiconductor device and method for producing the same
			Fujita, Akira; Sugitani, Choei

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
KR10-0302577	KR	7/4/2001	Semiconductor device and
(KR10-1998-0045382)		(10/28/1998)	manufacturing method
,		,	thereof, especially for an
			LCD capable of preventing
			short-circuit occurred by
			current leakage between
			terminals due to corrosion of
			metal wires by installing an
			interterminal anti-short-
			circuiting pattern that is
			effective for cutting off
			humidity
			Fujita, Akira; Sugitani, Choei
6064458	US	5/16/2000	Rubbing apparatus and
(09/181641)		(10/29/1998)	rubbing method
			Mori, Shigeru
TWI137991	TW	7/11/2001	Semiconductor device and
(TW087116884)		(10/9/1998)	method of making the same
DD (000,5700,0		0/6/0006	Nakata, Shinichi
DE69835793.0	DE	9/6/2006	Thin-film transistor with
(DE69835793.0)		(10/26/1998)	light-shielding film for use in
			liquid crystal devices, and
			method of making the same
ED0015265	ED	0/6/2006	Nakata, Shinichi
FR0915365	FR	9/6/2006	Thin-film transistor with
(FR98120242.7)		(10/26/1998)	light-shielding film for use in
			liquid crystal devices, and
			method of making the same
GB0915365	GB	0/6/2006	Nakata, Shinichi Thin-film transistor with
(GB98120242.7)	UB	9/6/2006	
(GD90120242.7)		(10/26/1998)	light-shielding film for use in
			liquid crystal devices, and
			method of making the same
6262436	US	7/17/2001	Nakata, Shinichi Semiconductor device and
(09/185640)	US	1	
(07/103040)		(11/4/1998)	method of making the same
			Nakata, Shinichi

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Patent/Application Number	Country	<u>Issue Date/</u> <u>Filing</u> Date	Title of Patent and Inventors
KR10-0323367 (KR10-1998-0047384)	KR	1/23/2002 (11/5/1998)	Semiconductor device suitable for a flat display LCD of an active matrix display comprising a-si TFT and its fabrication method
6051815 (09/262340)	US	4/18/2000 (3/4/1999)	Apparatus for heat-treating substrate and method for separating the substrate from the apparatus Satou, Yasuyuki
KR10-0326826 (KR10-1998-0037915)	KR	2/20/2002 (9/15/1998)	LCD HAVING A WIDE VIEWING ANGLE, ESPECIALLY CAPABLE OF REDUCING APPEARANCE OF LIGHT SPOTS BY ALLOWING SOME LIQUID CRYSTAL MOLECULES ADJACENT TO THE TWO ALIGNMENT LAYERS TO HAVE A PRE-TILT ANGLE OF MORE THAN 2° Matsumoto, Kimikazu; Fujimaki, Eriko
6078375 (09/154039)	US	6/20/2000 (9/16/1998)	Liquid crystal display device with wide viewing angle Matsumoto, Kimikazu; Fujimaki, Eriko
TWI144927 (TW087115488)	TW	11/1/2001 (9/17/1998)	Liquid crystal display device with wide viewing angle Matsumoto, Kimikazu; Fujimaki, Eriko
TW1164669 (TW087117847)	TW	9/21/2002 (10/28/1998)	In-plane switching type liquid crystal display Shibahara, Hideo

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		<u>Issue Date/</u>	
Patent/Application Number	<u>Country</u>	Filing Date	Title of Patent and Inventors
6104463	US	8/15/2000	In-plane switching type liquid
(09/181338)		(10/28/1998)	crystal display and method of
			operating the same
			Shibahara, Hideo
KR10-0303967	KR	7/16/2002	In-plane switching type liquid
(KR10-1998-0046663)		(10/31/1998)	crystal display device and a
			method for operating the
-			device for reducing uneven display by installing a
			chromaticity controller for
			controlling the chromaticity
			of the backlight
			Shibahara, Hideo
JP4775824	JP	7/8/2011	Microlens array substrate,
(JP2008-268528)	, T	(10/17/2008)	liquid crystal display element,
			liquid crystal display, and
			liquid crystal projector
			Matsushima, Hitoshi; Uehara,
			Shinichi; Sato, Yuko;
•			Sumiyoshi, Ken; Kaneko,
			Setsuo
JP4735867	JP	5/13/2011	Master die for microlens
(JP2008-268529)		(10/17/2008)	array
			Matsushima, Hitoshi; Uehara,
			Shinichi; Sato, Yuko;
			Sumiyoshi, Ken; Kaneko,
7525732	US	4/28/2009	Setsuo Method for forming finely-
(11/843677)		(8/23/2007)	structured parts, finely-
			structured parts formed
			thereby, and product using
			such finely-structured part
			Uehara, Shin-ichi; Sato,
			Yuko; Sumiyoshi, Ken;
			Kaneko, Setsuo; Matsushima,
			Jin

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Patent/Application Number	Country	<u>Issue Date/</u> <u>Filing Date</u>	Title of Patent and Inventors
6172730	US	1/9/2001	Liquid crystal display
(09/262867)		(3/5/1999)	apparatus having stepped
(section in glass substrate
			Fujita, Akira
6480254	US	11/12/2002	Liquid crystal display
(09/667786)		(9/22/2000)	apparatus having stepped
			section in glass substrate
			Fujita, Akira
JP3482908	JP	10/17/2003	Driving circuit, driving
(JP11-145768)		(5/26/1999)	circuit system, biasing circuit, and driving circuit device
			Tsuchi, Hiroshi
6624669	US	9/23/2003	Drive circuit and drive circuit
(09/578287)		(5/25/2000)	system for capacitive load
			Tsuchi, Hiroshi
KR10-0385780	KR	5/19/2003	Driving circuit, driving
(KR10-2000-0028753)		(5/26/2000)	circuit system, biasing circuit,
			and driving circuit device
			Tsuchi, Hiroshi
TWI174546	TW	3/21/2003	Drive circuit and drive circuit
(TW089110429)		(5/26/2000)	system for capacitive load
			Tsuchi, Hiroshi
6618204	US	9/9/2003	Light modulator, light source
(10/254493)		(9/25/2002)	using the light modulator,
			display apparatus using the
			light modulator, and method
			for driving the light
			modulator
			Takatori, Ken-ichi
6618027	US	9/9/2003	Light modulator, light source
(10/254710)		(9/25/2002)	using the light modulator,
			display apparatus using the
			light modulator, and method
			for driving the light
			modulator
			Takatori, Ken-ichi

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
JP3415602	JP	4/4/2003	Method for forming pattern
(JP2001-157209)	"	(5/25/2001)	Wethod for forming pattern
(012001 107205)		(3/23/2001)	Kido, Shusaku
KR10-0480458	KR	3/23/2005	Method for forming pattern
(KR10-2001-0036792)		(6/26/2001)	Parama Parama
			Kido, Shusaku
KR10-0500676	KR	7/1/2005	Method for forming pattern to
(KR10-2004-0058305)		(7/26/2004)	precisely control resist pattern
			after resist pattern is varied in
			dimension
			Kido, Shusaku
TWI162398	TW	9/1/2002	Method of forming a pattern
(TW090115660)		(6/26/2001)	and semiconductor device
			produced by this method
			Kido, Shusaku
6707107	US	3/16/2004	Method of deforming a
(09/888442)		(6/26/2001)	pattern and semiconductor
			device formed by utilizing
			deformed pattern
			Kido, Shusaku
7060623	US	6/13/2006	Method of deforming a
(10/201132)		(7/24/2002)	pattern and semiconductor
			device formed by utilizing
			deformed pattern
			Kido, Shusaku
6953976	US	10/11/2005	Method of deforming a
(10/300735)		(11/21/2002)	pattern and semiconductor
			device formed by utilizing
			deformed pattern
			Kido, Shusaku
6977422	US	12/20/2005	Method of deforming a
(10/778038)		(2/17/2004)	pattern and semiconductor
			device formed by utilizing
			deformed pattern
			Kido, Shusaku

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
7030467	US	4/18/2006	Method of deforming a
(10/778039)		(2/17/2004)	pattern and semiconductor
, i			device formed by utilizing
			deformed pattern
			Kido, Shusaku
6949766	US	9/27/2005	Method of deforming a
(10/778040)		(2/17/2004)	pattern and semiconductor
			device formed by utilizing
			deformed pattern
			Kido, Shusaku
7554164	US	6/30/2009	Semiconductor device having
(11/187978)		(7/25/2005)	a gap between a gate
			electrode and a dummy gate
			electrode
			Kido, Shusaku
6791145	US	9/14/2004	Semiconductor device formed
(10/778041)		(2/17/2004)	by utilizing deformed pattern
			Kido, Shusaku
JP3394483	JP	4/7/2003	Thin-film transistor substrate
(JP11-325739)		(11/16/1999)	and manufacturing method
			therefor
			Kato, Takeshi; Motojima,
	į		Hideto
GB2362509	GB	9/10/2002	Thin film transistor and
(GB0027792.1)		(11/14/2000)	fabrication method of the
			same
			Katoh, Tsuoyoshi;
			Motoshima, Hideto
KR10-0348647	KR	7/31/2002	Thin-film transistor substrate
(KR10-2000-0067437)		(11/14/2000)	and manufacturing method
		į.	therefor
			Katoh, Tsuoyoshi;
			Motosjima, Hideto

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		Issue Date/	
Patent/Application Number	<u>Country</u>	Filing Date	Title of Patent and Inventors
TWI189748	TW	11/1/2003	Thin film transistor and
(TW089124125)		(11/14/2000)	fabrication method of the
			same
			Katoh, Tsuoyoshi;
			Motoshima, Hideto
6730970	US	5/4/2004	Thin film transistor and
(09/711504)		(11/14/2000)	fabrication method of the
			same
			Katoh, Tsuoyoshi;
			Motoshima, Hideto
7049184	US	5/23/2006	Semiconductor thin film, thin
(10/838368)		(5/5/2004)	film transistor, method for
			manufacturing same, and
			manufacturing equipment of
		:	semiconductor thin film
			Tanabe, Hiroshi
8377805	US	2/19/2013	Semiconductor thin film, thin
(13/359406)		(1/26/2012)	film transistor, method for
			manufacturing same, and
			manufacturing equipment of
			semiconductor thin film
			Tanabe, Hiroshi
8710507	US	4/29/2014	Semiconductor thin film, thin
(13/761585)		(2/7/2013)	film transistor, method for
			manufacturing same, and
			manufacturing equipment of
			semiconductor thin film
			Tanabe, Hiroshi
JP4274127	JP	3/13/2009	TFT, TFT manufacturing
(JP2005-012465)		(1/20/2005)	method and image input
			deviceusing TFT
			Tanabe, Hiroshi; Haga,
			Hiroshi

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
7138303	US	11/21/2006	Method for manufacturing a
(10/815393)	05	(4/1/2004)	thin film transistor having
(10/010393)		(1/1/2001)	high mobility and high on-
			current
			Tanabe, Hiroshi; Haga,
			Hiroshi
7285809	US	10/23/2007	Thin film transistor having
(11/410184)		(4/24/2006)	high mobility and high on-
			current
			Tanabe, Hiroshi; Haga,
			Hiroshi
KR10-0264757	KR	6/5/2000	Active matrix LCD and
(KR10-1998-0002500)		(1/30/1998)	method of producing the
			same
			T 1 TT 1'
6665023	TTC	10/17/2002	Tanaka, Hiroaki
(09/323492)	US	12/16/2003	Active matrix liquid crystal
(09/323492)		(6/1/1999)	display device having
			particular capacitance patterns
	,		patterns
		ļ	Watanabe, Makoto;
			Watanabe, Takahiko
6020214	US	2/1/2000	Method for manufacturing
(09/116213)		(7/16/1998)	thin film transistor array
			substrate
			Watanabe, Takahiko;
			Sukegawa, Osamu
6137465	US	10/24/2000	Drive circuit for a LCD
(09/195617)		(11/19/1998)	device
			Sekine, Hiroyuki; Okumura,
7079101	US	7/19/2006	Fujio
(09/290579)	US	7/18/2006	Liquid crystal display device
(0)1470317)		(4/13/1999)	and driving method therefor
			Asada, Hideki
6567136	US	5/20/2003	Liquid crystal display device
(09/461001)		(12/14/1999)	Enquid orystal display device
((12/11/12/2))	Sakuramoto, Tamaki;
			Okumura, Hiroshi

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
6597414 (09/513923)	US	7/22/2003 (2/28/2000)	Horizontal electric-field type LCD device
			Hasegawa, Fumio
JP3179410 (JP10-150821)	JP	4/13/2001 (6/1/1998)	Liquid crystal display device Matsuyama, Hiroaki; Kobayashi, Kazumi; Hirai, Yoshihiko; Ishii, Toshiya; Murai, Hideya; Suzuki, Shigeyoshi
TWI134010 (TW088108722)	TW	5/28/2001 (5/27/1999)	Liquid crystal display device Matsuyama, Hiroaki; Kobayashi, Kazumi; Hirai, Yoshihiko; Ishii, Toshiya; Murai, Hideya; Suzuki, Shigeyoshi
6300996 (09/321454)	US	10/9/2001 (5/27/1999)	Liquid crystal display apparatus Matsuyama, Hiroaki; Kobayashi, Kazumi; Hirai, Yoshihiko; Ishii, Toshiya; Murai, Hideya; Suzuki, Masayoshi
KR10-0339896 (KR10-1999-0019785)	KR	5/27/2002 (5/31/1999)	Liquid crystal display apparatus Gobayasi, Gazmi; Hirai, Yoshiko; Ishihi, Doshiya; Masyama, Hiroyaki; Murai, Hideya; Szuki, Amasayoshi
5875009 (08/912790)	US	2/23/1999 (8/18/1997)	Sequential staggered type thin film transistor Shibahara, Hideo
JP3161528 (JP10-252663)	JP	2/23/2001 (9/7/1998)	Liquid crystal display panel Ihara, Hiroshi
TWI249069 (TW088115095)	TW	2/11/2006 (9/2/1999)	Liquid crystal display device Ihara, Hirofumi

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
KR10-0374435	KR	2/19/2003	Liquid crystal display device
(KR10-1999-0037827)		(9/7/1999)	and method of manufacturing
,			the same
			Ihara, Hirofumi
6268898	US	7/31/2001	Liquid crystal display device
(09/391151)		(9/7/1999)	and method of manufacturing the same
			Ihara, Hirofumi
TWI149753	TW	1/21/2002	Liquid crystal display thin
(TW087116368)		(10/1/1998)	film transistor array and
			method of fabricating the
			same
			Tani, Masatoshi
KR10-0307987	KR	8/27/2001	Thin-film transistor arrays for
(KR10-1998-0041995)		(10/8/1998)	liquid crystal display device
			and manufacturing method
			Tani, Masatoshi
5929947	US	7/27/1999	Liquid crystal display thin
(09/168085)		(10/8/1998)	film transistor array with
			redundant film formed over a
			contact hole and method of
			fabricating the same
			Tani, Masatoshi
6441877	US	8/27/2002	Active matrix type liquid
(09/413535)		(10/6/1999)	crystal display device and
			method of forming the same
			Watanabe, Takahiko
JP3147104	JP	1/12/2001	Active matrix type liquid
(JP10-315692)		(11/6/1998)	crystal display device and
			driving method therefor
			Sekine, Hiroyuki
TWI184523	TW	8/21/2003	Active matrix liquid crystal
(TW088119444)		(11/5/1999)	display apparatus and its
			driving method
			Sekine, Hiroyuki

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Patent/Application Number	Country	<u>Issue Date/</u> <u>Filing Date</u>	Title of Patent and Inventors
KR10-0325913 (KR10-1999-0049092)	KR	2/14/2002 (11/6/1999)	Liquid crystal display device
(444440 1999 00 1909 2)		(11,0,1333)	Sekine, Hiroyuki
6407728	US	6/18/2002	Active matrix liquid crystal
(09/435968)		(11/8/1999)	display device having signal selectors and method of driving the same
			Sekine, Hiroyuki
JP3185778 (JP11-032847)	JP	5/11/2001 (2/10/1999)	Active matrix type liquid crystal display device, its manufacture and its driving method
			Watanabe, Makoto; Watanabe, Takahiko
TWI165847 (TW089102090)	TW	11/1/2002 (2/9/2000)	Active matrix type liquid crystal display device, its manufacture and its driving method
			Watanabe, Makoto; Watanabe, Takahiko
KR10-0375897 (KR10-2000-0006122)	KR	2/28/2003 (2/10/2000)	Active matrix lcd, manufacturing method therefor and driving method therefor
			Watanabe, Makoto; Watanabe, Takahiko
6563481 (09/501685)	US	5/13/2003 (2/10/2000)	Active matrix liquid crystal display device, method of manufacturing the same, and method of driving the same
			Watanabe, Makoto; Watanabe, Takahiko
TWI152464 (TW087119765)	TW	3/21/2002 (11/27/1998)	Active matrix liquid-crystal display device
			Matsumoto, Kimikazu; Nishida, Shinichi; Shibahara, Hideo

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
KR10-0321270	KR	1/7/2002	Active matrix liquid crystal
(KR10-1998-0051439)	I	(11/28/1998)	display device
			Matsumoto, Kimikazu; Nishida, Shinichi; Shibahara, Hideo
JP3327252 (JP11-139963)	JP	7/12/2002 (5/20/1999)	Tape carrier, manufacture thereof, TCP, and liquid crystal display
			Ato, Kisho
KR10-0367320 (KR10-2000-0026703)	KR	12/24/2002 (5/18/2000)	Tape carrier, manufacture thereof, TCP, and liquid crystal display
			Atou, Noriaki
TWI147570 (TW089109654)	TW	(5/19/2000)	Tape carrier with high flexibility and high density connection wire pattern
			Atou, Noriaki
6633002 (09/575092)	US	10/14/2003 (5/19/2000)	Tape carrier having high flexibility with high density wiring patterns
			Atou, Noriaki
JP3482910 (JP11-149078)	JP	10/17/2003 (5/28/1999)	Scanning circuit
			Sato, Tetsushi; Sekine, Hiroyuki
TWI162272 (TW089108255)	TW	9/1/2002 (5/1/2000)	Scanning circuit
(,		(0,1,2000)	Sato, Tetsushi; Sekine, Hiroyuki
KR10-0371505 (KR10-2000-0028174)	KR	1/24/2003 (5/24/2000)	Scan circuit
·			Sato, Tetsushi; Sekine, Hiroyuki
6876352 (09/577843)	US	4/5/2005 (5/25/2000)	Scanning circuit
			Sato, Tetsushi; Sekine, Hiroyuki

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
JP3451583	JP	7/18/2003	Clamp circuit for liquid
(JP11-180309)		(6/25/1999)	crystal display device
			Shiki, Tatsuya
TWI159982	TW	7/11/2002	Clamping circuit for liquid
(TW089112297)		(6/22/2000)	crystal display device
			Shiki, Tatsuya
6657619	US	12/2/2003	Clamping circuit for liquid
(09/602182)		(6/22/2000)	crystal display device
			Shiki, Tatsuya
KR10-0378853	KR	3/24/2003	Clamp circuit for liquid
(KR10-2000-0034731)		(6/23/2000)	crystal display device
			Shiki, Tatsuya
6580486	US	6/17/2003	Active matrix liquid crystal
(09/604610)		(6/27/2000)	display device having
			electrostatic shielding layer
			between data lines
			Sekine, Hiroyuki
6669809	US	12/30/2003	Apparatus for removing a
(09/791793)		(2/26/2001)	coating film
			Hashimoto, Yoshiaki; Sato,
			Yasuyuki
JP3264270 (JP11-210754)	JP	12/28/2001 (7/26/1999)	Liquid crystal display device
(31 11-210/34)		(1120/1999)	Watanabe, Makoto;
			Watanabe, Takahiko
TWI180112	TW	6/21/2003	Active-matrix in-plane
(TW089114855)		(7/25/2000)	switching mode LCD panel
			Watanabe, Makoto;
			Watanabe, Takahiko
KR10-0331913	KR	3/26/2002	Liquid crystal display device
(KR10-2000-0042942)		(7/26/2000)	
			Watanabe, Makoto;
			Watanabe, Takahiko

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		Issue Date/	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
6646691	US	11/11/2003	Active-matrix in-plane
(09/626114)		(7/26/2000)	switching mode LCD panel
			having multiple common
			electrode voltage sources
			Watanabe, Makoto;
			Watanabe, Takahiko
6469754	US	10/22/2002	Liquid-crystal display with
(09/654624)		(9/5/2000)	scanner
			Okumura, Fujio
6429910	US	8/6/2002	Liquid crystal display device
(09/666819)		(9/21/2000)	and method for repairing
			breakage of circuit lines
			thereof
			Hirata, Tomoya; Miyahara,
			Tae
6788355	US	9/7/2004	Active matrix LCD panel
(09/695212)		(10/25/2000)	
			Ihida, Satoshi; Yamaguchi,
			Hirotaka; Tanaka, Hiroaki;
			Hayase, Takasuke; Kanou,
			Hiroshi; Kaneko, Wakahiko;
			Miyahara, Tae; Sakamoto,
			Michiaki; Nakata, Shinichi
6661401	US	12/9/2003	Circuit for driving a liquid
(09/711632)		(11/13/2000)	crystal display and method
			for driving the same circuit
			Sekine, Hiroyuki
6600172	US	7/29/2003	Image sensor and method of
(09/718603)		(11/22/2000)	fabricating the same
			Matsuno, Fumihiko
JP3266119	JP	1/11/2002	Liquid crystal display device
(JP10-344855)		(11/19/1998)	and video data transfer
			method
			Hori, Yoshihiko

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Patent/Application Number	Country	Issue Date/ Filing Date	Title of Patent and Inventors
TWI172615 (TW088120125)	TW	2/21/2003 (11/18/1999)	Liquid crystal display device and method for transferring image data
6407730 (09/442455)	US	6/18/2002 (11/18/1999)	Hori, Yoshihiko Liquid crystal display device and method for transferring image data
KR10-0313210 (KR10-1999-0051548)	KR	10/17/2001 (11/19/1999)	Hori, Yoshihiko Liquid crystal display device and method for transferring image data Hori, Yoshihiko
6437370 (09/503176)	US	8/20/2002 (2/14/2000)	Image sensor structure and manufacturing process therefor
JP3512665 (JP11-055478)	JP	1/16/2004 (3/3/1999)	Matsuno, Fumihiko Color liquid crystal panel and its manufacture Okamoto, Mamoru; Yamamoto, Yuji; Sakamoto, Michiaki; Nakada, Shinichi; Yoshikawa, Chikanori; Watanabe, Takahiko; Ihara, Hiroshi
(09/513758)	US	8/6/2002 (2/25/2000)	Color liquid-crystal panel having a frame-shaped black matrix and manufacturing process therefor Okamoto, Mamoru; Yamamoto, Yuji; Sakamoto, Michiaki; Nakata, Shinichi; Kikkawa, Hironori; Watanabe, Takahiko; Ihara, Hirofumi

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Data-4/A Visation Number	Commen	Issue Date/	Trial C D-AA I Y
Patent/Application Number JP3544489	Country JP	Filing Date 4/16/2004	Title of Patent and Inventors
(JP11-112008)	Jr	(4/20/1999)	Liquid crystal display device and its manufacture
(JF11-112006)		(4/20/1999)	and its manufacture
			Vaghilarya Chalana
			Yoshikawa, Shuken;
			Watanabe, Takahiko; Ihara,
			Hiroshi; Nakada, Shinichi;
			Okamoto, Mamoru;
-			Yamamoto, Yuji; Sakamoto, Michiaki
VD10 0271705	IZD.	1/00/0002	1
KR10-0371795	KR	1/28/2003	Liquid crystal display device
(KR10-2000-0020798)		(4/19/2000)	and its manufacture
			Ihara, Hiroshi; Nakada, Shinichi; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Yamamoto, Yuji; Yoshikawa,
			Shuken;
6330043	US	12/11/2001	Liquid crystal display device
(09/552035)		(4/19/2000)	and method of fabrication
		•	TFT panel
			Kikkawa, Hironori;
			Watanabe, Takahiko; Ihara,
			Hirofumi; Nakata, Shinichi;
			Okamoto, Mamoru;
			Yamamoto, Yuji; Sakamoto,
TWI100010	(DIX)	7/1/2002	Michiaki
TWI180819	TW	7/1/2003	Liquid crystal display device
(TW089107446)		(4/20/2000)	and method of fabricating
			TFT panel
			Vilslanda Ilinomani
			Kikkawa, Hironori;
			Watanabe, Takahiko; Ihara,
			Hirofumi; Nakata, Shinichi; Okamoto, Mamoru; Yuji,
			Yamamoto; Sakamoto, Michiaki
JP3803510	JP	5/12/2006	Liquid crystal display panel
(JP11-145316)	31	(5/25/1999)	Elquid crystal display panel
(0.11 110010)		(3/23/1999)	Sugimoto, Mitsuhiro
JP3680849	JP	5/27/2005	Liquid crystal display panel
(JP2003-301185)	71	(8/26/2003)	Enquid orystat display panel
(01 2005 501105)		(6/20/2003)	Sugimoto, Mitsuhiro
			Buginioto, Mitsuilito

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
TWI165651 (TW089109335)	TW	10/21/2002 (5/16/2000)	Liquid crystal panel
KR10-0367417 (KR10-2000-0028501)	KR	12/24/2002 (5/25/2000)	Sugimoto, Mitsuhiro Liquid crystal display panel
6486936 (09/579321)	US	11/26/2002 (5/25/2000)	Sugimoto, Mitsuhiro Liquid crystal panel with seal containing hard and soft spacers
JP3838818 (JP11-171294)	JP	8/11/2006 (6/17/1999)	Sugimoto, Mitsuhiro Liquid crystal display panel and its production
			Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hiroshi; Yoshikawa, Shuken; Nakada, Shinichi
TWI254179 (TW089111852)	TW	5/1/2006 (6/16/2000)	Liquid-crystal display panel and method for manufacturing same
			Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hirofumi; Kikkawa, Hironori; Nakata, Shinichi
6870592 (09/594721)	US	3/22/2005 (6/16/2000)	Liquid-crystal display panel with spacer in pixel electrode contact hole and method for manufacturing same
			Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hirofumi; Kikkawa, Hironori; Nakata, Shinichi

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Patent/Application Number	Country	<u>Issue Date/</u> <u>Filing</u> Date	Title of Patent and Inventors
KR10-0386826 (KR10-2000-0033502)	KR	5/26/2003 (6/17/2000)	Liquid crystal display panel and its production
			Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hiroshi; Yoshikawa, Shuken; Nakada, Shinichi
JP3539330 (JP2000-035827)	JP	4/2/2004 (2/14/2000)	Liquid crystal display panel and its manufacturing method Sato, Yuko; Sugimoto, Mitsuhiro
6549259 (09/780382)	US	4/15/2003 (2/12/2001)	Liquid crystal display panel and fabrication method of the same Sato, Yuko; Sugimoto,
KR10-0504683 (KR10-2001-0007007)	KR	7/21/2005 (2/13/2001)	Mitsuhiro Liquid crystal display panel and its manufacturing method Sato, Yuko; Sugimoto,
JP3792485 (JP2000-166318)	JP	4/14/2006 (6/2/2000)	Mitsuhiro Active matrix type liquid crystal display device Sakamoto, Michiaki; Maruyama, Muneo;
TWI291070 (TW090113101)	TW	12/11/2007 (5/30/2001)	Yamamoto, Yuji; Okamoto, Mamoru Active matrix liquid crystal display device Sakamoto, Michiaki;
			Maruyama, Muneo; Yamamoto, Yuji; Okamoto, Mamoru

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		<u>Issue Date/</u>	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
6822704	US	11/23/2004	Active matrix liquid crystal
(09/870749)		(6/1/2001)	display device
			Sakamoto, Michiaki;
			Maruyama, Muneo;
			Yamamoto, Yuji; Okamoto,
			Mamoru
7224414	US	5/29/2007	Active matrix liquid crystal
(10/761354)		(1/22/2004)	display device
			Sakamoto, Michiaki;
			Maruyama, Muneo;
			Yamamoto, Yuji; Okamoto,
			Mamoru
KR10-0413577	KR	12/18/2003	Active matrix liquid crystal
(KR10-2001-0030947)		(6/2/2001)	display device
			Maruyama, Muneo;
			Okamoto, Mamoru;
			Sakamoto, Michiaki;
			Yamamoto, Yuji
TWI288260	TW	10/11/2007	Reflector formed with highly
(TW090116361)		(7/4/2001)	reliable conductive pattern,
			process for fabricating
			reflector and liquid crystal
			display unit using the same
			Sato, Yasuhiro; Kaneko,
			Hideki; Nishiyama, Michiaki
7136123	US	11/14/2006	Reflector formed with highly
(10/985003)		(11/10/2004)	reliable conductive pattern,
		(process for fabricating
			reflector and liquid crystal
			display unit using the same
			Sato, Yasuhiro; Kaneko,
	*		Hideki; Nishiyama, Michiaki
TWI276901	TW	3/21/2007	LCD device having test
(TW089108890)		(5/10/2000)	contact pads
			_
			Ukita, Tooru

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
6930744	US	8/16/2005	LCD device having test
(09/568710)		(5/11/2000)	contact pads
			Ukita, Tooru
6608664	US	8/19/2003	Vibration-proof liquid crystal
(09/577702)		(5/23/2000)	display having mounting end
			regions of lower rigidity
			Hasegawa, Fumio
TWI174315	TW	3/21/2003	Liquid crystal display
(TW089110010)		(5/24/2000)	
			Hasegawa, Fumio
KR10-0374371	KR	2/19/2003	LIQUID CRYSTAL
(KR10-2000-0028246)		(5/25/2000)	DISPLAY
			Hasegawa, Fumio
6760081	US	7/6/2004	Liquid crystal display device
(09/577734)		(5/23/2000)	having uniform feedthrough
			voltage components
			Takagi, Kouji
TWI266941	TW	11/21/2006	A liquid-crystal display
(TW089110233)		(5/26/2000)	apparatus and a method for
		1	manufacturing same
			Hosoyamada, Shunichi
6414740	US	7/2/2002	LCD having temperature
(09/583730)		(5/31/2000)	detection elements provided
	-		on an active-substrate
			Hosoyamada, Shunichi
KR10-0386733	KR	5/26/2003	Liquid crystal display device,
(KR10-2000-0030180)		(6/1/2000)	and manufacturing method of
			liquid crystal display device
			Hosoyamada, Shunichi
TWI253535	TW	4/21/2006	LCD panel in which signal
(TW089110556)		(5/31/2000)	line terminals have slits to
			allow inspection of alignment
			of the terminals and the TCP
			leads
			Fujita, Akira

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		<u> Issue Date/</u>	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
6633361	US	10/14/2003	LCD panel in which signal
(09/587061)		(6/2/2000)	line terminals have slits to
			allow inspection of alignment
			of the terminals and the TCP
			leads
			Fujita, Akira
KR10-0335453	KR	4/23/2002	LCD panel unit for a LCD
(KR10-2000-0030571)		(6/3/2000)	device
			Fujita, Akira
6486931	US	11/26/2002	LCD optical guide plate with
(09/592914)		(6/13/2000)	a roughened back surface
(03/032311)		(0/15/2000)	having projections that
			support a reflecting sheet
			support a removing sheet
			Ueda, Shoichi
6542207	US	4/1/2003	Liquid crystal display
(09/599637)		(6/23/2000)	
			Yoshizawa, Seiji
KR10-0376348	KR	3/5/2003	Liquid crystal display device
(KR10-2000-0035553)		(6/27/2000)	reducing pixel error and
			manufacture method of the
			same
			Ihara, Hirohumi; Kaneko,
			Wakahiko; Iida, Takayas
TWI250349	TW	3/1/2006	Liquid crystal display device
(TW089112937)		(6/29/2000)	with less pixel error and
			method of manufacturing the
			same
			Ihara, Hiroshi; Kaneko,
			Wakahiko; Iida, Takayasu
7088401	US	8/8/2006	Liquid crystal display device
(09/609169)		(6/30/2000)	with less pixel error and
		·	method of manufacturing the
			same
			Ihara, Hirofumi; Kaneko,
			Wakahiko; Iida, Takayasu

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		Issue Date/	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
TWI190424	TW	11/11/2003	Thin-film transistor array and
(TW089102883)		(2/19/2000)	its manufacture
			Matsumoto, Seiichi
KR10-0399256	KR	9/15/2003	Thin-film transistor array and
(KR10-2000-0009205)		(2/24/2000)	its manufacture
			Matsumoto, Seiichi
6249011	US	6/19/2001	Thin film transistor array
(09/513925)	05	(2/28/2000)	with light shield layer
(0)/313/23/		(2/20/2000)	with fight shield layer
			Matsumoto, Seiichi
6352911	US	3/5/2002	Thin-film transistor array and
(09/619675)		(7/19/2000)	method for producing the
			same
			Matsumoto, Seiichi
KR10-0445776	KR	8/16/2004	Liquid crystal display and
(KR10-2000-0011930)		(3/10/2000)	manufacturing method
			thereof
			Kuroba, Shoichi
TWI205279	TW	6/21/2004	Liquid crystal display and
(TW089104423)	1 11	(3/10/2000)	manufacturing method
(11,003101125)		(5/10/2000)	thereof
			, moreon
			Kuroha, Shoichi
6760089	US	7/6/2004	Liquid crystal display and
(09/522609)		(3/10/2000)	manufacturing method
			thereof
			Kuroha, Shoichi
6954250	US	10/11/2005	Liquid crystal display and
(10/882286)		(7/2/2004)	manufacturing method
` '		(thereof
			Kuroha, Shoichi
TWI182529	TW	7/21/2003	LCD and manufacturing
(TW089105200)		(3/22/2000)	method thereof
			Motorwoon Himabit Him
			Matsuyama, Hiroaki; Hirai, Yoshihiko
			1 OSDIDIKO

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		<u>Issue Date/</u>	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
6714271	US	3/30/2004 (3/22/2000)	Liquid crystal display having
(09/533075)		(3/22/2000)	an in a pixel electrode along a boundary of differently
			oriented regions
			oriented regions
			Matsuyama, Hiroaki; Hirai, Yoshihiko
KR10-0358872	KR	10/16/2002	LCD and manufacturing
(KR10-2000-0014704)		(3/23/2000)	method thereof
		(5/25/2000)	
			Hirai, Yoshihiko;
			Matsuyama, Hiroaki
6809712	US	10/26/2004	Drive circuit of liquid crystal
(09/802215)		(3/8/2001)	display, having clip circuit
` ,			before polarity inversion
			circuit
			Takeda, Hiroshi
6778249	US	8/17/2004	Liquid-crystal display
(09/621460)		(7/21/2000)	element and method for
			manufacturing same
			Kamosawa, Hirofumi;
			Shimizu, Takeya
6690446	US	2/10/2004	Color liquid crystal display
(09/689901)		(10/13/2000)	panel, manufacturing method
			of the same, and liquid crystal
			display
			Okamoto, Mamoru;
			Yamamoto, Yuji; Sakamoto,
			Michiaki; Kikkawa, Hironori
6525786	US	2/25/2003	Transverse electric liquid
(09/711043)		(11/13/2000)	crystal display device
			0 01: :1:
TWI189333	TW	10/11/2002	Ono, Shin-ichirou
(TW089124340)	1 W	10/11/2003	Transverse electric liquid
(1 W 007124340)		(11/16/2000)	crystal display device
			Ono, Shin Ichirou
	·		

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		<u>Issue Date/</u>	
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
KR10-0400714	KR	9/24/2003	Transverse electric field
(KR10-2000-0068692)		(11/18/2000)	liquid crystal display device
			Ono, Shinichiro
7202844	US	4/10/2007	Liquid crystal display
(09/726721)		(11/30/2000)	controller and liquid crystal display
		-	Nakamigawa, Kazuhiro
6586873	US	7/1/2003	Display panel module with
(09/839118)		(4/23/2001)	improved bonding structure and method of forming the same
			Mizutani, Kazuhiro;
			Kitazume, Eiichi
KR10-0404064	KR	10/21/2003	Display panel module having
(KR10-2001-0021938)		(4/24/2001)	improved bonding structure and manufacturing method thereof
			Kitazume, Eiichi; Mizutani, Kazuhiro
6468840	US	10/22/2002	Active matrix substrate and
(09/841074)		(4/25/2001)	manufacturing method thereof
			Tanaka, Hiroaki; Uchida, Hiroyuki
6800872	US	10/5/2004	Active matrix thin film
(10/235493)		(9/6/2002)	transistor
			Tanaka, Hiroaki; Uchida,
	_		Hiroyuki
TWI178482	TW	5/21/2003	Active matrix substrate and
(TW090110057)		(4/26/2001)	manufacturing method
			thereof
			Tanaka, Hiroaki; Uchida, Hiroyuki

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		Issue Date/	·
Patent/Application Number	Country	Filing Date	Title of Patent and Inventors
KR10-0445791	KR	8/16/2004	Active matrix substrate and
(KR10-2001-0023218)		(4/28/2001)	manufacturing method thereof
			tnereor
			Tanaka, Hiroaki; Uchida,
			Hiroyuki
JP4410912	JP	11/20/2009	Electrostatic protection
(JP2000-170057)		(6/7/2000)	circuit
		2727222	Uchida, Hiroyuki
TWI195654	TW	2/1/2004	Surge protection circuit for
(TW090113333)		(6/1/2001)	semiconductor devices
			Uchida, Hiroyuki
KR10-0463411	KR	12/15/2004	Surge protection circuit for a
(KR10-2001-0031517)		(6/5/2001)	semiconductor display panel
			Uchida, Hiroyuki
6812528	US	11/2/2004	Surge protection circuit for
(09/874296)		(6/6/2001)	semiconductor devices
			Uchida, Hiroyuki
6621551	US	9/16/2003	Method and system for
(09/892836)		(6/27/2001)	fabricating a liquid crystal
			display by optically detecting
			anisotropic angular
			misalignment
			Metaygayya Todoski
TWI293397	TW	2/11/2008	Matsuzawa, Tadashi Liquid crystal display unit
(TW090116915)	1 44	(7/10/2001)	having pixel electrode
(1 11 0) 0 1 1 0) 1 3)		(7/10/2001)	encircled with partition wall
			and process for fabrication
			thereof
			Matsumoto, Kimikazu
6657699	US	12/2/2003	Liquid crystal display unit
(09/901034)		(7/10/2001)	having pixel electrode
			encircled with partition wall
-			and process for fabrication
		·	thereof
			Mataura to TZ' "
			Matsumoto, Kimikazu

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		<u> Issue Date/</u>	
Patent/Application Number	<u>Country</u>	Filing Date	Title of Patent and Inventors
6740596	US	5/25/2004	Manufacturing method of
(09/903244)		(7/11/2001)	active matrix substrate
			Hayase, Takasuke; Tanaka, Hiroaki; Kido, Shusaku; Harano, Toshihiko
6683661 (09/924059)	US	1/27/2004 (8/8/2001)	Liquid crystal display and image display device using the same
7518586	LIC		Matsumoto, Seiichi
(09/925601)	US	4/14/2009 (8/10/2001)	Method and circuit for driving liquid crystal display and image display device
			Hosoyamada, Shunichi
KR10-0485506 (KR10-2001-0048360)	KR	4/18/2005 (8/10/2001)	Liquid crystal display device and method of manufacturing thereof
			Kikkawa, Hironori; Maruyama, Muneo; Okamoto, Mamoru; Sakamoto, Michiaki; Yamamoto, Yuji
TWI202337 (TW090119732)	TW	5/11/2004 (8/10/2001)	Liquid crystal display device and method for manufacturing the same Kikkawa, Hironori; Muneo, Maruyama; Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki
6667777 (09/927501)	US	12/23/2003 (8/10/2001)	Liquid crystal display device and method for manufacturing the same Kikkawa, Hironori; Maruyama, Muneo; Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki

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75 / //A 75 // 157 N	~ .	Issue Date/	
Patent/Application Number KR10-0458994	Country KR	Filing Date 11/19/2004	Title of Patent and Inventors Liquid crystal display device
(KR10-2001-0055783)	N.K.	(9/11/2001)	and method for
(KK10-2001-0033183)		(9/11/2001)	manufacturing the same
			manufacturing the same
	,		Matsumoto, Kimikazu
TWI200887	TW	4/11/2004	Liquid crystal display and
(TW090122554)		(9/11/2001)	method of manufacturing
			same
			Matsumoto, Kimikazu
6873385	US	3/29/2005	Liquid crystal display with
(09/949633)		(9/12/2001)	selectively placed barriers
•			, ,
			Matsumoto, Kimikazu
JP4966444	JP	4/6/2012	TFT liquid crystal display
(JP2000-342844)		(11/10/2000)	device
·			
6021207	- TTG	10/1/10001	Tsubo, Yumiko
6831295	US	12/14/2004	TFT-LCD device having a
(10/008973)		(11/8/2001)	reduced feed-through voltage
			Tsubo, Yumiko
KR10-0535531	KR	12/2/2005	TFT liquid crystal display
(KR10-2001-0069662)		(11/9/2001)	device
			Tsubo, Yumiko
TWI185821	TW	9/1/2003	TFT-LCD device having a
(TW090127998)		(11/9/2001)	reduced feed-through voltage
6550017	7.70	5/6/2002	Tsubo, Yumiko
6559917	US	5/6/2003	Pattern formation method
(09/987088)		(11/13/2001)	using reflow enhancement
			layer and method for
			manufacturing reflective type liquid crystal display device
			using the same
			using the same
			Ikeno, Hidenori
6583556	US	6/24/2003	Flat-type fluorescent lamp for
(09/994807)		(11/28/2001)	illumination unit and liquid
			crystal device
			Otation, it makes
			Oishi, Toshiya; Fujishiro,
			Fumihiko; Kanatsu, Tsutomu

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
TWI285390	TW	8/11/2007	Flat-type fluorescent lamp for
(TW090129752)		(11/29/2001)	illumination unit and liquid crystal device
			Oishi, Toshiya; Fujishiro, Fumihiko; Kanatsu, Tsutomu
KR10-0485508 (KR10-2002-0007360)	KR	4/18/2005 (2/8/2002)	Liquid crystal display device and driving method thereof
			Yoshikawa, Fumitake
TWI189774 (TW091102723)	TW	11/1/2003 (2/8/2002)	Liquid crystal display device and driving method thereof
		N.	Yoshikawa, Fumitake
7033951 (10/077226)	US	4/25/2006 (2/15/2002)	Process for forming pattern and method for producing liquid crystal display apparatus
			Kido, Shusaku
7226865 (11/268811)	US	6/5/2007 (11/7/2005)	Process for forming pattern and method for producing liquid crystal display apparatus
			Kido, Shusaku
TWI200892 (TW091103514)	TW	4/11/2004 (2/26/2002)	Liquid crystal display with control electrodes for preventing lateral leak of electric field
			Sakamoto, Michiaki; Hidehira, Masanobu; Okamoto, Mamoru
KR10-0498254 (KR10-2002-0010678)	KR	6/21/2005 (2/27/2002)	Liquid crystal display with control electrodes for preventing lateral leak of electric field
			Hidehira, Masanobu; Okamoto, Mamoru; Sakamoto, Michiaki

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Dadam4/Amm16(* 37 I	a	Issue Date/	TOTAL OF A STATE OF THE STATE O
Patent/Application Number 7136116	Country US	Filing Date	Title of Patent and Inventors
(10/084355)	US	11/14/2006 (2/28/2002)	Liquid crystal display with control electrodes for
(10/004333)		(2/20/2002)	preventing lateral leak of
			electric field
			ciccure neid
			Sakamoto, Michiaki;
			Hidehira, Masanobu;
			Okamoto, Mamoru
TWI312884	TW	8/1/2009	Process for forming pattern
(TW091103004)		(2/21/2002)	
· · · · · · · · · · · · · · · · · · ·			Kido, Shusaku
6767694	US	7/27/2004	Process for forming pattern
(10/084848)		(2/25/2002)	and method for producing
			liquid crystal display
			apparatus employing process
			for forming pattern
			W. 1 Cl 1
KR10-0494431	VD.	6/1/2005	Kido, Shusaku
(KR10-2002-0011286)	KR	6/1/2005	Liquid crystal display
(KK10-2002-0011280)		(3/4/2002)	Kasuga, Koji
TWI300499	TW	9/1/2008	Lamp unit, light guide plate,
(TW091104220)	' ''	(3/5/2002)	liquid crystal display, and
(1770)110.220)		(3/3/2002)	method for assembling them
			medica for assembling them
	ļ		Kasuga, Koji
6894738	US	5/17/2005	Lamp unit, light guide plate,
(10/087787)		(3/5/2002)	liquid crystal display, and
			method for assembling them
CD TOT ON LILE OF O			Kasuga, Koji
CNZL02141106.9	CN	4/26/2006	Lamp unit, light conducting
(CN02141106.9)		(7/4/2002)	board, LCD and method for
			assembling same
			Kasuga, Yasuji
CNZL02127598.X	CN	7/6/2005	Liquid crystal display and
(CN02127598,X)		(8/2/2002)	manufacturing method
(21.0212,070,73)		(6/2/2002)	thereof
			moreor
			Okamoto, Mamoru;
			Sakamoto, Michiaki;
			Hidehira, Akinobu

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Patent/Application Number	Country	Issue Date/ Filing Date	Title of Patent and Inventors
6909418	US	6/21/2005	Image display apparatus
(10/119024)		(4/10/2002)	mage display apparates
		(Arai, Nobuhiro
JP4875248	JP	12/2/2011	Liquid crystal display device
(JP2001-117002)		(4/16/2001)	and control circuit
			Ito, Masaatsu; Takami,
			Kazuhiko; Okuzono, Noboru
7030852	US	4/18/2006	Liquid crystal display unit
(10/122240)		(4/16/2002)	having incoming pixel data
			rearrangement circuit
			7/ 3/ 1: 001
			Ito, Masahiro; Takami,
CNZL02140184.5	CN	12/3/2008	Kazuhiko; Okuzono, Noboru
(CN02140184.5)	CN	(7/3/2002)	LCD unit with input pixel data reregulating circuit
(CN02140104.3)		(11312002)	data reregulating circuit
			Ito, Masahiro; Takami,
			Iichihiko; Okuzo, Nobori
KR10-0512396	KR	8/29/2005	Liquid crystal display device
(KR10-2002-0020575)		(4/16/2002)	
			Kanatsu, Tsutomu; Ishida,
			Hiroshi
TWI182283	TW	7/21/2003	Liquid crystal display device
(TW091107717)		(4/16/2002)	
			Kanatsu, Tsutomu; Ishida,
6967925	LIC	2/15/2005	Hiroshi
6867825 (10/122242)	US	3/15/2005	Liquid crystal display device
(10/122242)		(4/16/2002)	Kanatsu, Tsutomu; Ishida,
			Hiroshi
7057678	US	6/6/2006	Liquid crystal display device
(10/123653)		(4/16/2002)	having backlight with return
Í			substrate above or below a
			portion of lamp tubes
		,	•
			Ishida, Hiroshi; Kanatsu,
			Tsutomu
6811458	US	11/2/2004	Method for manufacturing
(10/142287)		(5/9/2002)	liquid crystal display
			Kouya, Akiyoshi

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Patent/Application Number	Country	<u>Issue Date/</u> <u>Filing Date</u>	Title of Patent and Inventors
6707525	US	3/16/2004	Liquid crystal display device
(10/226268)		(8/23/2002)	quite organic display device
			Miyazaki, Tatsuya
KR10-0491324	KR	5/16/2005	Liquid crystal display
(KR10-2002-0051264)		(8/28/2002)	1 J J J J J J J J J J J J J J J J J J J
			Miyazaki, Tatsuya
TWI200737	TW	4/11/2004	Liquid crystal display device
(TW091119626)		(8/28/2002)	
			Miyazaki, Tatsuya
JP4771038	JP	7/1/2011	Liquid crystal display device
(JP2001-278590)		(9/13/2001)	
			Sato, Hiroki
KR10-0491325	KR	5/16/2005	Liquid crystal display
(KR10-2002-0055842)		(9/13/2002)	
			Satoh, Hiroki
TWI225565	TW	12/21/2004	Liquid crystal display (LCD)
(TW091120967)		(9/13/2002)	
		. <u> </u>	Sato, Hiroki
6870593	US	3/22/2005	Liquid crystal display cell
(10/242604)		(9/13/2002)	with improved spacer
			structure
			Satoh, Hiroki
TWI227361	TW	2/1/2005	Substrate for liquid-crystal
(TW092121767)	1 ,,,	(8/8/2003)	display device and fabrication
(,		(5/5/2005)	method thereof
			monod moreon
			Ishino, Takayuki
6982769	US	1/3/2006	Substrate for liquid-crystal
(10/638000)		(8/8/2003)	display device and fabrication
,			method thereof
			Ishino, Takayuki
KR10-0560587	KR	3/7/2006	Substrate for liquid-crystal
(KR10-2003-0055209)		(8/9/2003)	display device and fabrication
			method thereof
			Ishino, Takayuki
6567327	US	5/20/2003	Driving circuit,
(09/924609)		(8/9/2001)	charge/discharge circuit and
			the like
			·
			Tsuchi, Hiroshi

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Detent/Application Number	Country	Issue Date/	Title of Dedand and Lawrence
Patent/Application Number JP4491632	Country JP	Filing Date 4/16/2010	Title of Patent and Inventors Driving method for liquid
(JP2000-107245)	J1	(4/7/2000)	crystal display device
·			Hasegawa, Fumio; Aoki, Makoto
6831627 (09/826252)	US	12/14/2004 (4/4/2001)	Driving method for liquid crystal display
			Hasegawa, Fumio; Aoki, Makoto
6727874 (09/988189)	US	4/27/2004 (11/19/2001)	Driving circuit and driving method of color liquid crystal display, and color liquid crystal display device
			Okuzono, Noboru
7116297 (10/413458)	US	10/3/2006 (4/14/2003)	Liquid crystal display device and driving method for liquid crystal display device
			Koga, Koichi; Okuzono, Noboru; Yamaguchi, Machihiko
6747617 (09/714300)	US	6/8/2004 (11/16/2000)	Drive circuit for an organic EL apparatus
			Kawashima, Shingo
KR10-0454134 (KR10-2000-0068371)	KR	10/13/2004 (11/17/2000)	Organic EL driving circuit
			Kawashima, Shingo
TWI171629 (TW089103782)	TW	2/1/2003 (3/3/2000)	LCD panel and LCD device equipped therewith
			Nakajima, Keiichi
6587089	US	7/1/2003	LCD panel and LCD device
(09/518797)		(3/3/2000)	equipped therewith
			Nakajima, Keiichi
KR10-0330068	KR	3/13/2002	LCD panel and LCD device
(KR10-2000-0010871)	1	(3/4/2000)	equipped therewith
			Nakajima, Keiichi

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
JP3147098	JР	1/12/2001	Driving circuit of liquid
(JP10-227635)		(7/28/1998)	crystal display device
			Tsuchi, Hiroshi
KR10-0275651	KR	9/22/2000	Driver for liquid crystal
(KR10-1998-0030359)		(7/28/1998)	display apparatus with no
			operational amplifier
			Tsuchi, Hiroshi
6127997	US	10/3/2000	Driver for liquid crystal
(09/123482)		(7/28/1998)	display apparatus with no
			operational amplifier
			Tsuchi, Hiroshi
6380006	US	4/30/2002	Pattern formation method and
(09/878198)		(6/12/2001)	method of manufacturing
			display using it
			Kido, Shusaku
6513943	US	2/4/2003	Backlight unit and display
(09/810542)		(3/19/2001)	device using the same
			backlight unit
			Fukuyoshi, Hirokazu
TWI176526	TW	4/21/2003	Backlight unit and display
(TW090106549)		(3/20/2001)	device using the same
			backlight unit
			Fukuyoshi, Hirokazu
KR10-0380769	KR	4/4/2003	Back-face light source and
(KR10-2001-0015112)		(3/23/2001)	display using the same
			Fukuyoshi, Hirokazu
JP3353832	JP	9/27/2002	Method and apparatus for
(JP11-312090)		(11/2/1999)	manufacture of thin-film
			transistor
			Takechi, Kazue
6579749	US	6/17/2003	Fabrication method and
(09/440615)		(11/15/1999)	fabrication apparatus for thin
			film transistor
			Takechi, Kazushige

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Patent/Application Number	Country	<u>Issue Date/</u> Filing Date	Title of Patent and Inventors
KR10-0411442	KR	12/3/2003	Method for manufacturing
(KR10-1999-0050989)	IXIC	(11/17/1999)	thin film transistor and an
(11110 1555 0000505)		(11,17,1999)	apparatus for manufacturing
			the transistor
-			Daketzi, Gazsige
6693697	US	2/17/2004	Active-matrix type liquid
(10/042144)		(1/11/2002)	crystal display device having
			thick and thin overcoat layers
			layered over a black matrix
			Sakamoto, Michiaki;
			Yamamoto, Yuji; Okamoto,
			Mamoru; Kimura, Shigeru;
			Nakata, Shinichi; Kuroha,
			Shouichi; Hidehira,
			Masanobu; Horie, Yoshitaka;
TENTO 10 CT	CENTAL A	10/11/0005	Ishino, Takayuki
TWI291067	TW	12/11/2007	Fabrication method of liquid
(TW090111523)		(5/11/2001)	crystal display panel
			Sasaki, Takeshi
7145628	US	12/5/2006	Method for fabricating a
(09/855148)		(5/14/2001)	liquid crystal panel
			comprising spacers having an
			initial size larger than an
			appropriate cell gap
			Sasaki, Takeshi
7123314	US	10/17/2006	Thin-film transistor with set
(10/871000)		(6/21/2004)	trap level densities, and
	:		method of manufactures
			Matsunaga, Naoki; Sera,
			Kenji
6791656	US	9/14/2004	Liquid crystal display panel
(10/349937)		(1/24/2003)	avoiding display unevenness
			and manufacturing method
	,		Akiyoshi Kouya

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Tanna Data/

Revised Exhibit A

		<u> Issue Date/</u>	
Patent/Application Number	<u>Country</u>	Filing Date	Title of Patent and Inventors
TWI278697	TW	4/11/2007	Liquid crystal display panel
(TW089119815)		(9/26/2000)	avoiding display unevenness
			and manufacturing method
			Akiyoshi Kouya
TWI269918	TW	1/1/2007	LCD panel and method of
(TW089126848)		(12/14/2000)	fabricating same
			Takeshi Sasaki
6573972	US	6/3/2003	LCD panel and method of
(09/735806)		(12/14/2000)	fabricating same
			Takeshi Sasaki
JP5038560	JP	7/13/2012	Field-effect transistor,
(JP2001-233256)		(8/1/2001)	manufacturing method
			therefor, liquid crystal display
			device using the same and
			manufacturing method
			therefor
	!		Kenji Sera
6784456	US	8/31/2004	Field effect transistor as well
(10/207765)	ĺ	(7/31/2002)	as liquid crystal display using
,			the same
			Kenji Sera

- (b) any future reissues, reexaminations, extensions, continuations, continuing prosecution application, requests for continuing examinations, divisions, and registrations of any of the Patents:
- (c) rights to apply in any or all countries of the world for future patents, certificates of invention, utility models, industrial design protections, design patent protections, or other future governmental grants or issuances of any type related to the Patents; and
- (d) causes of action and enforcement rights of any kind under, or on account of, any of the Patents and/or any of the items described in either of the foregoing categories (b) or (c), including, without limitation, all causes of action, enforcement rights and all other rights to seek and obtain any other remedies of any kind for past, current and future infringement.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all future patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Assigned Patent Rights in

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PATENT REEL: 045469 FRAME: 0107

Exhibit A, Page 85 of 87

the name of Assignee, as the assignee to the entire interest therein. This Assignment of Patent Rights will inure for the benefit of any permitted successors or assigns of Assignee.

Assignor will, at the reasonable request of Assignee, take all reasonable steps necessary and proper, to confirm the assignment to Assignee of the Assigned Patent Rights pursuant to this Assignment of Patent Rights, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining and perfecting the Assigned Patent Rights.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed on FEBOMAY 13, 2018.

ASSIGNOR
Getner Foundation LLC
By: Name: HIM WEISFIELD Title: HINKIELZED PERSON
STATE OF Washington) iss.
COUNTY OF King)
On <u>February 13, 2018</u> , before me, <u>Jance L. Goe hel</u> , Notary Public in and for said State, personally appeared <u>Jim Weisfield</u> , personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.
WITNESS my hand and official seal. Signature Janice L. Joely (Seal) A Notary Public in and for the State of Washington COMMISSION EXPIRES My Compassion expires February 9, 2019

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ASSIGNEE

VISTA	PEAK VENTURES, LLC	
By:	Din (hul)	
Name:	DAUID PRIDHAM	
Title:	CEO	
	STATE OF Washington)	•
	COUNTY OF King)	
	On February 13,2018, before me, Unice L.E	ine be !
	Notary Public in and for said State, personally appeared Davie	
	personally known to me (or proved to me on the basis of satisfactor	ory evidence) to be the
	person whose name is subscribed to the within instrument and ack	•
	he/she executed the same in his/her authorized capacity, and that t	•
	the instrument the person, or the entity upon behalf of which the p	person acted, executed
	the instrument.	
	WITNESS my hand and official seal.	JANICE L. GOEBEL NOTARY PUBLIC
	Signature Lancie L. Doctor (Seal)	STATE OF WASHINGTON COMMISSION EXPIRES
	Notary Public in a for the State of Washington	FEBRUARY 9, 2019
	$1114 \times 1000 \times 1010 \times $. Martin Comment of the Comment of t

- 1 -

Exhibit B

PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
NEC Corporation	04/18/2011

RECEIVING PARTY DATA

Name:	Getner Foundation LLC
Street Address:	160 Greentree Drive, Suite 101
City:	Dover
State/Country:	DELAWARE
Postal Code:	19904

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	13111815

CORRESPONDENCE DATA

Fax Number: (216)696-8731

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Address Line 4: Cleveland, OHIO 44114

ATTORNEY DOCKET NUMBER:	VNECP281USA
NAME OF SUBMITTER:	Thomas Watson

Total Attachments: 8

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PATENT REEL: 026312 FRAME: 0209 DP \$40.00 13111

(US)

Assignment

Whereas, NEC Corporation, a Japanese corporation, having a place of business at 7-1, Shiba 5-chome, Minato-ku, Tokyo 108-8001, Japan, hereinafter called Assignor, is the owner and assignee of record of the patents and pending applications listed on the attached Schedule A; and

Whereas, Getner Foundation LLC, having a place of business at 160 Greentree Drive, Suite 101, Dover, DE, 19904, U.S.A, hereinafter called Assignee, desires to acquire the Assignor's entire right, title, and interest in said patents and pending applications listed on the attached Schedule A;

Now therefore, for valuable consideration, receipt whereof is hereby acknowledged;

Assignor hereby sells, assigns and transfers to Assignee, its successors and assigns, its entire right, title and interest including rights to recover for past infringement in:

- (a) the patents and pending applications listed on the attached Schedule A, (b) all continuations, divisions, reissues and continuations-in-part thereof and all other patent applications claiming priority rights from the items in category (a), and (c) all U.S. patents or other governmental grants or issuances that may be granted on any applications within the foregoing categories (a) and (b), the items in categories (a), (b), and (c) individually and collectively referred to hereafter as the "Patents";
- 2. all inventions claimed or described in the Patents (the "Inventions");
- 3. all non-United States patents, patent applications, and counterparts relating to any or all of the Inventions or the Patents, including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances ("Foreign Rights"), and including the right to file foreign applications directly in the name of Assignee, its successors and assigns;
- 4. the right to claim priority rights deriving from the Patents;
- 5. all causes of action and remedies related to the Patents, the Inventions, or Foreign Rights (including, without limitation, the right to sue for past, present, or future infringement, misappropriation or violation of rights related to any of the foregoing and the right to collect royalties and other payments under or on account of any of the foregoing); and

6. any and all other rights and interests arising out of, in connection with, or in relation to, the Patents, the Inventions, or Foreign Rights.

IN WITNESS WHEREOF, Assignor has executed this Assignment by proper persons duly authorized.

NEC Corporation

Date: April 18, 2011

Name: Kazuhiko Yamada Title: General Manager,

Intellectual Asset Development Promotion Division

Witness:

Date: April 18, 2011

Hiroyaki Ishii

Schedule A

US Patents

US Patents	
Application No.	Patent No.
09/711504	6730970
09/874296	6812528
10/084848	6767694
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US Patent Applications

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Application No.	Patent No.
12/381737	
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PATENT REEL: 026312 FRAME: 0217

Exhibit B, Page 9 of 9

Exhibit C

OP \$18560.00 10160071

PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
NEC Corporation	04/18/2011

RECEIVING PARTY DATA

Name:	Getner Foundation LLC
Street Address:	160 Greentree Drive, Suite 101
City:	Dover
State/Country:	DELAWARE
Postal Code:	19904

PROPERTY NUMBERS Total: 464

Property Type	Number
Application Number:	10160071
Application Number:	10035222
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Application Number:	10314299
Application Number:	10893298
Application Number:	09473274

CORRESPONDENCE DATA

Fax Number: (216)696-8731

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 216-696-8730

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Correspondent Name: Turocy & Watson, LLP

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Address Line 1: 127 Public Square Address Line 2: 57th Floor, Key Tower Address Line 4: Cleveland, OHIO 44114 ATTORNEY DOCKET NUMBER: **VNEC MATTERS** NAME OF SUBMITTER: Thomas E. Watson Total Attachments: 8 source=US NEC Getner Assignment#page1.tif source=US NEC Getner Assignment#page2.tif source=US NEC Getner Assignment#page3.tif source=US NEC Getner Assignment#page4.tif source=US NEC Getner Assignment#page5.tif source=US NEC Getner Assignment#page6.tif source=US NEC Getner Assignment#page7.tif source=US NEC Getner Assignment#page8.tif

(US)

Assignment

Whereas, NEC Corporation, a Japanese corporation, having a place of business at 7-1, Shiba 5-chome, Minato-ku, Tokyo 108-8001, Japan, hereinafter called Assignor, is the owner and assignee of record of the patents and pending applications listed on the attached Schedule A; and

Whereas, Getner Foundation LLC, having a place of business at 160 Greentree Drive, Suite 101, Dover, DE, 19904, U.S.A, hereinafter called Assignee, desires to acquire the Assignor's entire right, title, and interest in said patents and pending applications listed on the attached Schedule A;

Now therefore, for valuable consideration, receipt whereof is hereby acknowledged;

Assignor hereby sells, assigns and transfers to Assignee, its successors and assigns, its entire right, title and interest including rights to recover for past infringement in:

- (a) the patents and pending applications listed on the attached Schedule A, (b) all continuations, divisions, reissues and continuations-in-part thereof and all other patent applications claiming priority rights from the items in category (a), and (c) all U.S. patents or other governmental grants or issuances that may be granted on any applications within the foregoing categories (a) and (b), the items in categories (a), (b), and (c) individually and collectively referred to hereafter as the "Patents";
- 2. all inventions claimed or described in the Patents (the "Inventions");
- 3. all non-United States patents, patent applications, and counterparts relating to any or all of the Inventions or the Patents, including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances ("Foreign Rights"), and including the right to file foreign applications directly in the name of Assignee, its successors and assigns;
- 4. the right to claim priority rights deriving from the Patents;
- 5. all causes of action and remedies related to the Patents, the Inventions, or Foreign Rights (including, without limitation, the right to sue for past, present, or future infringement, misappropriation or violation of rights related to any of the foregoing and the right to collect royalties and other payments under or on account of any of the foregoing); and

6. any and all other rights and interests arising out of, in connection with, or in relation to, the Patents, the Inventions, or Foreign Rights.

IN WITNESS WHEREOF, Assignor has executed this Assignment by proper persons duly authorized.

NEC Corporation

Date: April 18, 2011

Name: Kazuhiko Yamada

Titie: General Manager, Intellectual Asset Development Promotion Division

Kingaki Johni Hiroyaki Ishii

Witness:

Date: April 18, 2011

Schedule A

US Patents

<u>US Patents</u>	
Application No.	Patent No.
09/711504	6730970
09/874296	6812528
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