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11 Attorneys for Plaintiff
12 AU Optronics Corporation America

13 UNITED STATES DISTRICT COURT
14
15 NORTHERN DISTRICT OF CALIFORNIA

16 AU OPTRONICS CORPORATION AMERICA,)
a California corporation,)

17)
18 Plaintiff,)

19 vs.)

20 VISTA PEAK VENTURES, LLC)

21 Defendant.)
22)

Case No.

**COMPLAINT FOR DECLARATORY
JUDGMENT AND EQUITABLE
RELIEF**

DEMAND FOR JURY TRIAL

23
24 Plaintiff AU OPTRONICS CORPORATION AMERICA, for its Complaint for Declaratory
25 Judgment and Equitable Relief, avers and alleges as follows:

26 THE PARTIES, JURISDICTION, AND VENUE

27 1. Plaintiff AU OPTRONICS CORPORATION AMERICA (“AUO USA”) is a
28 corporation organized and existing under the laws of the state of California with its principal place of

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1 business and center of operations located at 1525 McCarthy Blvd., Suite 218, Milpitas, California
2 95035.

3 2. Defendant VISTA PEAK VENTURES, LLC (“Defendant”) is a limited liability
4 company that purports to be organized and existing under the laws of the state of Texas with its
5 business address located at 1400 Preston Rd, Suite 472, Plano, TX 75093.

6 3. Defendant has filed three separate actions in the Marshall Division of the District
7 Court for the Eastern District of Texas accusing AUO USA’s parent company, AU Optronics
8 Corporation (“AUO”), of infringing the following United States Patents: U.S. Patent No. 5,929,947
9 (“the ’947 patent”), U.S. Patent No. 6,579,749 (“the ’749 patent”), U.S. Patent No. 6,674,093 (“the
10 ’093 patent”), U.S. Patent No. 6,800,872 (“the ’872 patent”), U.S. Patent No. 6,891,196 (“the ’196
11 patent”), U.S. Patent No. 7,088,401 (“the ’401 patent”), U.S. Patent No. 6,404,474 (“the ’474
12 patent”), U.S. Patent No. 6,657,699 (“the ’699 patent”), U.S. Patent No. 7,009,673 (“the ’673
13 patent”), U.S. Patent No. 7,499,119 (“the ’119 patent”), U.S. Patent No. 6,781,643 (“the ’643
14 patent”), U.S. Patent No. 7,046,327 (“the ’327 patent”), and U.S. Patent No. 6,730,970 (“the ’970
15 patent”), collectively, the “Asserted Patents.” *See Vista Peak Ventures, LLC v. AU Optronics Corp.*,
16 Case Nos. 2:18-cv-00276-JRG, ¶¶ 10, 22, 34, 46, 57, 68, 80; 2:18-cv-00278-JRG, ¶¶ 10, 33, 44, 45,
17 56; and 2:18-cv-00279-JRG, ¶¶ 10, 34, 45.

18 4. In an effort to manufacture personal jurisdiction over AUO, Defendant further alleges
19 in each of these complaints that AUO controls AUO USA and that AUO USA “has committed acts
20 within Texas giving rise to this action and/or has established minimum contacts with Texas such that
21 personal jurisdiction over AUO would not offend traditional notions of fair play and substantial
22 justice.” *See Vista Peak Ventures, LLC v. AU Optronics Corp.*, Case Nos. 2:18-cv-00276-JRG, ¶¶ 8-
23 9; 2:18-cv-00278-JRG, ¶¶ 8-9; and 2:18-cv-00279-JRG, ¶¶ 8-9.

24 5. In *ad damnum* clause in each of its complaints, Defendant requests the court, *inter*
25 *alia*, to enjoin AUO USA from “making, using, selling, offering to sell, or importing any products
26 that infringe the Asserted Patents, and any other injunctive relief the Court deems just and
27 equitable.” *See Vista Peak Ventures, LLC v. AU Optronics Corp.*, Case Nos. 2:18-cv-00276-JRG,
28 pp. 28-29; 2:18-cv-00278-JRG, pp. 22; and 2:18-cv-00279-JRG, pp. 19-20.

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1 6. Consequently, this action arises under the patent laws of the United States, namely 35
2 U.S.C. §§ 271, 281, and 284-285, among others.

3 7. Jurisdiction: This Court has subject matter jurisdiction pursuant to 28 U.S.C. §§ 1331
4 and 1338(a).

5 8. Intradistrict Assignment: Pursuant to Civil Local Rule 3-5(b), this case is properly
6 assigned to the San Jose division.

7 9. Defendant is subject to this Court’s specific personal jurisdiction pursuant to due
8 process and/or the California Long Arm Statute, due at least to Defendant’s substantial business in
9 this State and judicial district, including its engagement of Ascenda Law Group of 333 W. San
10 Carlos Street, Suite 200, San Jose, California 95110 as counsel to acquire the Asserted Patents, *see*
11 Assignment Reel/Frame No. 045469/0023 from Getner Foundation LLC to Vista Peak Ventures,
12 LLC (attached hereto as Exhibit A), and, upon information and belief, authorizing that Firm’s filing
13 of powers of attorney to act on behalf of Defendant before the United States Patent and Trademark
14 Office with respect to at least some of the Asserted Patents after their acquisition.

15 10. Defendant is also subject to this Court’s specific personal jurisdiction pursuant to due
16 process and/or the California Long Arm Statute due at least to Defendant’s targeting of specific
17 residents of this State and judicial district, including AUO USA, against whom Defendant seeks
18 injunctive relief in its complaints filed in the Eastern District of Texas based upon its assertions that
19 AUO USA both infringes the Asserted Patents and aids in AUO’s alleged infringement of the
20 Asserted Patents. *See Vista Peak Ventures, LLC v. AU Optronics Corp.*, Case Nos. 2:18-cv-00276-
21 JRG, ¶¶ 8-9; 2:18-cv-00278-JRG, ¶¶ 8-9; and 2:18-cv-00279-JRG, ¶¶ 8-9. Had Defendant included
22 AUO USA as a party defendant in its complaints against AUO, 28 U.S.C. §§ 1391(c)(3) and 1400(b)
23 would require those complaints to have been filed in this District. Defendant’s failure to name AUO
24 USA as a party defendant in its complaints notwithstanding Defendant’s claim for injunctive relief
25 against AUO USA for alleged infringement of the Asserted Patents is tantamount to depriving AUO
26 USA of its procedural rights to defend such claims in the legally appropriate venue, namely, this
27 District.

28 11. Venue is appropriate pursuant to 28 U.S.C. § 1391(b) because this District is the

1 location where a substantial portion of the events at issue in this suit occurred.

2 FACTUAL BACKGROUND

3 12. Each of the Asserted Patents was issued between July 27, 1999, and March 3, 2009,
4 to NEC Corporation or its affiliates (collectively, “NEC”).

5 13. Since at least 2001, AUO and AUO USA have been and continue to be engaged in
6 the business of thin film transistor liquid crystal displays (“TFT-LCDs”) and other flat panel displays
7 used in a wide variety of applications.

8 14. Since at least 2003, NEC has been a customer of AUO, purchasing its TFT-LCD
9 display products. *See* https://www.auo.com/en-global/New_Archive/detail/news_Product_20030120.

10 15. Upon information and belief, NEC has been aware of the TFT-LCD technology of
11 AUO that Defendant accuses of infringement in its complaints since at least 2003.

12 16. NEC assigned the Asserted Patents to Getner Foundation LLC (“Getner”) in April
13 2011. *See* Assignment Reel Frame No. 026312/0213 from NEC Corporation to Getner Foundation
14 LLC (attached hereto as Exhibit B) and Assignment Reel Frame No. 026254/0400 from NEC
15 Corporation to Getner Foundation LLC (attached hereto as Exhibit C). Getner assigned the Asserted
16 Patents to Defendant in February 2018. *See* Exhibit A.

17 17. For each Asserted Patent, Defendant has accused AUO of patent infringement under
18 35 U.S.C. § 271(a) by “making, offering for sale, selling, and/or importing those TFT-LCD panels,
19 their components, and/or products containing same that incorporate the fundamental technologies
20 covered by [one of the Asserted Patents], or by having [AUO USA] do the same.” *See Vista Peak*
21 *Ventures, LLC v. AU Optronics Corp.*, Case Nos. 2:18-cv-00276-JRG, ¶¶ 24, 36, 48, 59, 70, 82;
22 2:18-cv-00278-JRG, ¶¶ 24, 35, 47, 58; and 2:18-cv-00279-JRG, ¶¶ 25, 36, 47.

23 18. Defendant is seeking from the District Court in the Eastern District of Texas:
24 (i) damages sustained as a result of the alleged infringements, including up to treble damages; and
25 (ii) “[a] preliminary and permanent injunction against AUO, [AUO USA], or anyone acting on its
26 behalf from making, using, selling, offering to sell, or importing any products that infringe the
27 Asserted Patents, and any other injunctive relief the Court deems just and equitable... .” *See Vista*
28

1 *Peak Ventures, LLC v. AU Optronics Corp.*, Case Nos. 2:18-cv-00276-JRG, ¶ 93; 2:18-cv-00278-
2 JRG, ¶69; and 2:18-cv-00279-JRG, ¶ 57.

3 FIRST CAUSE OF ACTION:

4 DECLARATORY JUDGMENT OF

5 NON-INFRINGEMENT OF EACH OF THE ASSERTED PATENTS

6 19. AUO USA hereby incorporates by reference Paragraphs 1 through 18, above, as if
7 fully set forth herein.

8 20. On July 10, 2018, AUO USA received notice of three separate lawsuits Defendant
9 filed against AUO in which Defendant, *inter alia*, also accused AUO USA of engaging in conduct
10 constituting direct and indirect infringement of the Asserted Patents in the United States and aiding
11 AUO in such infringement.

12 21. AUO USA denies that it has directly infringed any valid claim of the Asserted Patents
13 and further denies that it has induced the infringement or contributed to the infringement of any valid
14 claim of the Asserted Patents, either individually or in concert with AUO.

15 22. By virtue of the foregoing, an actual and justiciable controversy has arisen and now
16 exists between AUO USA and Defendant, within the jurisdiction of the Court, regarding whether
17 AUO USA infringes one or more of the Asserted Patents.

18 23. Declaratory relief is both appropriate and necessary in light of the conflicting
19 positions of the parties. AUO USA desires a judicial determination of the parties' respective rights
20 and obligations in connection with each of the Asserted Patents.

21 24. For the reasons set forth above, AUO USA respectfully requests that this Court
22 declare that, for each claim in the Asserted Patents for which Defendant claims infringement by
23 AUO USA, AUO USA has not directly infringed each one of the Asserted Patents, either literally or
24 under the doctrine of equivalents, and has neither induced the infringement nor contributed to the
25 infringement, of such claims either individually or in concert with AUO. Specifically, AUO USA
26 respectfully requests that this Court issue a judicial declaration to the effect that (*inter alia*, without
27 limitation) AUO USA's:

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- Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '947 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '749 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '093 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '872 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '196 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '401 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '474 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '699 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '673 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '119 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '643 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '327 patent, either literally or under the doctrine of equivalents;
 - Use, importation, sales, and offers to sell TFT-LCD panels does not directly or indirectly infringe the '970 patent, either literally or under the doctrine of equivalents;
- and

- Actions do not constitute inducement of infringement or contributory infringement of any claim in the Asserted Patents.

SECOND CAUSE OF ACTION:

DECLARATORY JUDGMENT OF

NO WILLFUL INFRINGEMENT OF EACH OF THE ASSERTED PATENTS

25. AUO USA hereby incorporates by reference Paragraphs 1 through 18, above, as if fully set forth herein.

26. On July 10, 2018, AUO USA received notice of three separate lawsuits Defendant filed against AUO in which Defendant, *inter alia*, accused AUO USA of engaging in alleged willful infringement of the Asserted Patents in the United States. *Vista Peak Ventures, LLC v. AU Optronics Corp.*, Case Nos. 2:18-cv-00276-JRG, ¶¶ 29, 41, 52, 63, 75, 87; 2:18-cv-00278-JRG, ¶¶ 28, 40, 51, 63; and 2:18-cv-00279-JRG, ¶¶ 29, 40, 51.

27. AUO USA denies that it infringes, directly or indirectly, any valid claim of the Asserted Patents.

28. By virtue of the foregoing, an actual and justiciable controversy has arisen and now exists between AUO USA and Defendant, within the jurisdiction of the Court, regarding whether AUO USA engaged in any course of conduct or possessed the requisite intent to willfully infringe one or more of the Asserted Patents.

29. Declaratory relief is both appropriate and necessary in light of the conflicting positions of the parties. AUO USA desires a judicial determination of the parties' respective rights and obligations in connection with each of the Asserted Patents.

30. For the reasons set forth above, AUO USA respectfully requests that this Court declare that, for each claim in the Asserted Patents for which Defendant claims infringement by AUO USA, AUO USA does not willfully infringe such claims. Specifically, AUO USA respectfully requests that this Court issue a judicial declaration to the effect that (*inter alia*, without limitation) AUO USA's:

- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '947 patent;

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- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '749 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '093 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '872 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '196 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '401 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '474 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '699 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '673 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '119 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '643 patent;
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '327 patent; and
- Use, importation, sales, and offers to sell TFT-LCD panels does not willfully infringe the '970 patent.

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1 THIRD CAUSE OF ACTION:

2 DEFENDANT’S RELIEF UNDER THE ASSERTED PATENTS

3 IS BARRED BY EQUITABLE ESTOPPEL

4 31. AUO USA hereby incorporates by reference Paragraphs 1 through 18, above, as if
5 fully set forth herein.

6 32. The Asserted Patents issued to NEC before and after 2003.

7 33. Upon information and belief, NEC has been aware of various AUO display
8 technologies since at least 2003 and aware of the TFT-LCD technology accused of infringement by
9 Defendant in its complaints since around that same time.

10 34. Neither AUO nor AUO USA has been accused of infringing the Asserted Patents
11 until February 2018, years after the time they began selling the products accused of infringing those
12 patents and years after NEC, the original owner of the Asserted Patents, had been made aware of the
13 accused AUO display technologies.

14 35. As a result of the inexcusable delay in bringing suit against AUO and AUO USA for
15 alleged infringement of the Asserted Patents, and AUO USA’s detrimental reliance on NEC having
16 not brought any infringement action for the Asserted Patents, Defendant should be estopped from
17 obtaining relief for any alleged patent infringement against AUO USA.

18 36. Under the circumstances, it would be against equity and good conscience to permit
19 Defendant to obtain any relief against AUO USA under the Asserted Patents, including any damages
20 or injunctions based on AUO USA’s alleged infringements of the same.

21 PRAYER FOR RELIEF

22 WHEREFORE, AUO USA prays that the Court:

23 1. Determine and declare the parties’ respective rights and obligations under the
24 Asserted Patents.

25 2. Find and declare that AUO USA does not infringe each of the Asserted Patents,
26 directly or indirectly, literally or under the doctrine of equivalents, and in no event willfully.

27 3. Find and declare that Defendant is equitably estopped from any relief against AUO
28 USA under the Asserted Patents.

1 4. Award AUO USA its reasonable attorney fees and costs incurred in this matter.

2 5. Award AUO USA such other and further relief as the Court deems just and proper,
3 premises considered.

4
5 Dated: August 1, 2018

Respectfully submitted,
LOCKE LORD LLP

6
7 By: /s/ Regina J. McClendon
Regina J. McClendon

8 Attorneys for Plaintiff
9 AU OPTRONICS CORPORATION AMERICA

10
11
12 **DEMAND FOR JURY TRIAL**

13 Pursuant to Fed. R. Civ. P. 38, Plaintiff AU Optronics Corporation America hereby
14 demands a trial by jury.

15 Dated: August 1, 2018

LOCKE LORD LLP

16
17 By: /s/ Regina J. McClendon

18 Attorneys for Plaintiff
19 AU OPTRONICS CORPORATION AMERICA

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Exhibit A

PATENT ASSIGNMENT COVER SHEET

Electronic Version v1.1
 Stylesheet Version v1.2

EPAS ID: PAT4843891

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
GETNER FOUNDATION LLC	02/13/2018

RECEIVING PARTY DATA

Name:	VISTA PEAK VENTURES, LLC
Street Address:	1400 PRESTON ROAD
Internal Address:	SUITE 472
City:	PLANO
State/Country:	TEXAS
Postal Code:	75201

PROPERTY NUMBERS Total: 250

Property Type	Number
Patent Number:	6566154
Patent Number:	6756187
Patent Number:	7214473
Patent Number:	6704076
Patent Number:	7006065
Patent Number:	7671829
Patent Number:	7598939
Patent Number:	8599125
Patent Number:	9111495
Patent Number:	7589383
Patent Number:	7005916
Patent Number:	7586504
Patent Number:	8471794
Patent Number:	6023314
Patent Number:	6055324
Patent Number:	6005653
Patent Number:	6310668
Patent Number:	6022110
Patent Number:	6031513

504797159

**PATENT
 REEL: 045469 FRAME: 0023**

Property Type	Number
Patent Number:	6073369
Patent Number:	6717630
Patent Number:	6750895
Patent Number:	6824285
Patent Number:	6930665
Patent Number:	6734943
Patent Number:	7027121
Patent Number:	7218368
Patent Number:	6011860
Patent Number:	6573970
Patent Number:	7146076
Patent Number:	7354810
Patent Number:	7193678
Patent Number:	7468765
Patent Number:	7355323
Patent Number:	7626648
Patent Number:	7388625
Patent Number:	6781643
Patent Number:	6445431
Patent Number:	6501520
Patent Number:	6822263
Patent Number:	7119363
Patent Number:	7585708
Patent Number:	6800541
Patent Number:	6674093
Patent Number:	6891196
Patent Number:	7223622
Patent Number:	6512246
Patent Number:	6667188
Patent Number:	6797535
Patent Number:	6583456
Patent Number:	6429916
Patent Number:	6398610
Patent Number:	7012029
Patent Number:	7554207
Patent Number:	6734460
Patent Number:	6894311
Patent Number:	7037766

PATENT
REEL: 045469 FRAME: 0024

Property Type	Number
Patent Number:	6632696
Patent Number:	6649226
Patent Number:	6781647
Patent Number:	7009673
Patent Number:	7492431
Patent Number:	6881894
Patent Number:	7271041
Patent Number:	7012849
Patent Number:	7665816
Patent Number:	7109554
Patent Number:	7105905
Patent Number:	7369725
Patent Number:	7656472
Patent Number:	7212722
Patent Number:	7122444
Patent Number:	7579222
Patent Number:	7373834
Patent Number:	6404474
Patent Number:	6594143
Patent Number:	6853407
Patent Number:	6617263
Patent Number:	7408135
Patent Number:	6885439
Patent Number:	7301588
Patent Number:	7593070
Patent Number:	6950165
Patent Number:	7476470
Patent Number:	7151516
Patent Number:	6840647
Patent Number:	6816140
Patent Number:	6815269
Patent Number:	7199780
Patent Number:	6967702
Patent Number:	7071040
Patent Number:	7106394
Patent Number:	7050020
Patent Number:	7148871
Patent Number:	7046327

PATENT
REEL: 045469 FRAME: 0025

Property Type	Number
Patent Number:	7057340
Patent Number:	7310127
Patent Number:	7554162
Patent Number:	7242450
Patent Number:	7090365
Patent Number:	7511301
Patent Number:	7280178
Patent Number:	7294881
Patent Number:	7160003
Patent Number:	7339422
Patent Number:	7183635
Patent Number:	7405148
Patent Number:	7483109
Patent Number:	7489295
Patent Number:	7345432
Patent Number:	7525618
Patent Number:	7350956
Patent Number:	7473657
Patent Number:	7372211
Patent Number:	7564531
Patent Number:	7551340
Patent Number:	7446825
Patent Number:	7499119
Patent Number:	RE44063
Patent Number:	7520655
Patent Number:	RE45229
Patent Number:	RE44118
Patent Number:	7568574
Patent Number:	7491079
Patent Number:	7518690
Patent Number:	6611092
Patent Number:	6864947
Patent Number:	6184966
Patent Number:	6323931
Patent Number:	6683662
Patent Number:	6064458
Patent Number:	6262436
Patent Number:	6051815

PATENT
REEL: 045469 FRAME: 0026

Property Type	Number
Patent Number:	6078375
Patent Number:	6104463
Patent Number:	7525732
Patent Number:	6172730
Patent Number:	6480254
Patent Number:	6624669
Patent Number:	6618204
Patent Number:	6618027
Patent Number:	6707107
Patent Number:	7060623
Patent Number:	6953976
Patent Number:	6977422
Patent Number:	7030467
Patent Number:	6949766
Patent Number:	7554164
Patent Number:	6791145
Patent Number:	6730970
Patent Number:	7049184
Patent Number:	8377805
Patent Number:	8710507
Patent Number:	7138303
Patent Number:	7285809
Patent Number:	6665023
Patent Number:	6020214
Patent Number:	6137465
Patent Number:	7079101
Patent Number:	6567136
Patent Number:	6597414
Patent Number:	6300996
Patent Number:	5875009
Patent Number:	6268898
Patent Number:	5929947
Patent Number:	6441877
Patent Number:	6407728
Patent Number:	6563481
Patent Number:	6633002
Patent Number:	6876352
Patent Number:	6657619

PATENT
REEL: 045469 FRAME: 0027

Property Type	Number
Patent Number:	6580486
Patent Number:	6669809
Patent Number:	6646691
Patent Number:	6469754
Patent Number:	6429910
Patent Number:	6788355
Patent Number:	6661401
Patent Number:	6600172
Patent Number:	6407730
Patent Number:	6437370
Patent Number:	6429917
Patent Number:	6330043
Patent Number:	6486936
Patent Number:	6870592
Patent Number:	6549259
Patent Number:	6822704
Patent Number:	7224414
Patent Number:	7136123
Patent Number:	6930744
Patent Number:	6608664
Patent Number:	6760081
Patent Number:	6414740
Patent Number:	6633361
Patent Number:	6486931
Patent Number:	6542207
Patent Number:	7088401
Patent Number:	6249011
Patent Number:	6352911
Patent Number:	6760089
Patent Number:	6954250
Patent Number:	6714271
Patent Number:	6809712
Patent Number:	6778249
Patent Number:	6690446
Patent Number:	6525786
Patent Number:	7202844
Patent Number:	6586873
Patent Number:	6468840

PATENT
REEL: 045469 FRAME: 0028

Property Type	Number
Patent Number:	6800872
Patent Number:	6812528
Patent Number:	6621551
Patent Number:	6657699
Patent Number:	6740596
Patent Number:	6683661
Patent Number:	7518586
Patent Number:	6667777
Patent Number:	6873385
Patent Number:	6831295
Patent Number:	6559917
Patent Number:	6583556
Patent Number:	7033951
Patent Number:	7226865
Patent Number:	7136116
Patent Number:	6767694
Patent Number:	6894738
Patent Number:	6909418
Patent Number:	7030852
Patent Number:	6867825
Patent Number:	7057678
Patent Number:	6811458
Patent Number:	6707525
Patent Number:	6870593
Patent Number:	6982769
Patent Number:	6567327
Patent Number:	6831627
Patent Number:	6727874
Patent Number:	7116297
Patent Number:	6747617
Patent Number:	6587089
Patent Number:	6127997
Patent Number:	6380006
Patent Number:	6513943
Patent Number:	6579749
Patent Number:	6693697
Patent Number:	7145628
Patent Number:	7123314

PATENT
REEL: 045469 FRAME: 0029

Property Type	Number
Patent Number:	6791656
Patent Number:	6573972
Patent Number:	6784456

CORRESPONDENCE DATA

Fax Number: (408)773-6177

Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.

Phone: 866-877-4883

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DATE SIGNED: 02/28/2018

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Revised Exhibit A

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Getner Foundation LLC, a Delaware limited liability company with an address at 251 Little Falls Drive, Wilmington, Delaware 19808 (“*Assignor*”), does hereby sell, assign, transfer, and convey unto Vista Peak Ventures, LLC, a Texas limited liability company with an address at 1400 Preston Road, Suite 472, Plano, TX 75201 (“*Assignee*”), all of Assignor's right, title, and interest in and to the following (collectively, the “*Assigned Patent Rights*”):

(a) the patents and patent applications listed in the table below (the “*Patents*”):

Patent/Application Number	Country	Issue Date/ Filing Date	Title of Patent and Inventors
TWI304501 (TW091111212)	TW	12/21/2008 (5/27/2002)	Method for manufacturing liquid crystal display device Yamamoto, Yuji
6566154 (10/160071)	US	5/20/2003 (6/4/2002)	Method for manufacturing liquid crystal display device Yamamoto, Yuji
JP3522249 (JP2001-326584)	JP	2/20/2004 (10/24/2001)	Method for removing organic film Kido, Shusaku
6756187 (10/035222)	US	6/29/2004 (1/4/2002)	Method for removing patterned layer from lower layer through reflow Kido, Shusaku
7214473 (10/773272)	US	5/8/2007 (2/9/2004)	Method for removing patterned layer from lower layer through reflow Kido, Shusaku
TWI287178 (TW090133502)	TW	9/21/2007 (12/31/2001)	Method for removing patterned layer from lower layer through reflow Kido, Shusaku
6704076 (10/173518)	US	3/9/2004 (6/17/2002)	Method for fabricating a liquid crystal display device Okamoto, Mamoru; Sakamoto, Michiaki; Kikkawa, Hironori

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Patent/Application Number	Country	Issue Date/ Filing Date	Title of Patent and Inventors
CNZL02141283.9 (CN02141283.9)	CN	6/21/2006 (7/5/2002)	Method for making a LCD device Okamoto, Mamoru; Sakamoto, Michiaki; Kikkawa, Hironori
JP3412583 (JP11-316873)	JP	3/28/2003 (11/8/1999)	Method and circuit for driving color liquid crystal display Sugawara, Noriaki; Koga, Kouichi
7006065 (09/707816)	US	2/28/2006 (11/7/2000)	Gamma compensation method and circuit for color liquid crystal display Sugawara, Noriaki; Koga, Kouichi
7671829 (11/243626)	US	3/2/2010 (10/5/2005)	Driving method and driving circuit for color liquid crystal display Sugawara, Noriaki; Koga, Kouichi
KR10-0367418 (KR10-2000-0066269)	KR	12/24/2002 (11/8/2000)	Method and circuit for driving color liquid crystal display Koga, Kouichi; Sugawara, Noriaki
TWI156469 (TW089123670)	TW	5/21/2002 (11/8/2000)	Driving method and driving circuit for color liquid crystal Sugawara, Noriaki; Koga, Kouichi
JP4127305 (JP2006-259148)	JP	5/23/2008 (9/25/2006)	Liquid crystal display device, backlight, and driving method and manufacturing method thereof Sumiyoshi, Ken; Yoshioka, Toshihiro; Hayama, Hiroshi; Matsushima, Hiroshi

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Patent/Application Number	Country	Issue Date/ Filing Date	Title of Patent and Inventors
JP4870652 (JP2007-312459)	JP	11/25/2011 (3/12/2007)	Liquid crystal display device, backlight and its driving method and manufacturing method Sumiyoshi, Ken; Yoshioka, Toshihiro; Hayama, Hiroshi; Matsushima, Hiroshi
7598939 (11/272663)	US	10/6/2009 (11/14/2005)	Liquid crystal display device, backlight used for same display device, method for driving same backlight and method for manufacturing same backlight Sumiyoshi, Ken; Yoshioka, Toshihiro; Hayama, Hiroshi; Matsushima, Jin
8599125 (12/381737)	US	12/3/2013 (3/16/2009)	Liquid crystal display device, backlight used for same display device, method for driving same backlight and method for manufacturing same backlight Sumiyoshi, Ken; Yoshioka, Toshihiro; Hayama, Hiroshi; Matsushima, Jin
9111495 (14/066560)	US	8/18/2015 (10/29/2013)	Liquid crystal display device, backlight used for same display device, method for driving same backlight and method for manufacturing same backlight Sumiyoshi, Ken; Yoshioka, Toshihiro; Hayama, Hiroshi; Matsushima, Jin
7589383 (11/441213)	US	9/15/2009 (5/26/2006)	Thin film semiconductor device and method of manufacturing the same Yanase, Jirou

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
JP5559244 (JP2012-119817)	JP	6/13/2014 (5/25/2012)	Thin-film semiconductor device and manufacturing method thereof Kanji Yanase
JP4487488 (JP2003-033071)	JP	4/9/2010 (2/12/2003)	Circuit of driving display device, its control method, portable telephone and portable electronic apparatus Nakahira, Yoshihiko; Tsuchi, Hiroshi
7005916 (10/358245)	US	2/28/2006 (2/5/2003)	Amplifier circuit, driving circuit of display apparatus, portable telephone and portable electronic apparatus Nakahira, Yoshihiko; Tsuchi, Hiroshi
7586504 (11/294605)	US	9/8/2009 (12/6/2005)	Amplifier circuit, driving circuit of display apparatus, portable telephone and portable electronic apparatus Nakahira, Yoshihiko; Tsuchi, Hiroshi
8471794 (12/504981)	US	6/25/2013 (7/17/2009)	Driving circuit for display apparatus, and method for controlling same Nakahira, Yoshihiko; Tsuchi, Hiroshi
CNZL03103193.5 (CN03103193.5)	CN	4/26/2006 (2/8/2003)	Amplifier of display device, drive circuit, mobile phone and portable electronic apparatus Nakadaira, Yoshihiko; Tsuchi, Hiroo
6023314 (09/088731)	US	2/8/2000 (6/2/1998)	Elliptically polarizing plate and liquid crystal display device using same Sumiyoshi, Ken

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
6055324 (09/103502)	US	4/25/2000 (6/24/1998)	Fingerprint image entry device of electrostatic capacitance sensing type Fujieda, Ichiro
6005653 (09/169134)	US	12/21/1999 (10/9/1998)	Method and apparatus for sealing liquid crystal display element cell employing seal members with Z-shaped cross sections that form pressure regions Matsuzawa, Tadashi
JP3036513 (JP10-162691)	JP	2/25/2000 (6/10/1998)	Liquid crystal display device Ukita, Toru
6310668 (09/329559)	US	10/30/2001 (6/10/1999)	LCD wherein opening in source electrode overlaps gate electrode to compensate variations in parasitic capacitance Ukita, Tooru
6022110 (09/115669)	US	2/8/2000 (7/14/1998)	Projection color liquid crystal display apparatus Tsujiikawa, Susumu
6031513 (09/019162)	US	2/29/2000 (2/6/1998)	Liquid crystal display Ikeda, Naoyasu
6073369 (09/049150)	US	6/13/2000 (3/27/1998)	Substrate drying apparatus and method Yasuyuki, Satou
JP3125766 (JP10-271140)	JP	11/2/2000 (9/25/1998)	Liquid crystal display device and its manufacture Ikeda, Munehiro
6717630 (09/404705)	US	4/6/2004 (9/23/1999)	Liquid crystal display device and method of fabricating the same Ikeda, Munehiro

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
6750895 (09/670596)	US	6/15/2004 (9/29/2000)	Optical printer head with integrated drive circuitry Otose, Tomohiko; Asada, Hideki; Uezono, Tsutomu; Oda, Atsushi; Toguchi, Satoru
JP3801032 (JP2001-365327)	JP	5/12/2006 (11/29/2001)	Light source and liquid crystal display using the light source Saito, Goro; Yagi, Toshie; Mimura, Hiroji; Sumiyoshi, Ken
6824285 (10/277930)	US	11/30/2004 (10/23/2002)	Light source and liquid crystal display device using this light source Saitoh, Goroh; Yagi, Yoshie; Mimura, Koji; Sumiyoshi, Ken
6930665 (10/353951)	US	8/16/2005 (1/30/2003)	Display device for D/A conversion using load capacitances of two signal lines Sekine, Hiroyuki
JP3937304 (JP2002-003920)	JP	4/6/2007 (1/10/2002)	Liquid crystal display panel and its manufacturing method Noiri, Yoshikazu
6734943 (10/338829)	US	5/11/2004 (1/9/2003)	Liquid crystal display panel with sealing member on second partition wall and manufacturing method thereof Noiri, Yoshikazu

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
7027121 (10/620125)	US	4/11/2006 (7/15/2003)	Method for manufacturing liquid crystal display device having the first substrate and support substrate pressed against each other under vacuum condition without use of an adhesive Horiuchi, Toshihiro; Noiri, Yoshikazu; Yoshizoe, Hidefumi; Ueebisu, Shinji; Mukai, Toshiaki
7218368 (10/990892)	US	5/15/2007 (11/17/2004)	Rubbing roll orientation processor for liquid crystal display manufacturing Mori, Makoto
6011860 (09/020908)	US	1/4/2000 (2/9/1998)	Small reliable image input apparatus incorporated in finger-print collation system of personal identification Fujieda, Ichiro; Suga, Michihisa
6573970 (09/617040)	US	6/3/2003 (7/14/2000)	Liquid crystal display device Saitoh, Goroh; Takatori, Ken-ichi
JP4442061 (JP2001-229082)	JP	1/22/2010 (7/30/2001)	Diffraction optical element and optical device using the same Fujieda, Ichiro
7146076 (10/201133)	US	12/5/2006 (7/24/2002)	Diffraction optical element and optical device using the same Fujieda, Ichiro
JP5318308 (JP2001-247238)	JP	7/19/2013 (8/16/2001)	Semiconductor substrate production system Tanabe, Hiroshi

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
7354810 (10/856275)	US	4/8/2008 (5/28/2004)	Method of and apparatus for manufacturing semiconductor thin film, and method of manufacturing thin film transistor Takaoka, Hiromichi; Nakata, Mitsuru
JP4221270 (JP2003-364836)	JP	11/21/2008 (10/24/2003)	Liquid crystal display device and method for manufacturing the same Ueyama, Hiroshi
7193678 (10/948758)	US	3/20/2007 (9/24/2004)	Liquid crystal display device and method for manufacturing the same Ueyama, Hiroshi
7468765 (11/001240)	US	12/23/2008 (12/2/2004)	Electro-optical display device and image projection unit Shiota, Kunihiro; Tanabe, Hiroshi
JP4771042 (JP2004-216325)	JP	7/1/2011 (7/23/2004)	Piezoelectric element mounted device, liquid-drop discharging device using same, and image output device Otose, Tomohiko
7355323 (11/187964)	US	4/8/2008 (7/25/2005)	Piezoelectric device, droplet jetting device using the same, and image output device Otose, Tomohiko

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
7626648 (11/242935)	US	12/1/2009 (10/5/2005)	Pixel circuit substrate, LCD apparatus and projection display apparatus having interlayer insulating film comprising a laminate of inorganic insulating material having a uniformly flat surface over TFT Shiota, Kunihiro; Tanabe, Hiroshi
7388625 (11/313955)	US	6/17/2008 (12/22/2005)	Thin-film transistor array substrate and liquid crystal display device Matsuzaki, Tadahiro; Sera, Kenji
6781643 (09/573185)	US	8/24/2004 (5/18/2000)	Active matrix liquid crystal display device Watanabe, Makoto; Watanabe, Takahiko
6445431 (09/732920)	US	9/3/2002 (12/11/2000)	Liquid crystal display with polarization layer interior to substrates Nose, Takashi; Kaneko, Setsuo; Suzuki, Shigeyoshi
6501520 (10/112947)	US	12/31/2002 (4/2/2002)	Liquid crystal display with polarization layer interior to substrates Nose, Takashi; Kaneko, Setsuo; Suzuki, Shigeyoshi
6822263 (10/457928)	US	11/23/2004 (6/9/2003)	Thin film transistor formed on a transparent substrate Sato, Yoshinobu; Yuda, Katsuhisa; Tanabe, Hiroshi
7119363 (10/911833)	US	10/10/2006 (8/5/2004)	Thin film transistor formed on a transparent substrate Sato, Yoshinobu; Yuda, Katsuhisa; Tanabe, Hiroshi

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Patent/Application Number	Country	Issue Date/ Filing Date	Title of Patent and Inventors
7585708 (11/512596)	US	9/8/2009 (8/30/2006)	Method for manufacturing a thin-film transistor Sato, Yoshinobu; Yuda, Katsuhisa; Tanabe, Hiroshi
6800541 (10/263771)	US	10/5/2004 (10/4/2002)	Pulse laser irradiation method for forming a semiconductor thin film Okumura, Hiroshi
JP3391343 (JP2000-308262)	JP	1/24/2003 (10/6/2000)	Active matrix substrate and manufacturing method therefore Tanaka, Hiroaki; Yamaguchi, Hirotaka; Kaneko, Wakahiko; Sakamoto, Michiaki; Itoida, Satoshi; Hayase, Takasuke; Yoshikawa, Tae; Kano, Hiroshi
TWI151519 (TW089121918)	TW	3/1/2002 (10/19/2000)	Active matrix substrate and the manufacturing method therefore Tanaka, Hiroaki; Yamaguchi, Hirotaka; Kaneko, Wakahiko; Sakamoto, Michiaki; Itoida, Satoshi; Hayase, Takasuke; Yoshikawa, Tae; Kano, Hiroshi
KR10-0393293 (KR10-2000-0062610)	KR	7/21/2003 (10/24/2000)	Active matrix substrate and manufacturing method therefore Hayase, Takasuke; Ihida, Satoshi; Kaneko, Wakahiko; Kano, Hiroshi; Sakamoto, Michiaki; Tanaka, Hiroaki; Yamaguchi, Hirotaka; Yoshikawa, Tae

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
6674093 (09/695321)	US	1/6/2004 (10/25/2000)	Active matrix substrate and manufacturing method therefore Tanaka, Hiroaki; Yamaguchi, Hirotaka; Kaneko, Wakahiko; Sakamoto, Michiaki; Ihida, Satoshi; Hayase, Takasuke; Yoshikawa, Tae; Kanou, Hiroshi
6891196 (10/617035)	US	5/10/2005 (7/11/2003)	Active matrix substrate and manufacturing method therefore Tanaka, Hiroaki; Yamaguchi, Hirotaka; Kaneko, Wakahiko; Sakamoto, Michiaki; Ihida, Satoshi; Hayase, Takasuke; Yoshikawa, Tae; Kanou, Hiroshi
7223622 (10/833153)	US	5/29/2007 (4/28/2004)	Active-matrix substrate and method of fabricating same Hirata, Kazumi
JP3645755 (JP11-263635)	JP	2/10/2005 (9/17/1999)	Thin-film transistor and its manufacturing method Tanabe, Hiroshi
6512246 (09/656631)	US	1/28/2003 (9/7/2000)	Thin film transistor Tanabe, Hiroshi
6667188 (10/272071)	US	12/23/2003 (10/16/2002)	Thin film transistor and method for fabricating same Tanabe, Hiroshi
6797535 (10/693395)	US	9/28/2004 (10/24/2003)	Thin film transistor and method for fabricating same Tanabe, Hiroshi
KR10-0396227 (KR10-2000-0053772)	KR	8/19/2003 (9/9/2000)	Thin-film transistor and its manufacturing method Tanabe, Hiroshi

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
TWI248546 (TW089119085)	TW	2/1/2006 (9/15/2000)	Thin film transistor and method for fabricating same Tanabe, Hiroshi
JP4525451 (JP2005-129251)	JP	6/11/2010 (4/27/2005)	Semiconductor device and image sensor device using same Haga, Hiroshi; Okumura, Fujio
6583456 (09/956669)	US	6/24/2003 (9/20/2001)	Image sensor with light receiving elements of differing areas and image reader both having semiconductor device Haga, Hiroshi; Fujieda, Ichiro; Okumura, Fujio
JP3267271 (JP11-197741)	JP	1/11/2002 (7/12/1999)	Liquid crystal display device and its production Sakamoto, Michiaki; Okamoto, Mamoru; Nakada, Shinichi; Yamamoto, Yuji; Watanabe, Takahiko; Yoshikawa, Shuken; Maruyama, Muneo
TWI184524 (TW088121147)	TW	8/21/2003 (12/3/1999)	Liquid crystal display device and the manufacturing method thereof Nakata, Shinichi; Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hirofumi; Kikkawa, Hironori; Maruyama, Muneo

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
KR10-0376355 (KR10-1999-0056047)	KR	3/5/2003 (12/9/1999)	LIQUID CRYSTAL DISPLAY DEVICE AND MANUFACTURING METHOD THEREOF Nakata, Sinitzi; Yamamoto, Yuzi; Okamoto, Mamoru; Sakamoto, Mitziaki; Watanabe, Takahiko; Ihara, Hirohumi; Kikawa, Hironori; Maruyama, Muneo
6429916 (09/459010)	US	8/6/2002 (12/10/1999)	Liquid crystal display with filter and light shield separated from contact hole Nakata, Shinichi; Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hirofumi; Kikkawa, Hironori; Maruyama, Muneo
6398610 (09/470398)	US	6/4/2002 (12/22/1999)	Display panel production line Inoue, Yoshiyasu
JP3730958 (JP2002-375607)	JP	9/5/2005 (12/25/2002)	Method of patterning multilayer film and multilayer wiring electrode Kato, Takeshi; Kido, Shusaku; Maeda, Akiyoshi
7012029 (10/745636)	US	3/14/2006 (12/29/2003)	Method of forming a lamination film pattern and improved lamination film pattern Kato, Tsuyoshi; Kido, Syuusaku; Maeda, Akitoshi
7554207 (11/295462)	US	6/30/2009 (12/7/2005)	Method of forming a lamination film pattern and improved lamination film pattern Kato, Tsuyoshi; Kido, Syuusaku; Maeda, Akitoshi

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
6734460 (10/114093)	US	5/11/2004 (4/2/2002)	Active matrix substrate and method of fabricating the same Okumura, Hiroshi; Sukegawa, Osamu
KR10-0502685 (KR10-2002-0018192)	KR	7/12/2005 (4/3/2002)	Active matrix substrate and manufacturing method therefore Okumura, Hiroshi; Sukegawa, Osamu
TWI181982 (TW091106803)	TW	7/11/2003 (4/3/2002)	Active matrix substrate and method of fabricating the same Okumura, Hiroshi; Sukegawa, Osamu
CNZL02127312.X (CN02127312.X)	CN	10/5/2005 (7/31/2002)	Active matrix substrate and manufacture thereof Okumura, Nobu; Tasuku, Kawanobu
JP4920140 (JP2001-150092)	JP	2/10/2012 (5/18/2001)	Liquid crystal display device and its manufacturing method Maeda, Akiyoshi; Tanaka, Hiroaki; Kimura, Shigeru; Kimura, Satoshi
KR10-0531094 (KR10-2002-0027406)	KR	11/18/2005 (5/17/2002)	Liquid crystal display device and its manufacturing method Kimura, Satoshi; Kimura, Shigeru; Maeda, Akitoshi; Tanaka, Hiroaki
TWI189785 (TW091110359)	TW	11/1/2003 (5/17/2002)	Active matrix substrate for liquid crystal display and its fabrication Maeda, Akitoshi; Tanaka, Hiroaki; Kimura, Shigeru; Kimura, Satoshi

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
6894311 (10/151544)	US	5/17/2005 (5/20/2002)	Active matrix substrate for liquid crystal display utilizing interconnection lines formed from multilayered films that include an aluminum-neodymium alloy layer Maeda, Akitoshi; Tanaka, Hiroaki; Kimura, Shigeru; Kimura, Satoshi
CNZL02140183.7 (CN02140183.7)	CN	10/25/2006 (7/3/2002)	tive matrix substrate for LCD and mfg. method thereof Maeda, Akitoshi; Tanaka, Hiroaki; Kimura, Shigeru; Kimura, Satoshi
7037766 (10/793086)	US	5/2/2006 (3/4/2004)	Method of fabricating an active matrix substrate for liquid crystal display utilizing interconnection lines formed from multilayered films that include an aluminum-neodymium alloy layer Maeda, Akitoshi; Tanaka, Hiroaki; Kimura, Shigeru; Kimura, Satoshi
JP5411236 (JP2011-249576)	JP	11/15/2013 (11/15/2011)	Liquid crystal display device and its manufacturing method Maeda, Akiyoshi; Tanaka, Hiroaki; Kimura, Shigeru; Kimura, Satoshi

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Patent/Application Number	Country	Issue Date/ Filing Date	Title of Patent and Inventors
JP5408829 (JP2000-252076)	JP	11/15/2013 (8/23/2000)	Active matrix substrate and method of manufacturing the same Kimura, Shigeru; Watanabe, Takahiko; Yoshikawa, Tae; Uchida, Hiroyuki; Kido, Shusaku; Nakada, Shinichi; Hamada, Tsutomu; Shimodousono, Hisashi; Doi, Satoshi; Harano, Toshihiko; Maeda, Akiyoshi; Itoida, Satoshi; Tanaka, Hiroaki; Hayase, Takasuke; Kuroba, Shoichi; Ihara, Hiroshi; Takechi, Kazue
6632696 (09/745657)	US	10/14/2003 (12/20/2000)	Manufacturing method of active matrix substrate plate and manufacturing method therefor Kimura, Shigeru; Watanabe, Takahiko; Yoshikawa, Tae; Uchida, Hiroyuki; Kido, Shusaku; Nakata, Shinichi; Hamada, Tsutomu; Shimodouzono, Hisanobu; Doi, Satoshi; Harano, Toshihiko; Maeda, Akitoshi; Ihida, Satoshi; Tanaka, Hiroaki; Hayase, Takasuke; Kuroha, Shouichi; Ihara, Hirofumi; Takechi, Kazushige

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
KR10-0463410 (KR10-2000-0083299)	KR	12/15/2004 (12/27/2000)	Active matrix substrate plate and manufacturing method therefor Doi, Satoshi; Hamada, Tsutomu; Harano, Toshihiko; Hayase, Takasuke; Ihara, Hirofumi; Ihida, Satoshi; Kido, Shusaku; Kimura, Shigeru; Kuroha, Shoichi; Maeda, Akitoshi; Nakata, Shinichi; Shimodouzono, Hisanobu; Takechi, Kazushige; Tanaka, Hiroaki; Uchida, Hiroyuki; Watanabe, Takahiko; Yoshikawa, Tae
TWI165555 (TW089127961)	TW	10/11/2002 (12/27/2000)	Active matrix substrate plate and manufacturing method therefor Kimura, Shigeru; Watanabe, Takahiko; Yoshikawa, Tae; Uchida, Hiroyuki; Kido, Shusaku; Nakata, Shinichi; Hamada, Tsutomu; Shimodouzono, Hisanobu; Doi, Satoshi; Harano, Toshihiko; Maeda, Akitoshi; Ihida, Satoshi; Tanaka, Hiroaki; Hayase, Takasuke; Kuroha, Shouichi; Ihara, Hirofumi; Takechi, Kazushige
JP3652997 (JP2001-085793)	JP	3/4/2005 (3/23/2001)	Liquid crystal material and liquid crystal display device Matsumoto, Koichi
6649226 (09/877180)	US	11/18/2003 (6/11/2001)	Liquid crystal material and liquid crystal display Matsumoto, Kimikazu

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KR10-0437141 (KR10-2001-0033060)	KR	6/14/2004 (6/13/2001)	Liquid crystal material and liquid crystal display Matsumoto, Kimikazu
TWI206385 (TW090114361)	TW	6/21/2004 (6/13/2001)	Liquid crystal material and liquid crystal display Matsumoto, Kimikazu
6781647 (09/842580)	US	8/24/2004 (4/26/2001)	Liquid crystal display device having a front light unit Fujieda, Ichiro
JP3570974 (JP2000-216202)	JP	7/2/2004 (7/17/2000)	Active matrix type liquid crystal display device Matsumoto, Koichi; Nishida, Shinichi
KR10-0396823 (KR10-2001-0042383)	KR	8/21/2003 (7/13/2001)	Active matrix type liquid crystal display device Matsumoto, Kimikazu; Nishida, Shinichi
TWI238915 (TW090117502)	TW	9/1/2005 (7/17/2001)	Active matrix liquid crystal display device Matsumoto, Kimikazu; Nishida, Shinichi
7009673 (10/656138)	US	3/7/2006 (9/8/2003)	Active matrix liquid crystal display having a thin film transistor over which alignment of liquid crystal molecules does not change Matsumoto, Kimikazu; Nishida, Shinichi
7492431 (11/291997)	US	2/17/2009 (12/2/2005)	Active matrix liquid crystal display having a thin film transistor over which alignment of liquid crystal molecules does not change Matsumoto, Kimikazu; Nishida, Shinichi

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6881894 (09/975791)	US	4/19/2005 (10/12/2001)	EMI shielding structures Kai, Takanobu
7271041 (11/102765)	US	9/18/2007 (4/11/2005)	Method for manufacturing thin film transistor Takahashi, Mitsuasa
7012849 (10/619594)	US	3/14/2006 (7/16/2003)	Semiconductor, image output device, and driving method of a functional device Otose, Tomohiko
7665816 (11/330968)	US	2/23/2010 (1/13/2006)	Image output device with an injector Otose, Tomohiko
CNZL03132804.0 (CN03132804.0)	CN	4/9/2008 (7/21/2003)	Semiconductor, image output device and driving method for function device Otose, Tomohiko
TWI291225 (TW092104387)	TW	12/11/2007 (3/5/2003)	Thin film semiconductor device and method for manufacturing same Takahashi, Mitsuasa
7109554 (10/961094)	US	9/19/2006 (10/12/2004)	Thin film semiconductor device and method for manufacturing same Takahashi, Mitsuasa
JP4423659 (JP2003-024473)	JP	12/18/2009 (1/31/2003)	Thin film transistor, TFT substrate, and liquid crystal display Matsunaga, Naoki; Sera, Kenji
7105905 (10/724040)	US	9/12/2006 (12/1/2003)	Thin film transistor, TFT substrate and liquid crystal display unit Matsunaga, Naoki; Sera, Kenji

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CNZL200310124837.9 (CN200310124837.9)	CN	12/6/2006 (12/31/2003)	Film transistor, TFT substrate and LCD Matsunaga, Naoki; Sera, Kenji
JP4945107 (JP2005-268809)	JP	(9/15/2005)	Light source device and its manufacturing method, indicator device and its manufacturing method, and drive method for indicator device Takatori, Kenichi; Sumiyoshi, Ken
7369725 (11/521009)	US	5/6/2008 (9/14/2006)	Light source device and method for manufacturing the same, display device and method for manufacturing the same, and method for driving display device Takatori, Kenichi; Sumiyoshi, Ken
7656472 (12/050324)	US	2/2/2010 (3/18/2008)	Light source device and method for manufacturing the same, display device and method for manufacturing the same, and method for driving display device Takatori, Kenichi; Sumiyoshi, Ken
7212722 (11/299951)	US	5/1/2007 (12/13/2005)	Backlight unit and liquid-crystal display device using the same Shinohara, Toshiya
CNZL200610051348.9 (CN200610051348.9)	CN	6/25/2008 (1/5/2006)	Backlight unit and liquid-crystal display device using the same Shinohara, Toshiya

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7122444 (10/974932)	US	10/17/2006 (10/28/2004)	Manufacturing method of thin film device substrate Nakata, Mitsura; Takechi, Kazushige; Kanoh, Hiroshi
7579222 (11/503286)	US	8/25/2009 (8/14/2006)	Manufacturing method of thin film device substrate Nakata, Mitsura; Takechi, Kazushige; Kanoh, Hiroshi
7373834 (11/586512)	US	5/20/2008 (10/26/2006)	Pressure sensor Takahashi, Mitsuasa
JP3127894 (JP10-209148)	JP	11/10/2000 (7/24/1998)	Active matrix type liquid crystal display device Watanabe, Makoto; Watanabe, Takahiko
TWI188084 (TW088110844)	TW	10/11/2003 (6/28/1999)	Active matrix-type liquid crystal display device Watanabe, Makoto; Watanabe, Takahiko
CNZL99109819.6 (CN99109819.6)	CN	3/12/2003 (7/15/1999)	Active matrix-type liquid crystal display device Watanabe, Sei; Watanabe, Kihiko
6404474 (09/357060)	US	6/11/2002 (7/19/1999)	Horizontal electric field LCD with increased capacitance between pixel and common electrodes Watanabe, Makoto; Watanabe, Takahiko
KR10-0313726 (KR10-1999-0030005)	KR	10/24/2001 (7/23/1999)	Active matrix-type liquid crystal display device Watanabe, Makoto; Watanabe, Takahiko

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JP3538046 (JP10-353579)	JP	3/26/2004 (12/11/1998)	Liquid crystal module support structure and portable terminal device mounting the same Yano, Kiichi; Miwa, Tomoo; Sato, Yasuyoshi; Ogawa, Toshinao; Nishiyama, Tomoaki; Baba, Masatake; Okabe, Akira; Fujishiro, Fumihiko; Kato, Takehiro; Mikami, Kazuaki; Fukuyoshi, Hirokazu
TWI151064 (TW088121453)	TW	2/21/2002 (12/8/1999)	Liquid crystal module installation structure and portable terminal device having the same Yano, Takakazu; Miwa, Tomoo; Sato, Yasuhiro; Ogawa, Toshihisa; Nishiyama, Michiaki; Baba, Masatake; Okabe, Hikaru; Fumihiko, Fujishiro; Kato, Yudai; Mikami, Kazuaki; Fukuyoshi, Hirokazu
KR10-0419043 (KR10-1999-0056493)	KR	2/4/2004 (12/10/1999)	Structure for supporting liquid module Yano, Takakazu; Miwa, Tomoo; Sato, Yasuhiro; Ogawa, Toshihisa; Nishiyama, Michiaki; Baba, Masatake; Okabe, Hikaru; Fujishiro, Fumihiko; Kato, Katsuhiko; Mikami, Kazuaki; Fukuyoshi, Hirokazu

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CNZL99126036.8 (CN99126036.8)	CN	1/7/2004 (12/13/1999)	Liquid crystal module installation structure and portable terminal device having same Yano, Takakazu; Miwa, Tomoo; Sato, Yasuhiro; Ogawa, Toshihisa; Nishiyama, Michiaki; Baba, Masatake; Okabe, Hikaru; Fujishiro, Fumihiko; Kato, Katsuhiro; Mikami, Kazuaki; Fukuyoshi, Hirokazu
6594143 (09/459280)	US	7/15/2003 (12/13/1999)	Liquid crystal module mounting structure and mobile terminal mounted with the same Yano, Takakazu; Miwa, Tomoo; Sato, Yasuhiro; Ogawa, Toshihisa; Nishiyama, Michiaki; Baba, Masatake; Okabe, Hikaru; Fujishiro, Fumihiko; Kato, Katsuhiro; Mikami, Kazuaki; Fukuyoshi, Hirokazu
JP4375517 (JP2001-220829)	JP	9/18/2009 (7/23/2001)	Liquid crystal display device Sato, Tetsushi; Sekine, Hiroyuki; Yoshinaga, Kazuhide
6853407 (10/196356)	US	2/8/2005 (7/17/2002)	Liquid crystal display device including a lightly doped drain region Sato, Tetsushi; Sekine, Hiroyuki; Yoshinaga, Kazuhide
DE60222508.6 (DE60222508.6)	DE	9/19/2007 (7/18/2002)	Liquid crystal display device Sato, Tetsushi; Sekine, Hiroyuki; Yoshinaga, Kazuhide

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DE60248633.5 (DE60248633.5)	DE	1/18/2017 (7/18/2002)	Liquid crystal display device Sato, Tetsushi; Sekine, Hiroyuki; Yoshinaga, Kazuhide
FR1279997 (FR02016030.5)	FR	9/19/2007 (7/18/2002)	Liquid crystal display device Sato, Tetsushi; Sekine, Hiroyuki; Yoshinaga, Kazuhide
FR1744206 (FR06022058.9)	FR	1/18/2017 (7/18/2002)	Liquid crystal display device Sato, Tetsushi; Sekine, Hiroyuki; Yoshinaga, Kazuhide
GB1279997 (GB02016030.5)	GB	9/19/2007 (7/18/2002)	Liquid crystal display device Sato, Tetsushi; Sekine, Hiroyuki; Yoshinaga, Kazuhide
GB1744206 (GB06022058.9)	GB	1/18/2017 (7/18/2002)	Liquid crystal display device Sato, Tetsushi; Sekine, Hiroyuki; Yoshinaga, Kazuhide
CNZL02126554.2 (CN02126554.2)	CN	1/19/2005 (7/23/2002)	Liquid crystal display Sato, Tetsushi; Sekine, Hiroyuki; Yoshinaga, Kazuhide
TWI290247 (TW091115733)	TW	11/21/2007 (7/15/2002)	Backlight unit uniformly illuminating object regardless of lapse of time and liquid crystal display panel device using the same Ueda, Shoichi; Sakaki, Yoshinobu
JP4819016 (JP2007-258154)	JP	9/9/2011 (10/1/2007)	Method of manufacturing liquid crystal display device Kido, Shusaku

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TWI311658 (TW091114593)	TW	7/1/2009 (7/2/2002)	Pattern forming method and method for manufacturing liquid crystal display device using the same Kido, Shusaku
6617263 (10/189571)	US	9/9/2003 (7/8/2002)	Pattern forming method for flattening an organic film for a liquid crystal display device Kido, Shusaku
7408135 (10/252407)	US	8/5/2008 (9/24/2002)	Imaging device having dual opening with open/close unit and electronic equipment incorporated therein the same Fujieda, Ichiro
JP3858263 (JP2001-344895)	JP	9/29/2006 (11/9/2001)	Fingerprint image input device and electronic equipment using the same Fujieda, Ichiro
6885439 (10/288925)	US	4/26/2005 (11/6/2002)	Fingerprint input devices and electronic devices provided with the same Fujieda, Ichiro
CNZL02150502.0 (CN02150502.0)	CN	8/17/2005 (11/11/2002)	Fingerprint input device and electronic equipment with the fingerprint input device Fujie, Ichiro
TWI286643 (TW092104486)	TW	9/11/2007 (3/3/2003)	Optical unit and LCD device using the optical unit Ogawa, Toshihisa
7301588 (10/376637)	US	11/27/2007 (3/3/2003)	Optical unit and LCD device using the optical unit Ogawa, Toshihisa
7593070 (11/984403)	US	9/22/2009 (11/16/2007)	Optical unit and LCD device using the optical unit Ogawa, Toshihisa

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CNZL03105113.8 (CN03105113.8)	CN	12/14/2005 (3/4/2003)	Light unit element and liquid crystal display equipment using light unit element Ogawa, Toshinao
KR10-0543817 (KR10-2003-0013286)	KR	1/10/2006 (3/4/2003)	Optical unit and LCD device using the optical unit Ogawa, Toshihisa
TWI206131 (TW092107034)	TW	6/21/2004 (3/27/2003)	In-plane switching mode liquid crystal display device Matsumoto, Kimikazu; Itakura, Kunimasa; Nishida, Shinichi
6950165 (10/397256)	US	9/27/2005 (3/27/2003)	In-plane switching mode liquid crystal display device Matsumoto, Kimikazu; Itakura, Kunimasa; Nishida, Shinichi
KR10-0510566 (KR10-2003-0019773)	KR	8/19/2005 (3/29/2003)	In-plane switching mode liquid crystal display device Matsumoto, Kimikazu; Itakura, Kunimasa; Nishida, Shinichi
CNZL03108606.3 (CN03108606.3)	CN	4/12/2006 (3/31/2003)	In-plane switching mode liquid crystal display device Nishida, Shinichi; Itakura, Kunimasa; Matsumoto, Kimikazu
7476470 (10/987772)	US	1/13/2009 (11/12/2004)	Method for manufacturing thin film semiconductor device and method for forming resist pattern thereof Takahashi, Mitsuasa
7151516 (10/632713)	US	12/19/2006 (8/1/2003)	Liquid crystal display device Takeda, Hiroshi; Hirano, Youji

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6840647 (09/991600)	US	1/11/2005 (11/23/2001)	Lighting device, liquid crystal display device including the same, and method of fabricating the same Hayashi, Kazuhiko; Fujieda, Ichiro; Oda, Atsushi; Kaneko, Setsuo
6816140 (10/013263)	US	11/9/2004 (12/7/2001)	Displaying device and displaying method and manufacturing method of the device Fujieda, Ichiro
JP4190798 (JP2002-132310)	JP	9/26/2008 (5/8/2002)	Thin film transistor and manufacturing method thereof Okumura, Nobu
TWI206712 (TW092112290)	TW	6/21/2004 (5/6/2003)	Thin-film transistor and method for manufacturing same Okumura, Hiroshi
6815269 (10/430540)	US	11/9/2004 (5/6/2003)	Thin-film transistor and method for manufacturing the same Okumura, Hiroshi
KR10-0510934 (KR10-2003-0028829)	KR	8/22/2005 (5/7/2003)	Thin-film transistor and method for manufacturing same Okumura, Hiroshi
CNZL03130906.2 (CN03130906.2)	CN	5/30/2007 (5/8/2003)	Thin film transistor and manufacturing method thereof Oku, Muranobu

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7199780 (10/457374)	US	4/3/2007 (6/10/2003)	Field sequential driving type liquid crystal display apparatus capable of increasing brightness while suppressing irregularity, and its driving method Sekine, Hiroyuki
JP4133088 (JP2002-224997)	JP	6/6/2008 (8/1/2002)	Liquid crystal display device Ishii, Toshiya; Sakamoto, Michiaki; Hayakawa, Kiyomi
KR10-0575034 (KR10-2003-0053185)	KR	4/24/2006 (7/31/2003)	Liquid crystal display device Ishii, Toshiya; Sakamoto, Michiaki; Hayakawa, Kiyomi
CNZL03152510.5 (CN03152510.5)	CN	7/11/2007 (8/1/2003)	LCD device Donburi, Toshiya; Sakamoto, Michiaki; Sasagawa, Kiyomi
TWI225564 (TW092121105)	TW	12/21/2004 (8/1/2003)	Liquid crystal display device Ishii, Toshiya; Sakamoto, Michiaki; Hayakawa, Kiyomi
6967702 (10/633219)	US	11/22/2005 (8/1/2003)	Liquid crystal display device Ishii, Toshiya; Sakamoto, Michiaki; Hayakawa, Kiyomi
7071040 (10/639478)	US	7/4/2006 (8/13/2003)	Method of fabricating thin film transistor Matsunaga, Naoki; Sera, Kenji
JP4371687 (JP2003-087871)	JP	9/11/2009 (3/27/2003)	Backlight device and liquid crystal display Ono, Shinichirou; Fukuyoshi, Hirokazu
7106394 (10/807245)	US	9/12/2006 (3/24/2004)	Backlight unit in a liquid crystal display device Ono, Shin-Ichirou; Fukuyoshi, Hirokazu

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TWI255948 (TW093108056)	TW	6/1/2006 (3/25/2004)	Backlight unit in a liquid crystal display device Ono, Shinichirou; Fukuyoshi, Hirokazu
CNZL200410031388.8 (CN200410031388.8)	CN	4/25/2007 (3/26/2004)	Poor light apparatus and liquid crystal display Ono, Shin-Ichirou; Fukuyoshi, Hirokazu
KR10-0685086 (KR10-2004-0020688)	KR	2/13/2007 (3/26/2004)	Backlight unit of LCD for improving luminance Ono, Shinichirou; Fukuyoshi, Hirokazu
7050020 (10/646799)	US	5/23/2006 (8/25/2003)	3D image/2D image switching display apparatus and portable terminal device Uehara, Shin-ichi; Takanashi, Nobuaki; Hayama, Hiroshi
GB1394593 (GB03019284.3)	GB	6/2/2010 (8/26/2003)	3D image / 2D image switching display apparatus Uehara, Shin-ichi; Takanashi, Nobuaki; Hayama, Hiroshi
7148871 (10/656297)	US	12/12/2006 (9/8/2003)	Liquid crystal display device, liquid crystal display device driving method, and liquid crystal projector apparatus Sekine, Hiroyuki
TWI255382 (TW093111937)	TW	5/21/2006 (4/28/2004)	Liquid crystal display device Okamoto, Mamoru; Nishida, Shinichi; Konno, Takayuki; Maruyama, Muneo
7046327 (10/833318)	US	5/16/2006 (4/28/2004)	Liquid crystal display device including columnar spacer above gate line Okamoto, Mamoru; Nishida, Shinichi; Konno, Takayuki; Maruyama, Muneo

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7057340 (10/838203)	US	6/6/2006 (5/5/2004)	Organic electroluminescence display device Imura, Hironori
CNZL200410043242.5 (CN200410043242.5)	CN	7/15/2009 (5/14/2004)	Organic electroluminescence display device Imura, Hironori
KR10-0707544 (KR10-2004-0034135)	KR	4/6/2007 (5/14/2004)	Organic electroluminescence display device robust to stress from outside Imura, Hironori
JP4135088 (JP2003-283668)	JP	6/13/2008 (7/31/2003)	Liquid crystal display and liquid crystal projector Sato, Yuko; Matsushima, Jin; Sugimoto, Mitsuhiro; Sumiyoshi, Ken
7310127 (10/901206)	US	12/18/2007 (7/29/2004)	LCD device and LCD projector Sato, Yuko; Matsushima, Jin; Sugimoto, Mitsuhiro; Sumiyoshi, Ken
CNZL200410055632.4 (CN200410055632.4)	CN	8/29/2007 (7/30/2004)	LCD device and LCD projector Sato, Yuko; Matsushima, Jin; Sugimoto, Mitsuhiro; Sumiyoshi, Ken
7554162 (10/873149)	US	6/30/2009 (6/23/2004)	Thin film transistor substrate with low reflectance upper electrode Hayashi, Kenichi; Shimamoto, Hirofumi; Matsuzaki, Tadahiro
7242450 (10/931560)	US	7/10/2007 (9/1/2004)	Orientation film rubbing apparatus and method to suppress scraping-off of the orientation film Mizoguchi, Chikaaki

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JP4047796 (JP2003-376749)	JP	11/30/2007 (11/6/2003)	Backlight device and double-faced display device Ogawa, Toshihisa; Ono, Shinichirou
7090365 (10/970737)	US	8/15/2006 (10/21/2004)	Backlight unit having an inclined light emission surface Ogawa, Toshihisa; Ono, Shin-ichirou
CNZL200410084884.X (CN200410084884.X)	CN	11/26/2008 (11/1/2004)	Backlight unit having an inclined light emission surface Ogawa, Toshihisa; Ono, Shin-ichirou
KR10-0669056 (KR10-2004-0090014)	KR	1/9/2007 (11/5/2004)	Backlight unit having an inclined light emission surface Ogawa, Toshihisa; Ono, Shinichirou
TWI293389 (TW093133818)	TW	2/11/2008 (11/5/2004)	Backlight unit and double-sided display device Ogawa, Toshihisa; Ono, Shin-ichirou
TWI247431 (TW093136866)	TW	1/11/2006 (11/30/2004)	Liquid crystal display unit Kimura, Shigeru
7511301 (11/000153)	US	3/31/2009 (12/1/2004)	Liquid crystal display unit Kimura, Shigeru
JP4512976 (JP2003-411873)	JP	5/21/2010 (12/10/2003)	Liquid crystal display device and its manufacturing method Hashimoto, Nobuaki
TWI269109 (TW093137736)	TW	12/21/2006 (12/7/2004)	Liquid crystal display device and method of manufacturing the same Hashimoto, Yoshiaki

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7280178 (11/007157)	US	10/9/2007 (12/9/2004)	Liquid crystal display device and method of manufacturing the same Hashimoto, Yoshiaki
CNZL200410100094.6 (CN200410100094.6)	CN	6/11/2008 (12/10/2004)	Liquid crystal display device and method of manufacturing the same Hashimoto, Yoshiaki
KR10-0704062 (KR10-2004-0104009)	KR	3/29/2007 (12/10/2004)	Liquid crystal display device and method of manufacturing the same Hashimoto, Yoshiaki
7294881 (11/047814)	US	11/13/2007 (2/2/2005)	Nonvolatile semiconductor memory having a charge accumulation layer connected to a gate Korenari, Takahiro; Sera, Kenji; Kanou, Hiroshi
JP4438441 (JP2004-038571)	JP	1/15/2010 (2/16/2004)	Lighting system and display device equipped with it Saito, Goro; Imai, Masao
7160003 (11/052765)	US	1/9/2007 (2/9/2005)	Illumination device and display device incorporating the same Saitoh, Goroh; Imai, Masao
CNZL200510007993.6 (CN200510007993.6)	CN	3/5/2008 (2/16/2005)	Illumination device and display device incorporating the same Saitoh, Goroh; Imai, Masao
JP4412027 (JP2004-096525)	JP	11/27/2009 (3/29/2004)	Amplifier circuit and display device Tsuchi, Hiroshi
CNZL200510062558.3 (CN200510062558.3)	CN	4/29/2009 (3/29/2005)	Amplifier circuit and display device Tsuchi, Hiroshi

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7339422 (11/093099)	US	3/4/2008 (3/29/2005)	Amplifier circuit and display device Tsuchi, Hiroshi
7183635 (11/086338)	US	2/27/2007 (3/23/2005)	Semiconductor device having a low-resistance bus interconnect, method of manufacturing same, and display apparatus employing same Toba, Tamaki
7405148 (11/610940)	US	7/29/2008 (12/14/2006)	Semiconductor device having a low-resistance bus interconnect, method of manufacturing same, and display apparatus employing same Toba, Tamaki
CNZL200510062941.9 (CN200510062941.9)	CN	6/11/2008 (3/31/2005)	Semiconductor device having a low-resistance bus interconnect, method of manufacturing same, and display apparatus employing same Toba, Tamaki
CNZL200510063863.4 (CN200510063863.4)	CN	(4/8/2005)	Space saving on peripheral rim outside display pixel region in display device Ishibashi, Osamu
7483109 (11/102777)	US	1/27/2009 (4/11/2005)	Space saving on peripheral rim outside display pixel region in display device Ishibashi, Osamu
TWI282961 (TW094113667)	TW	6/21/2007 (4/28/2005)	Liquid crystal display device, and light source driving circuit and method to be used in same Honbo, Nobuaki

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CNZL200510066717.7 (CN200510066717.7)	CN	1/23/2008 (4/30/2005)	Liquid crystal display device, and light source driving circuit and method to be used in same Honbo, Nobuaki
7489295 (11/118432)	US	2/10/2009 (5/2/2005)	Liquid crystal display device, and light source driving circuit and method to be used in same Honbo, Nobuaki
JP4491638 (JP2004-150514)	JP	4/16/2010 (5/20/2004)	External commutated inverter circuit for backlight and driving method Honpo, Nobuaki
7345432 (11/131231)	US	3/18/2008 (5/18/2005)	Inverter circuit for lighting backlight of liquid crystal display and method for driving the same Honbo, Nobuaki
CNZL200610005009.7 (CN200610005009.7)	CN	9/3/2008 (1/18/2006)	Liquid crystal display panel and liquid crystal display device Hiroya, Tsutomu; Sumiyoshi, Ken; Shigemura, Kouji
7525618 (11/334681)	US	4/28/2009 (1/18/2006)	Liquid crystal display panel and liquid crystal display device Hiroya, Tsutomu; Sumiyoshi, Ken; Shigemura, Kouji
JP4382681 (JP2005-027909)	JP	10/2/2009 (2/3/2005)	Liquid crystal display device Sakaki, Yoshinobu
TWI301923 (TW095103448)	TW	10/11/2008 (1/27/2006)	Backlight unit and liquid- crystal display device using the same Sakaki, Yoshinobu

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KR10-0823063 (KR10-2006-0010090)	KR	4/11/2008 (2/2/2006)	Backlight unit and liquid-crystal display device using the same Sakaki, Yoshinobu
7350956 (11/345306)	US	4/1/2008 (2/2/2006)	Backlight unit and liquid-crystal display device using the same Sakaki, Yoshinobu
JP5309059 (JP2010-049923)	JP	7/5/2013 (3/5/2010)	Method and apparatus for deciding occurrence of microcrystals Okumura, Nobu
7473657 (11/361756)	US	1/6/2009 (2/24/2006)	Laser irradiation method and apparatus for forming a polycrystalline silicon film Okumura, Hiroshi
7372211 (11/362839)	US	5/13/2008 (2/28/2006)	Cold cathode tube lighting device and driving method and integrated circuit to be used in same Honbo, Nobuaki
JP4543385 (JP2005-073631)	JP	7/9/2010 (3/15/2005)	Liquid crystal display device and its manufacturing method Yasuda, Yukiyasu; Suzuki, Seiji
KR10-0823061 (KR10-2006-0023072)	KR	4/11/2008 (3/13/2006)	Liquid crystal display device capable of settling disclination defect and manufacturing method of the same Yasuda, Kyounsei; Suzuki, Seiji

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CNZL200610059141.6 (CN200610059141.6)	CN	8/20/2008 (3/15/2006)	Liquid crystal display device and manufacturing method of the same Yasuda, Kyounei; Suzuki, Seiji
7564531 (11/375112)	US	7/21/2009 (3/15/2006)	LCD device and method including a plastic substrate with metal layer containing copper surrounded by barrier metal film embedded in a groove within the plastic substrate Yasuda, Kyounei; Suzuki, Seiji
CNZL200610100954.5 (CN200610100954.5)	CN	9/17/2008 (8/2/2006)	Optical element, optical device, mobile information terminal, and manufacturing method Sumiyoshi, Ken; Mimura, Koji
7551340 (11/461959)	US	6/23/2009 (8/2/2006)	Optical element, optical device, mobile information terminal, and manufacturing method Sumiyoshi, Ken; Mimura, Koji
JP4650624 (JP2005-216776)	JP	12/24/2010 (7/27/2005)	Backlight unit and liquid crystal display device Shinohara, Toshiya; Sugitani, Choei
TWI339761 (TW095126699)	TW	4/1/2011 (7/21/2006)	Backlight unit, display device provided with the backlight unit, and method of manufacturing the display device Shinohara, Toshiya; Sugitani, Choei

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7446825 (11/490119)	US	11/4/2008 (7/21/2006)	Backlight unit, display device provided with the backlight unit, and method of manufacturing the display device Shinohara, Toshiya; Sugitani, Chouei
KR10-0777871 (KR10-2006-0070122)	KR	11/13/2007 (7/26/2006)	Backlight unit for preventing deformation of a reflector sheet caused by an fpc, a display device having the backlight unit, and a method for manufacturing the display device Shinohara, Toshiya; Sugitani, Chouei
CNZL200610107589.0 (CN200610107589.0)	CN	12/31/2008 (7/27/2006)	Backlight unit, display device provided with the backlight unit, and method of manufacturing the display device Shinohara, Toshiya; Sugitani, Chouei
7499119 (11/582315)	US	3/3/2009 (10/18/2006)	Liquid-crystal display device with thin-film transistors and method of fabricating the same Yasuda, Kyounei
JP4771065 (JP2005-288352)	JP	7/1/2011 (9/30/2005)	Light source device, display device, and terminal device Uehara, Shinichi; Imai, Masao
CNZL200610139942.3 (CN200610139942.3)	CN	5/9/2012 (9/27/2006)	Light source device, display device, and terminal device Uehara, Shin-ichi; Imai, Masao

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RE44063 (13/111815)	US	3/12/2013 (5/19/2011)	Light source device, display device, and terminal device Uehara, Shin-ichi; Imai, Masao
JP4813861 (JP2005-282719)	JP	9/2/2011 (9/28/2005)	Liquid crystal module Morishita, Shizuo
TWI361307 (TW095134782)	TW	4/1/2012 (9/20/2006)	Liquid crystal display module Morishita, Shizuo
7520655 (11/526571)	US	4/21/2009 (9/26/2006)	Liquid crystal display module Morishita, Shizuo
CNZL200610141508.9 (CN200610141508.9)	CN	7/22/2009 (9/28/2006)	Liquid crystal display module Morishita, Shizuo
JP4994649 (JP2005-349752)	JP	5/18/2012 (12/2/2005)	Backlight for color liquid crystal display device Takahashi, Yoshitomo
RE45229 (13/292700)	US	11/4/2014 (11/9/2011)	Backlight for color liquid crystal display apparatus Takahashi, Mitsuasa
JP4577229 (JP2006-038953)	JP	9/3/2010 (2/16/2006)	Backlight device and liquid crystal display Nishigaki, Eitaro
RE44118 (13/245678)	US	4/2/2013 (9/26/2011)	Backlight unit and liquid-crystal display device using the same Nishigaki, Eitaro
7568574 (11/798090)	US	8/4/2009 (5/10/2007)	Substrate transportation method and apparatus Nagatomo, Kazuya
JP4939133 (JP2006-187865)	JP	3/2/2012 (7/7/2006)	Connector Uchida, Toshio

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7491079 (11/822489)	US	2/17/2009 (7/6/2007)	Connector and method of connecting the same Uchida, Toshio
JP4890100 (JP2006-144763)	JP	12/22/2011 (5/25/2006)	Image display device Komahisa, Manabu
7518690 (11/802784)	US	4/14/2009 (5/24/2007)	Image display device Komaju, Manabu
JP4493064 (JP2000-308171)	JP	4/16/2010 (10/6/2000)	Flat-face type fluorescent lamp, its fixing structure, and liquid crystal display device Fujishiro, Fumihiko
TWI259495 (TW090124797)	TW	8/1/2006 (10/5/2001)	Flat fluorescent lamp having unique mating portion and liquid crystal display device incorporating such a flat fluorescent lamp Fujishiro, Fumihiko
6611092 (09/971994)	US	8/26/2003 (10/5/2001)	Flat fluorescent lamp having unique mating portion and liquid crystal display device incorporating such a flat fluorescent lamp Fujishiro, Fumihiko
6864947 (09/774921)	US	3/8/2005 (1/31/2001)	Method of fabricating liquid crystal display substrate and the same Shiraishi, Hideaki
TWI150201 (TW087117617)	TW	2/1/2002 (10/23/1998)	Semiconductor device and method for producing the same Fujita, Akira; Sugitani, Choei

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DE69808405.5 (DE69808405.5)	DE	10/2/2002 (10/26/1998)	Connection terminal structure for liquid crystal display and semiconductor device, and method of producing the same Fujita, Akira; Sugitani, Choei
FR0913719 (FR98120246.8)	FR	10/2/2002 (10/26/1998)	Connection terminal structure for liquid crystal display and semiconductor device, and method of producing the same Fujita, Akira; Sugitani, Choei
GB0913719 (GB98120246.8)	GB	10/2/2002 (10/26/1998)	Connection terminal structure for liquid crystal display and semiconductor device, and method of producing the same Fujita, Akira; Sugitani, Choei
6184966 (09/179654)	US	2/6/2001 (10/27/1998)	Semiconductor device and method for producing the same Fujita, Akira; Sugitani, Choei
6323931 (09/671414)	US	11/27/2001 (9/27/2000)	LCD with external circuit having anti-short-circuit pattern and particular structure Fujita, Akira; Sugitani, Choei
6683662 (10/003550)	US	1/27/2004 (10/24/2001)	Semiconductor device and method for producing the same Fujita, Akira; Sugitani, Choei

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KR10-0302577 (KR10-1998-0045382)	KR	7/4/2001 (10/28/1998)	Semiconductor device and manufacturing method thereof, especially for an LCD capable of preventing short-circuit occurred by current leakage between terminals due to corrosion of metal wires by installing an interterminal anti-short-circuiting pattern that is effective for cutting off humidity Fujita, Akira; Sugitani, Choei
6064458 (09/181641)	US	5/16/2000 (10/29/1998)	Rubbing apparatus and rubbing method Mori, Shigeru
TWI137991 (TW087116884)	TW	7/11/2001 (10/9/1998)	Semiconductor device and method of making the same Nakata, Shinichi
DE69835793.0 (DE69835793.0)	DE	9/6/2006 (10/26/1998)	Thin-film transistor with light-shielding film for use in liquid crystal devices, and method of making the same Nakata, Shinichi
FR0915365 (FR98120242.7)	FR	9/6/2006 (10/26/1998)	Thin-film transistor with light-shielding film for use in liquid crystal devices, and method of making the same Nakata, Shinichi
GB0915365 (GB98120242.7)	GB	9/6/2006 (10/26/1998)	Thin-film transistor with light-shielding film for use in liquid crystal devices, and method of making the same Nakata, Shinichi
6262436 (09/185640)	US	7/17/2001 (11/4/1998)	Semiconductor device and method of making the same Nakata, Shinichi

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KR10-0323367 (KR10-1998-0047384)	KR	1/23/2002 (11/5/1998)	Semiconductor device suitable for a flat display LCD of an active matrix display comprising a-si TFT and its fabrication method Nakata, Shinichi
6051815 (09/262340)	US	4/18/2000 (3/4/1999)	Apparatus for heat-treating substrate and method for separating the substrate from the apparatus Satou, Yasuyuki
KR10-0326826 (KR10-1998-0037915)	KR	2/20/2002 (9/15/1998)	LCD HAVING A WIDE VIEWING ANGLE, ESPECIALLY CAPABLE OF REDUCING APPEARANCE OF LIGHT SPOTS BY ALLOWING SOME LIQUID CRYSTAL MOLECULES ADJACENT TO THE TWO ALIGNMENT LAYERS TO HAVE A PRE-TILT ANGLE OF MORE THAN 2° Matsumoto, Kimikazu; Fujimaki, Eriko
6078375 (09/154039)	US	6/20/2000 (9/16/1998)	Liquid crystal display device with wide viewing angle Matsumoto, Kimikazu; Fujimaki, Eriko
TWI144927 (TW087115488)	TW	11/1/2001 (9/17/1998)	Liquid crystal display device with wide viewing angle Matsumoto, Kimikazu; Fujimaki, Eriko
TWI164669 (TW087117847)	TW	9/21/2002 (10/28/1998)	In-plane switching type liquid crystal display Shibahara, Hideo

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6104463 (09/181338)	US	8/15/2000 (10/28/1998)	In-plane switching type liquid crystal display and method of operating the same Shibahara, Hideo
KR10-0303967 (KR10-1998-0046663)	KR	7/16/2002 (10/31/1998)	In-plane switching type liquid crystal display device and a method for operating the device for reducing uneven display by installing a chromaticity controller for controlling the chromaticity of the backlight Shibahara, Hideo
JP4775824 (JP2008-268528)	JP	7/8/2011 (10/17/2008)	Microlens array substrate, liquid crystal display element, liquid crystal display, and liquid crystal projector Matsushima, Hitoshi; Uehara, Shinichi; Sato, Yuko; Sumiyoshi, Ken; Kaneko, Setsuo
JP4735867 (JP2008-268529)	JP	5/13/2011 (10/17/2008)	Master die for microlens array Matsushima, Hitoshi; Uehara, Shinichi; Sato, Yuko; Sumiyoshi, Ken; Kaneko, Setsuo
7525732 (11/843677)	US	4/28/2009 (8/23/2007)	Method for forming finely-structured parts, finely-structured parts formed thereby, and product using such finely-structured part Uehara, Shin-ichi; Sato, Yuko; Sumiyoshi, Ken; Kaneko, Setsuo; Matsushima, Jin

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6172730 (09/262867)	US	1/9/2001 (3/5/1999)	Liquid crystal display apparatus having stepped section in glass substrate Fujita, Akira
6480254 (09/667786)	US	11/12/2002 (9/22/2000)	Liquid crystal display apparatus having stepped section in glass substrate Fujita, Akira
JP3482908 (JP11-145768)	JP	10/17/2003 (5/26/1999)	Driving circuit, driving circuit system, biasing circuit, and driving circuit device Tsuchi, Hiroshi
6624669 (09/578287)	US	9/23/2003 (5/25/2000)	Drive circuit and drive circuit system for capacitive load Tsuchi, Hiroshi
KR10-0385780 (KR10-2000-0028753)	KR	5/19/2003 (5/26/2000)	Driving circuit, driving circuit system, biasing circuit, and driving circuit device Tsuchi, Hiroshi
TWI174546 (TW089110429)	TW	3/21/2003 (5/26/2000)	Drive circuit and drive circuit system for capacitive load Tsuchi, Hiroshi
6618204 (10/254493)	US	9/9/2003 (9/25/2002)	Light modulator, light source using the light modulator, display apparatus using the light modulator, and method for driving the light modulator Takatori, Ken-ichi
6618027 (10/254710)	US	9/9/2003 (9/25/2002)	Light modulator, light source using the light modulator, display apparatus using the light modulator, and method for driving the light modulator Takatori, Ken-ichi

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JP3415602 (JP2001-157209)	JP	4/4/2003 (5/25/2001)	Method for forming pattern Kido, Shusaku
KR10-0480458 (KR10-2001-0036792)	KR	3/23/2005 (6/26/2001)	Method for forming pattern Kido, Shusaku
KR10-0500676 (KR10-2004-0058305)	KR	7/1/2005 (7/26/2004)	Method for forming pattern to precisely control resist pattern after resist pattern is varied in dimension Kido, Shusaku
TWI162398 (TW090115660)	TW	9/1/2002 (6/26/2001)	Method of forming a pattern and semiconductor device produced by this method Kido, Shusaku
6707107 (09/888442)	US	3/16/2004 (6/26/2001)	Method of deforming a pattern and semiconductor device formed by utilizing deformed pattern Kido, Shusaku
7060623 (10/201132)	US	6/13/2006 (7/24/2002)	Method of deforming a pattern and semiconductor device formed by utilizing deformed pattern Kido, Shusaku
6953976 (10/300735)	US	10/11/2005 (11/21/2002)	Method of deforming a pattern and semiconductor device formed by utilizing deformed pattern Kido, Shusaku
6977422 (10/778038)	US	12/20/2005 (2/17/2004)	Method of deforming a pattern and semiconductor device formed by utilizing deformed pattern Kido, Shusaku

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7030467 (10/778039)	US	4/18/2006 (2/17/2004)	Method of deforming a pattern and semiconductor device formed by utilizing deformed pattern Kido, Shusaku
6949766 (10/778040)	US	9/27/2005 (2/17/2004)	Method of deforming a pattern and semiconductor device formed by utilizing deformed pattern Kido, Shusaku
7554164 (11/187978)	US	6/30/2009 (7/25/2005)	Semiconductor device having a gap between a gate electrode and a dummy gate electrode Kido, Shusaku
6791145 (10/778041)	US	9/14/2004 (2/17/2004)	Semiconductor device formed by utilizing deformed pattern Kido, Shusaku
JP3394483 (JP11-325739)	JP	4/7/2003 (11/16/1999)	Thin-film transistor substrate and manufacturing method therefor Kato, Takeshi; Motojima, Hideto
GB2362509 (GB0027792.1)	GB	9/10/2002 (11/14/2000)	Thin film transistor and fabrication method of the same Kato, Tsuyoshi; Motoshima, Hideto
KR10-0348647 (KR10-2000-0067437)	KR	7/31/2002 (11/14/2000)	Thin-film transistor substrate and manufacturing method therefor Kato, Tsuyoshi; Motosjima, Hideto

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TWI189748 (TW089124125)	TW	11/1/2003 (11/14/2000)	Thin film transistor and fabrication method of the same Katoh, Tsuoyoshi; Motoshima, Hideto
6730970 (09/711504)	US	5/4/2004 (11/14/2000)	Thin film transistor and fabrication method of the same Katoh, Tsuoyoshi; Motoshima, Hideto
7049184 (10/838368)	US	5/23/2006 (5/5/2004)	Semiconductor thin film, thin film transistor, method for manufacturing same, and manufacturing equipment of semiconductor thin film Tanabe, Hiroshi
8377805 (13/359406)	US	2/19/2013 (1/26/2012)	Semiconductor thin film, thin film transistor, method for manufacturing same, and manufacturing equipment of semiconductor thin film Tanabe, Hiroshi
8710507 (13/761585)	US	4/29/2014 (2/7/2013)	Semiconductor thin film, thin film transistor, method for manufacturing same, and manufacturing equipment of semiconductor thin film Tanabe, Hiroshi
JP4274127 (JP2005-012465)	JP	3/13/2009 (1/20/2005)	TFT, TFT manufacturing method and image input device using TFT Tanabe, Hiroshi; Haga, Hiroshi

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7138303 (10/815393)	US	11/21/2006 (4/1/2004)	Method for manufacturing a thin film transistor having high mobility and high on-current Tanabe, Hiroshi; Haga, Hiroshi
7285809 (11/410184)	US	10/23/2007 (4/24/2006)	Thin film transistor having high mobility and high on-current Tanabe, Hiroshi; Haga, Hiroshi
KR10-0264757 (KR10-1998-0002500)	KR	6/5/2000 (1/30/1998)	Active matrix LCD and method of producing the same Tanaka, Hiroaki
6665023 (09/323492)	US	12/16/2003 (6/1/1999)	Active matrix liquid crystal display device having particular capacitance patterns Watanabe, Makoto; Watanabe, Takahiko
6020214 (09/116213)	US	2/1/2000 (7/16/1998)	Method for manufacturing thin film transistor array substrate Watanabe, Takahiko; Sukegawa, Osamu
6137465 (09/195617)	US	10/24/2000 (11/19/1998)	Drive circuit for a LCD device Sekine, Hiroyuki; Okumura, Fujio
7079101 (09/290579)	US	7/18/2006 (4/13/1999)	Liquid crystal display device and driving method therefor Asada, Hideki
6567136 (09/461001)	US	5/20/2003 (12/14/1999)	Liquid crystal display device Sakuramoto, Tamaki; Okumura, Hiroshi

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6597414 (09/513923)	US	7/22/2003 (2/28/2000)	Horizontal electric-field type LCD device Hasegawa, Fumio
JP3179410 (JP10-150821)	JP	4/13/2001 (6/1/1998)	Liquid crystal display device Matsuyama, Hiroaki; Kobayashi, Kazumi; Hirai, Yoshihiko; Ishii, Toshiya; Murai, Hideya; Suzuki, Shigeyoshi
TWI134010 (TW088108722)	TW	5/28/2001 (5/27/1999)	Liquid crystal display device Matsuyama, Hiroaki; Kobayashi, Kazumi; Hirai, Yoshihiko; Ishii, Toshiya; Murai, Hideya; Suzuki, Shigeyoshi
6300996 (09/321454)	US	10/9/2001 (5/27/1999)	Liquid crystal display apparatus Matsuyama, Hiroaki; Kobayashi, Kazumi; Hirai, Yoshihiko; Ishii, Toshiya; Murai, Hideya; Suzuki, Masayoshi
KR10-0339896 (KR10-1999-0019785)	KR	5/27/2002 (5/31/1999)	Liquid crystal display apparatus Gobayasi, Gazmi; Hirai, Yoshiko; Ishii, Doshiya; Masyama, Hiroyaki; Murai, Hideya; Szuki, Amasayoshi
5875009 (08/912790)	US	2/23/1999 (8/18/1997)	Sequential staggered type thin film transistor Shibahara, Hideo
JP3161528 (JP10-252663)	JP	2/23/2001 (9/7/1998)	Liquid crystal display panel Ihara, Hiroshi
TWI249069 (TW088115095)	TW	2/11/2006 (9/2/1999)	Liquid crystal display device Ihara, Hirofumi

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KR10-0374435 (KR10-1999-0037827)	KR	2/19/2003 (9/7/1999)	Liquid crystal display device and method of manufacturing the same Ihara, Hirofumi
6268898 (09/391151)	US	7/31/2001 (9/7/1999)	Liquid crystal display device and method of manufacturing the same Ihara, Hirofumi
TWI149753 (TW087116368)	TW	1/21/2002 (10/1/1998)	Liquid crystal display thin film transistor array and method of fabricating the same Tani, Masatoshi
KR10-0307987 (KR10-1998-0041995)	KR	8/27/2001 (10/8/1998)	Thin-film transistor arrays for liquid crystal display device and manufacturing method Tani, Masatoshi
5929947 (09/168085)	US	7/27/1999 (10/8/1998)	Liquid crystal display thin film transistor array with redundant film formed over a contact hole and method of fabricating the same Tani, Masatoshi
6441877 (09/413535)	US	8/27/2002 (10/6/1999)	Active matrix type liquid crystal display device and method of forming the same Watanabe, Takahiko
JP3147104 (JP10-315692)	JP	1/12/2001 (11/6/1998)	Active matrix type liquid crystal display device and driving method therefor Sekine, Hiroyuki
TWI184523 (TW088119444)	TW	8/21/2003 (11/5/1999)	Active matrix liquid crystal display apparatus and its driving method Sekine, Hiroyuki

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KR10-0325913 (KR10-1999-0049092)	KR	2/14/2002 (11/6/1999)	Liquid crystal display device Sekine, Hiroyuki
6407728 (09/435968)	US	6/18/2002 (11/8/1999)	Active matrix liquid crystal display device having signal selectors and method of driving the same Sekine, Hiroyuki
JP3185778 (JP11-032847)	JP	5/11/2001 (2/10/1999)	Active matrix type liquid crystal display device, its manufacture and its driving method Watanabe, Makoto; Watanabe, Takahiko
TWI165847 (TW089102090)	TW	11/1/2002 (2/9/2000)	Active matrix type liquid crystal display device, its manufacture and its driving method Watanabe, Makoto; Watanabe, Takahiko
KR10-0375897 (KR10-2000-0006122)	KR	2/28/2003 (2/10/2000)	Active matrix lcd, manufacturing method therefor and driving method therefor Watanabe, Makoto; Watanabe, Takahiko
6563481 (09/501685)	US	5/13/2003 (2/10/2000)	Active matrix liquid crystal display device, method of manufacturing the same, and method of driving the same Watanabe, Makoto; Watanabe, Takahiko
TWI152464 (TW087119765)	TW	3/21/2002 (11/27/1998)	Active matrix liquid-crystal display device Matsumoto, Kimikazu; Nishida, Shinichi; Shibahara, Hideo

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KR10-0321270 (KR10-1998-0051439)	KR	1/7/2002 (11/28/1998)	Active matrix liquid crystal display device Matsumoto, Kimikazu; Nishida, Shinichi; Shibahara, Hideo
JP3327252 (JP11-139963)	JP	7/12/2002 (5/20/1999)	Tape carrier, manufacture thereof, TCP, and liquid crystal display Ato, Kisho
KR10-0367320 (KR10-2000-0026703)	KR	12/24/2002 (5/18/2000)	Tape carrier, manufacture thereof, TCP, and liquid crystal display Atou, Noriaki
TWI147570 (TW089109654)	TW	12/21/2001 (5/19/2000)	Tape carrier with high flexibility and high density connection wire pattern Atou, Noriaki
6633002 (09/575092)	US	10/14/2003 (5/19/2000)	Tape carrier having high flexibility with high density wiring patterns Atou, Noriaki
JP3482910 (JP11-149078)	JP	10/17/2003 (5/28/1999)	Scanning circuit Sato, Tetsushi; Sekine, Hiroyuki
TWI162272 (TW089108255)	TW	9/1/2002 (5/1/2000)	Scanning circuit Sato, Tetsushi; Sekine, Hiroyuki
KR10-0371505 (KR10-2000-0028174)	KR	1/24/2003 (5/24/2000)	Scan circuit Sato, Tetsushi; Sekine, Hiroyuki
6876352 (09/577843)	US	4/5/2005 (5/25/2000)	Scanning circuit Sato, Tetsushi; Sekine, Hiroyuki

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<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
JP3451583 (JP11-180309)	JP	7/18/2003 (6/25/1999)	Clamp circuit for liquid crystal display device Shiki, Tatsuya
TWI159982 (TW089112297)	TW	7/11/2002 (6/22/2000)	Clamping circuit for liquid crystal display device Shiki, Tatsuya
6657619 (09/602182)	US	12/2/2003 (6/22/2000)	Clamping circuit for liquid crystal display device Shiki, Tatsuya
KR10-0378853 (KR10-2000-0034731)	KR	3/24/2003 (6/23/2000)	Clamp circuit for liquid crystal display device Shiki, Tatsuya
6580486 (09/604610)	US	6/17/2003 (6/27/2000)	Active matrix liquid crystal display device having electrostatic shielding layer between data lines Sekine, Hiroyuki
6669809 (09/791793)	US	12/30/2003 (2/26/2001)	Apparatus for removing a coating film Hashimoto, Yoshiaki; Sato, Yasuyuki
JP3264270 (JP11-210754)	JP	12/28/2001 (7/26/1999)	Liquid crystal display device Watanabe, Makoto; Watanabe, Takahiko
TWI180112 (TW089114855)	TW	6/21/2003 (7/25/2000)	Active-matrix in-plane switching mode LCD panel Watanabe, Makoto; Watanabe, Takahiko
KR10-0331913 (KR10-2000-0042942)	KR	3/26/2002 (7/26/2000)	Liquid crystal display device Watanabe, Makoto; Watanabe, Takahiko

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6646691 (09/626114)	US	11/11/2003 (7/26/2000)	Active-matrix in-plane switching mode LCD panel having multiple common electrode voltage sources Watanabe, Makoto; Watanabe, Takahiko
6469754 (09/654624)	US	10/22/2002 (9/5/2000)	Liquid-crystal display with scanner Okumura, Fujio
6429910 (09/666819)	US	8/6/2002 (9/21/2000)	Liquid crystal display device and method for repairing breakage of circuit lines thereof Hirata, Tomoya; Miyahara, Tae
6788355 (09/695212)	US	9/7/2004 (10/25/2000)	Active matrix LCD panel Ihida, Satoshi; Yamaguchi, Hiroataka; Tanaka, Hiroaki; Hayase, Takasuke; Kanou, Hiroshi; Kaneko, Wakahiko; Miyahara, Tae; Sakamoto, Michiaki; Nakata, Shinichi
6661401 (09/711632)	US	12/9/2003 (11/13/2000)	Circuit for driving a liquid crystal display and method for driving the same circuit Sekine, Hiroyuki
6600172 (09/718603)	US	7/29/2003 (11/22/2000)	Image sensor and method of fabricating the same Matsuno, Fumihiko
JP3266119 (JP10-344855)	JP	1/11/2002 (11/19/1998)	Liquid crystal display device and video data transfer method Hori, Yoshihiko

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TWI172615 (TW088120125)	TW	2/21/2003 (11/18/1999)	Liquid crystal display device and method for transferring image data Hori, Yoshihiko
6407730 (09/442455)	US	6/18/2002 (11/18/1999)	Liquid crystal display device and method for transferring image data Hori, Yoshihiko
KR10-0313210 (KR10-1999-0051548)	KR	10/17/2001 (11/19/1999)	Liquid crystal display device and method for transferring image data Hori, Yoshihiko
6437370 (09/503176)	US	8/20/2002 (2/14/2000)	Image sensor structure and manufacturing process therefor Matsuno, Fumihiko
JP3512665 (JP11-055478)	JP	1/16/2004 (3/3/1999)	Color liquid crystal panel and its manufacture Okamoto, Mamoru; Yamamoto, Yuji; Sakamoto, Michiaki; Nakada, Shinichi; Yoshikawa, Chikanori; Watanabe, Takahiko; Ihara, Hiroshi
6429917 (09/513758)	US	8/6/2002 (2/25/2000)	Color liquid-crystal panel having a frame-shaped black matrix and manufacturing process therefor Okamoto, Mamoru; Yamamoto, Yuji; Sakamoto, Michiaki; Nakata, Shinichi; Kikkawa, Hironori; Watanabe, Takahiko; Ihara, Hirofumi

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JP3544489 (JP11-112008)	JP	4/16/2004 (4/20/1999)	Liquid crystal display device and its manufacture Yoshikawa, Shuken; Watanabe, Takahiko; Ihara, Hiroshi; Nakada, Shinichi; Okamoto, Mamoru; Yamamoto, Yuji; Sakamoto, Michiaki
KR10-0371795 (KR10-2000-0020798)	KR	1/28/2003 (4/19/2000)	Liquid crystal display device and its manufacture Ihara, Hiroshi; Nakada, Shinichi; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Yamamoto, Yuji; Yoshikawa, Shuken;
6330043 (09/552035)	US	12/11/2001 (4/19/2000)	Liquid crystal display device and method of fabrication TFT panel Kikkawa, Hironori; Watanabe, Takahiko; Ihara, Hirofumi; Nakata, Shinichi; Okamoto, Mamoru; Yamamoto, Yuji; Sakamoto, Michiaki
TWI180819 (TW089107446)	TW	7/1/2003 (4/20/2000)	Liquid crystal display device and method of fabricating TFT panel Kikkawa, Hironori; Watanabe, Takahiko; Ihara, Hirofumi; Nakata, Shinichi; Okamoto, Mamoru; Yuji, Yamamoto; Sakamoto, Michiaki
JP3803510 (JP11-145316)	JP	5/12/2006 (5/25/1999)	Liquid crystal display panel Sugimoto, Mitsuhiro
JP3680849 (JP2003-301185)	JP	5/27/2005 (8/26/2003)	Liquid crystal display panel Sugimoto, Mitsuhiro

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TWI165651 (TW089109335)	TW	10/21/2002 (5/16/2000)	Liquid crystal panel Sugimoto, Mitsuhiro
KR10-0367417 (KR10-2000-0028501)	KR	12/24/2002 (5/25/2000)	Liquid crystal display panel Sugimoto, Mitsuhiro
6486936 (09/579321)	US	11/26/2002 (5/25/2000)	Liquid crystal panel with seal containing hard and soft spacers Sugimoto, Mitsuhiro
JP3838818 (JP11-171294)	JP	8/11/2006 (6/17/1999)	Liquid crystal display panel and its production Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hiroshi; Yoshikawa, Shuken; Nakada, Shinichi
TWI254179 (TW089111852)	TW	5/1/2006 (6/16/2000)	Liquid-crystal display panel and method for manufacturing same Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hirofumi; Kikkawa, Hironori; Nakata, Shinichi
6870592 (09/594721)	US	3/22/2005 (6/16/2000)	Liquid-crystal display panel with spacer in pixel electrode contact hole and method for manufacturing same Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hirofumi; Kikkawa, Hironori; Nakata, Shinichi

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KR10-0386826 (KR10-2000-0033502)	KR	5/26/2003 (6/17/2000)	Liquid crystal display panel and its production Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki; Watanabe, Takahiko; Ihara, Hiroshi; Yoshikawa, Shuken; Nakada, Shinichi
JP3539330 (JP2000-035827)	JP	4/2/2004 (2/14/2000)	Liquid crystal display panel and its manufacturing method Sato, Yuko; Sugimoto, Mitsuhiro
6549259 (09/780382)	US	4/15/2003 (2/12/2001)	Liquid crystal display panel and fabrication method of the same Sato, Yuko; Sugimoto, Mitsuhiro
KR10-0504683 (KR10-2001-0007007)	KR	7/21/2005 (2/13/2001)	Liquid crystal display panel and its manufacturing method Sato, Yuko; Sugimoto, Mitsuhiro
JP3792485 (JP2000-166318)	JP	4/14/2006 (6/2/2000)	Active matrix type liquid crystal display device Sakamoto, Michiaki; Maruyama, Muneo; Yamamoto, Yuji; Okamoto, Mamoru
TWI291070 (TW090113101)	TW	12/11/2007 (5/30/2001)	Active matrix liquid crystal display device Sakamoto, Michiaki; Maruyama, Muneo; Yamamoto, Yuji; Okamoto, Mamoru

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6822704 (09/870749)	US	11/23/2004 (6/1/2001)	Active matrix liquid crystal display device Sakamoto, Michiaki; Maruyama, Muneo; Yamamoto, Yuji; Okamoto, Mamoru
7224414 (10/761354)	US	5/29/2007 (1/22/2004)	Active matrix liquid crystal display device Sakamoto, Michiaki; Maruyama, Muneo; Yamamoto, Yuji; Okamoto, Mamoru
KR10-0413577 (KR10-2001-0030947)	KR	12/18/2003 (6/2/2001)	Active matrix liquid crystal display device Maruyama, Muneo; Okamoto, Mamoru; Sakamoto, Michiaki; Yamamoto, Yuji
TWI288260 (TW090116361)	TW	10/11/2007 (7/4/2001)	Reflector formed with highly reliable conductive pattern, process for fabricating reflector and liquid crystal display unit using the same Sato, Yasuhiro; Kaneko, Hideki; Nishiyama, Michiaki
7136123 (10/985003)	US	11/14/2006 (11/10/2004)	Reflector formed with highly reliable conductive pattern, process for fabricating reflector and liquid crystal display unit using the same Sato, Yasuhiro; Kaneko, Hideki; Nishiyama, Michiaki
TWI276901 (TW089108890)	TW	3/21/2007 (5/10/2000)	LCD device having test contact pads Ukita, Tooru

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6930744 (09/568710)	US	8/16/2005 (5/11/2000)	LCD device having test contact pads Ukita, Tooru
6608664 (09/577702)	US	8/19/2003 (5/23/2000)	Vibration-proof liquid crystal display having mounting end regions of lower rigidity Hasegawa, Fumio
TWI174315 (TW089110010)	TW	3/21/2003 (5/24/2000)	Liquid crystal display Hasegawa, Fumio
KR10-0374371 (KR10-2000-0028246)	KR	2/19/2003 (5/25/2000)	LIQUID CRYSTAL DISPLAY Hasegawa, Fumio
6760081 (09/577734)	US	7/6/2004 (5/23/2000)	Liquid crystal display device having uniform feedthrough voltage components Takagi, Kouji
TWI266941 (TW089110233)	TW	11/21/2006 (5/26/2000)	A liquid-crystal display apparatus and a method for manufacturing same Hosoyamada, Shunichi
6414740 (09/583730)	US	7/2/2002 (5/31/2000)	LCD having temperature detection elements provided on an active-substrate Hosoyamada, Shunichi
KR10-0386733 (KR10-2000-0030180)	KR	5/26/2003 (6/1/2000)	Liquid crystal display device, and manufacturing method of liquid crystal display device Hosoyamada, Shunichi
TWI253535 (TW089110556)	TW	4/21/2006 (5/31/2000)	LCD panel in which signal line terminals have slits to allow inspection of alignment of the terminals and the TCP leads Fujita, Akira

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6633361 (09/587061)	US	10/14/2003 (6/2/2000)	LCD panel in which signal line terminals have slits to allow inspection of alignment of the terminals and the TCP leads Fujita, Akira
KR10-0335453 (KR10-2000-0030571)	KR	4/23/2002 (6/3/2000)	LCD panel unit for a LCD device Fujita, Akira
6486931 (09/592914)	US	11/26/2002 (6/13/2000)	LCD optical guide plate with a roughened back surface having projections that support a reflecting sheet Ueda, Shoichi
6542207 (09/599637)	US	4/1/2003 (6/23/2000)	Liquid crystal display Yoshizawa, Seiji
KR10-0376348 (KR10-2000-0035553)	KR	3/5/2003 (6/27/2000)	Liquid crystal display device reducing pixel error and manufacture method of the same Ihara, Hirohumi; Kaneko, Wakahiko; Iida, Takayasu
TWI250349 (TW089112937)	TW	3/1/2006 (6/29/2000)	Liquid crystal display device with less pixel error and method of manufacturing the same Ihara, Hiroshi; Kaneko, Wakahiko; Iida, Takayasu
7088401 (09/609169)	US	8/8/2006 (6/30/2000)	Liquid crystal display device with less pixel error and method of manufacturing the same Ihara, Hirofumi; Kaneko, Wakahiko; Iida, Takayasu

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TWI190424 (TW089102883)	TW	11/11/2003 (2/19/2000)	Thin-film transistor array and its manufacture Matsumoto, Seiichi
KR10-0399256 (KR10-2000-0009205)	KR	9/15/2003 (2/24/2000)	Thin-film transistor array and its manufacture Matsumoto, Seiichi
6249011 (09/513925)	US	6/19/2001 (2/28/2000)	Thin film transistor array with light shield layer Matsumoto, Seiichi
6352911 (09/619675)	US	3/5/2002 (7/19/2000)	Thin-film transistor array and method for producing the same Matsumoto, Seiichi
KR10-0445776 (KR10-2000-0011930)	KR	8/16/2004 (3/10/2000)	Liquid crystal display and manufacturing method thereof Kuroba, Shoichi
TWI205279 (TW089104423)	TW	6/21/2004 (3/10/2000)	Liquid crystal display and manufacturing method thereof Kuroha, Shoichi
6760089 (09/522609)	US	7/6/2004 (3/10/2000)	Liquid crystal display and manufacturing method thereof Kuroha, Shoichi
6954250 (10/882286)	US	10/11/2005 (7/2/2004)	Liquid crystal display and manufacturing method thereof Kuroha, Shoichi
TWI182529 (TW089105200)	TW	7/21/2003 (3/22/2000)	LCD and manufacturing method thereof Matsuyama, Hiroaki; Hirai, Yoshihiko

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6714271 (09/533075)	US	3/30/2004 (3/22/2000)	Liquid crystal display having an in a pixel electrode along a boundary of differently oriented regions Matsuyama, Hiroaki; Hirai, Yoshihiko
KR10-0358872 (KR10-2000-0014704)	KR	10/16/2002 (3/23/2000)	LCD and manufacturing method thereof Hirai, Yoshihiko; Matsuyama, Hiroaki
6809712 (09/802215)	US	10/26/2004 (3/8/2001)	Drive circuit of liquid crystal display, having clip circuit before polarity inversion circuit Takeda, Hiroshi
6778249 (09/621460)	US	8/17/2004 (7/21/2000)	Liquid-crystal display element and method for manufacturing same Kamosawa, Hirofumi; Shimizu, Takeya
6690446 (09/689901)	US	2/10/2004 (10/13/2000)	Color liquid crystal display panel, manufacturing method of the same, and liquid crystal display Okamoto, Mamoru; Yamamoto, Yuji; Sakamoto, Michiaki; Kikkawa, Hironori
6525786 (09/711043)	US	2/25/2003 (11/13/2000)	Transverse electric liquid crystal display device Ono, Shin-ichirou
TWI189333 (TW089124340)	TW	10/11/2003 (11/16/2000)	Transverse electric liquid crystal display device Ono, Shin Ichirou

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KR10-0400714 (KR10-2000-0068692)	KR	9/24/2003 (11/18/2000)	Transverse electric field liquid crystal display device Ono, Shinichiro
7202844 (09/726721)	US	4/10/2007 (11/30/2000)	Liquid crystal display controller and liquid crystal display Nakamigawa, Kazuhiro
6586873 (09/839118)	US	7/1/2003 (4/23/2001)	Display panel module with improved bonding structure and method of forming the same Mizutani, Kazuhiro; Kitazume, Eiichi
KR10-0404064 (KR10-2001-0021938)	KR	10/21/2003 (4/24/2001)	Display panel module having improved bonding structure and manufacturing method thereof Kitazume, Eiichi; Mizutani, Kazuhiro
6468840 (09/841074)	US	10/22/2002 (4/25/2001)	Active matrix substrate and manufacturing method thereof Tanaka, Hiroaki; Uchida, Hiroyuki
6800872 (10/235493)	US	10/5/2004 (9/6/2002)	Active matrix thin film transistor Tanaka, Hiroaki; Uchida, Hiroyuki
TW1178482 (TW090110057)	TW	5/21/2003 (4/26/2001)	Active matrix substrate and manufacturing method thereof Tanaka, Hiroaki; Uchida, Hiroyuki

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KR10-0445791 (KR10-2001-0023218)	KR	8/16/2004 (4/28/2001)	Active matrix substrate and manufacturing method thereof Tanaka, Hiroaki; Uchida, Hiroyuki
JP4410912 (JP2000-170057)	JP	11/20/2009 (6/7/2000)	Electrostatic protection circuit Uchida, Hiroyuki
TWI195654 (TW090113333)	TW	2/1/2004 (6/1/2001)	Surge protection circuit for semiconductor devices Uchida, Hiroyuki
KR10-0463411 (KR10-2001-0031517)	KR	12/15/2004 (6/5/2001)	Surge protection circuit for a semiconductor display panel Uchida, Hiroyuki
6812528 (09/874296)	US	11/2/2004 (6/6/2001)	Surge protection circuit for semiconductor devices Uchida, Hiroyuki
6621551 (09/892836)	US	9/16/2003 (6/27/2001)	Method and system for fabricating a liquid crystal display by optically detecting anisotropic angular misalignment Matsuzawa, Tadashi
TWI293397 (TW090116915)	TW	2/11/2008 (7/10/2001)	Liquid crystal display unit having pixel electrode encircled with partition wall and process for fabrication thereof Matsumoto, Kimikazu
6657699 (09/901034)	US	12/2/2003 (7/10/2001)	Liquid crystal display unit having pixel electrode encircled with partition wall and process for fabrication thereof Matsumoto, Kimikazu

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6740596 (09/903244)	US	5/25/2004 (7/11/2001)	Manufacturing method of active matrix substrate Hayase, Takasuke; Tanaka, Hiroaki; Kido, Shusaku; Harano, Toshihiko
6683661 (09/924059)	US	1/27/2004 (8/8/2001)	Liquid crystal display and image display device using the same Matsumoto, Seiichi
7518586 (09/925601)	US	4/14/2009 (8/10/2001)	Method and circuit for driving liquid crystal display and image display device Hosoyamada, Shunichi
KR10-0485506 (KR10-2001-0048360)	KR	4/18/2005 (8/10/2001)	Liquid crystal display device and method of manufacturing thereof Kikkawa, Hironori; Maruyama, Muneo; Okamoto, Mamoru; Sakamoto, Michiaki; Yamamoto, Yuji
TWI202337 (TW090119732)	TW	5/11/2004 (8/10/2001)	Liquid crystal display device and method for manufacturing the same Kikkawa, Hironori; Muneo, Maruyama; Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki
6667777 (09/927501)	US	12/23/2003 (8/10/2001)	Liquid crystal display device and method for manufacturing the same Kikkawa, Hironori; Maruyama, Muneo; Yamamoto, Yuji; Okamoto, Mamoru; Sakamoto, Michiaki

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KR10-0458994 (KR10-2001-0055783)	KR	11/19/2004 (9/11/2001)	Liquid crystal display device and method for manufacturing the same Matsumoto, Kimikazu
TWI200887 (TW090122554)	TW	4/11/2004 (9/11/2001)	Liquid crystal display and method of manufacturing same Matsumoto, Kimikazu
6873385 (09/949633)	US	3/29/2005 (9/12/2001)	Liquid crystal display with selectively placed barriers Matsumoto, Kimikazu
JP4966444 (JP2000-342844)	JP	4/6/2012 (11/10/2000)	TFT liquid crystal display device Tsubo, Yumiko
6831295 (10/008973)	US	12/14/2004 (11/8/2001)	TFT-LCD device having a reduced feed-through voltage Tsubo, Yumiko
KR10-0535531 (KR10-2001-0069662)	KR	12/2/2005 (11/9/2001)	TFT liquid crystal display device Tsubo, Yumiko
TWI185821 (TW090127998)	TW	9/1/2003 (11/9/2001)	TFT-LCD device having a reduced feed-through voltage Tsubo, Yumiko
6559917 (09/987088)	US	5/6/2003 (11/13/2001)	Pattern formation method using reflow enhancement layer and method for manufacturing reflective type liquid crystal display device using the same Ikeno, Hidenori
6583556 (09/994807)	US	6/24/2003 (11/28/2001)	Flat-type fluorescent lamp for illumination unit and liquid crystal device Oishi, Toshiya; Fujishiro, Fumihiko; Kanatsu, Tsutomu

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TWI285390 (TW090129752)	TW	8/11/2007 (11/29/2001)	Flat-type fluorescent lamp for illumination unit and liquid crystal device Oishi, Toshiya; Fujishiro, Fumihiko; Kanatsu, Tsutomu
KR10-0485508 (KR10-2002-0007360)	KR	4/18/2005 (2/8/2002)	Liquid crystal display device and driving method thereof Yoshikawa, Fumitake
TWI189774 (TW091102723)	TW	11/1/2003 (2/8/2002)	Liquid crystal display device and driving method thereof Yoshikawa, Fumitake
7033951 (10/077226)	US	4/25/2006 (2/15/2002)	Process for forming pattern and method for producing liquid crystal display apparatus Kido, Shusaku
7226865 (11/268811)	US	6/5/2007 (11/7/2005)	Process for forming pattern and method for producing liquid crystal display apparatus Kido, Shusaku
TWI200892 (TW091103514)	TW	4/11/2004 (2/26/2002)	Liquid crystal display with control electrodes for preventing lateral leak of electric field Sakamoto, Michiaki; Hidehira, Masanobu; Okamoto, Mamoru
KR10-0498254 (KR10-2002-0010678)	KR	6/21/2005 (2/27/2002)	Liquid crystal display with control electrodes for preventing lateral leak of electric field Hidehira, Masanobu; Okamoto, Mamoru; Sakamoto, Michiaki

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7136116 (10/084355)	US	11/14/2006 (2/28/2002)	Liquid crystal display with control electrodes for preventing lateral leak of electric field Sakamoto, Michiaki; Hidehira, Masanobu; Okamoto, Mamoru
TWI312884 (TW091103004)	TW	8/1/2009 (2/21/2002)	Process for forming pattern Kido, Shusaku
6767694 (10/084848)	US	7/27/2004 (2/25/2002)	Process for forming pattern and method for producing liquid crystal display apparatus employing process for forming pattern Kido, Shusaku
KR10-0494431 (KR10-2002-0011286)	KR	6/1/2005 (3/4/2002)	Liquid crystal display Kasuga, Koji
TWI300499 (TW091104220)	TW	9/1/2008 (3/5/2002)	Lamp unit, light guide plate, liquid crystal display, and method for assembling them Kasuga, Koji
6894738 (10/087787)	US	5/17/2005 (3/5/2002)	Lamp unit, light guide plate, liquid crystal display, and method for assembling them Kasuga, Koji
CNZL02141106.9 (CN02141106.9)	CN	4/26/2006 (7/4/2002)	Lamp unit, light conducting board, LCD and method for assembling same Kasuga, Yasuji
CNZL02127598.X (CN02127598.X)	CN	7/6/2005 (8/2/2002)	Liquid crystal display and manufacturing method thereof Okamoto, Mamoru; Sakamoto, Michiaki; Hidehira, Akinobu

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6909418 (10/119024)	US	6/21/2005 (4/10/2002)	Image display apparatus Arai, Nobuhiro
JP4875248 (JP2001-117002)	JP	12/2/2011 (4/16/2001)	Liquid crystal display device and control circuit Ito, Masaatsu; Takami, Kazuhiko; Okuzono, Noboru
7030852 (10/122240)	US	4/18/2006 (4/16/2002)	Liquid crystal display unit having incoming pixel data rearrangement circuit Ito, Masahiro; Takami, Kazuhiko; Okuzono, Noboru
CNZL02140184.5 (CN02140184.5)	CN	12/3/2008 (7/3/2002)	LCD unit with input pixel data reregulating circuit Ito, Masahiro; Takami, Iichihiko; Okuzo, Nobori
KR10-0512396 (KR10-2002-0020575)	KR	8/29/2005 (4/16/2002)	Liquid crystal display device Kanatsu, Tsutomu; Ishida, Hiroshi
TWI182283 (TW091107717)	TW	7/21/2003 (4/16/2002)	Liquid crystal display device Kanatsu, Tsutomu; Ishida, Hiroshi
6867825 (10/122242)	US	3/15/2005 (4/16/2002)	Liquid crystal display device Kanatsu, Tsutomu; Ishida, Hiroshi
7057678 (10/123653)	US	6/6/2006 (4/16/2002)	Liquid crystal display device having backlight with return substrate above or below a portion of lamp tubes Ishida, Hiroshi; Kanatsu, Tsutomu
6811458 (10/142287)	US	11/2/2004 (5/9/2002)	Method for manufacturing liquid crystal display Kouya, Akiyoshi

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6707525 (10/226268)	US	3/16/2004 (8/23/2002)	Liquid crystal display device Miyazaki, Tatsuya
KR10-0491324 (KR10-2002-0051264)	KR	5/16/2005 (8/28/2002)	Liquid crystal display Miyazaki, Tatsuya
TWI200737 (TW091119626)	TW	4/11/2004 (8/28/2002)	Liquid crystal display device Miyazaki, Tatsuya
JP4771038 (JP2001-278590)	JP	7/1/2011 (9/13/2001)	Liquid crystal display device Sato, Hiroki
KR10-0491325 (KR10-2002-0055842)	KR	5/16/2005 (9/13/2002)	Liquid crystal display Sato, Hiroki
TWI225565 (TW091120967)	TW	12/21/2004 (9/13/2002)	Liquid crystal display (LCD) Sato, Hiroki
6870593 (10/242604)	US	3/22/2005 (9/13/2002)	Liquid crystal display cell with improved spacer structure Sato, Hiroki
TWI227361 (TW092121767)	TW	2/1/2005 (8/8/2003)	Substrate for liquid-crystal display device and fabrication method thereof Ishino, Takayuki
6982769 (10/638000)	US	1/3/2006 (8/8/2003)	Substrate for liquid-crystal display device and fabrication method thereof Ishino, Takayuki
KR10-0560587 (KR10-2003-0055209)	KR	3/7/2006 (8/9/2003)	Substrate for liquid-crystal display device and fabrication method thereof Ishino, Takayuki
6567327 (09/924609)	US	5/20/2003 (8/9/2001)	Driving circuit, charge/discharge circuit and the like Tsuchi, Hiroshi

Revised Exhibit A

<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
JP4491632 (JP2000-107245)	JP	4/16/2010 (4/7/2000)	Driving method for liquid crystal display device Hasegawa, Fumio; Aoki, Makoto
6831627 (09/826252)	US	12/14/2004 (4/4/2001)	Driving method for liquid crystal display Hasegawa, Fumio; Aoki, Makoto
6727874 (09/988189)	US	4/27/2004 (11/19/2001)	Driving circuit and driving method of color liquid crystal display, and color liquid crystal display device Okuzono, Noboru
7116297 (10/413458)	US	10/3/2006 (4/14/2003)	Liquid crystal display device and driving method for liquid crystal display device Koga, Koichi; Okuzono, Noboru; Yamaguchi, Machihiko
6747617 (09/714300)	US	6/8/2004 (11/16/2000)	Drive circuit for an organic EL apparatus Kawashima, Shingo
KR10-0454134 (KR10-2000-0068371)	KR	10/13/2004 (11/17/2000)	Organic EL driving circuit Kawashima, Shingo
TWI171629 (TW089103782)	TW	2/1/2003 (3/3/2000)	LCD panel and LCD device equipped therewith Nakajima, Keiichi
6587089 (09/518797)	US	7/1/2003 (3/3/2000)	LCD panel and LCD device equipped therewith Nakajima, Keiichi
KR10-0330068 (KR10-2000-0010871)	KR	3/13/2002 (3/4/2000)	LCD panel and LCD device equipped therewith Nakajima, Keiichi

Revised Exhibit A

Patent/Application Number	Country	Issue Date/ Filing Date	Title of Patent and Inventors
JP3147098 (JP10-227635)	JP	1/12/2001 (7/28/1998)	Driving circuit of liquid crystal display device Tsuchi, Hiroshi
KR10-0275651 (KR10-1998-0030359)	KR	9/22/2000 (7/28/1998)	Driver for liquid crystal display apparatus with no operational amplifier Tsuchi, Hiroshi
6127997 (09/123482)	US	10/3/2000 (7/28/1998)	Driver for liquid crystal display apparatus with no operational amplifier Tsuchi, Hiroshi
6380006 (09/878198)	US	4/30/2002 (6/12/2001)	Pattern formation method and method of manufacturing display using it Kido, Shusaku
6513943 (09/810542)	US	2/4/2003 (3/19/2001)	Backlight unit and display device using the same backlight unit Fukuyoshi, Hirokazu
TWI176526 (TW090106549)	TW	4/21/2003 (3/20/2001)	Backlight unit and display device using the same backlight unit Fukuyoshi, Hirokazu
KR10-0380769 (KR10-2001-0015112)	KR	4/4/2003 (3/23/2001)	Back-face light source and display using the same Fukuyoshi, Hirokazu
JP3353832 (JP11-312090)	JP	9/27/2002 (11/2/1999)	Method and apparatus for manufacture of thin-film transistor Takechi, Kazue
6579749 (09/440615)	US	6/17/2003 (11/15/1999)	Fabrication method and fabrication apparatus for thin film transistor Takechi, Kazushige

Revised Exhibit A

<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
KR10-0411442 (KR10-1999-0050989)	KR	12/3/2003 (11/17/1999)	Method for manufacturing thin film transistor and an apparatus for manufacturing the transistor Daketzi, Gazsige
6693697 (10/042144)	US	2/17/2004 (1/11/2002)	Active-matrix type liquid crystal display device having thick and thin overcoat layers layered over a black matrix Sakamoto, Michiaki; Yamamoto, Yuji; Okamoto, Mamoru; Kimura, Shigeru; Nakata, Shinichi; Kuroha, Shouichi; Hidehira, Masanobu; Horie, Yoshitaka; Ishino, Takayuki
TWI291067 (TW090111523)	TW	12/11/2007 (5/11/2001)	Fabrication method of liquid crystal display panel Sasaki, Takeshi
7145628 (09/855148)	US	12/5/2006 (5/14/2001)	Method for fabricating a liquid crystal panel comprising spacers having an initial size larger than an appropriate cell gap Sasaki, Takeshi
7123314 (10/871000)	US	10/17/2006 (6/21/2004)	Thin-film transistor with set trap level densities, and method of manufactures Matsunaga, Naoki; Sera, Kenji
6791656 (10/349937)	US	9/14/2004 (1/24/2003)	Liquid crystal display panel avoiding display unevenness and manufacturing method Akiyoshi Kouya

Revised Exhibit A

<u>Patent/Application Number</u>	<u>Country</u>	<u>Issue Date/ Filing Date</u>	<u>Title of Patent and Inventors</u>
TWI278697 (TW089119815)	TW	4/11/2007 (9/26/2000)	Liquid crystal display panel avoiding display unevenness and manufacturing method Akiyoshi Kouya
TWI269918 (TW089126848)	TW	1/1/2007 (12/14/2000)	LCD panel and method of fabricating same Takeshi Sasaki
6573972 (09/735806)	US	6/3/2003 (12/14/2000)	LCD panel and method of fabricating same Takeshi Sasaki
JP5038560 (JP2001-233256)	JP	7/13/2012 (8/1/2001)	Field-effect transistor, manufacturing method therefor, liquid crystal display device using the same and manufacturing method therefor Kenji Sera
6784456 (10/207765)	US	8/31/2004 (7/31/2002)	Field effect transistor as well as liquid crystal display using the same Kenji Sera

(b) any future reissues, reexaminations, extensions, continuations, continuing prosecution application, requests for continuing examinations, divisions, and registrations of any of the Patents;

(c) rights to apply in any or all countries of the world for future patents, certificates of invention, utility models, industrial design protections, design patent protections, or other future governmental grants or issuances of any type related to the Patents; and

(d) causes of action and enforcement rights of any kind under, or on account of, any of the Patents and/or any of the items described in either of the foregoing categories (b) or (c), including, without limitation, all causes of action, enforcement rights and all other rights to seek and obtain any other remedies of any kind for past, current and future infringement.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all future patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Assigned Patent Rights in

Exhibit B

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
NEC Corporation	04/18/2011
RECEIVING PARTY DATA	
Name:	Getner Foundation LLC
Street Address:	160 Greentree Drive, Suite 101
City:	Dover
State/Country:	DELAWARE
Postal Code:	19904
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	13111815
CORRESPONDENCE DATA	
Fax Number:	(216)696-8731
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	216-696-8730
Email:	rwardzala@thepatentattorneys.com
Correspondent Name:	Turocy & Watson, LLP
Address Line 1:	127 Public Square
Address Line 2:	57th Floor, Key Tower
Address Line 4:	Cleveland, OHIO 44114
ATTORNEY DOCKET NUMBER:	VNECP281USA
NAME OF SUBMITTER:	Thomas Watson
Total Attachments: 8 source=US NEC Getner Assignment#page1.tif source=US NEC Getner Assignment#page2.tif source=US NEC Getner Assignment#page3.tif source=US NEC Getner Assignment#page4.tif source=US NEC Getner Assignment#page5.tif source=US NEC Getner Assignment#page6.tif source=US NEC Getner Assignment#page7.tif source=US NEC Getner Assignment#page8.tif	

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(US)

Assignment

Whereas, NEC Corporation, a Japanese corporation, having a place of business at 7-1, Shiba 5-chome, Minato-ku, Tokyo 108-8001, Japan, hereinafter called Assignor, is the owner and assignee of record of the patents and pending applications listed on the attached Schedule A; and

Whereas, Getner Foundation LLC, having a place of business at 160 Greentree Drive, Suite 101, Dover, DE, 19904, U.S.A, hereinafter called Assignee, desires to acquire the Assignor's entire right, title, and interest in said patents and pending applications listed on the attached Schedule A;

Now therefore, for valuable consideration, receipt whereof is hereby acknowledged;

Assignor hereby sells, assigns and transfers to Assignee, its successors and assigns, its entire right, title and interest including rights to recover for past infringement in:

1. (a) the patents and pending applications listed on the attached Schedule A, (b) all continuations, divisions, reissues and continuations-in-part thereof and all other patent applications claiming priority rights from the items in category (a), and (c) all U.S. patents or other governmental grants or issuances that may be granted on any applications within the foregoing categories (a) and (b), the items in categories (a), (b), and (c) individually and collectively referred to hereafter as the "**Patents**";
2. all inventions claimed or described in the Patents (the "**Inventions**");
3. all non-United States patents, patent applications, and counterparts relating to any or all of the Inventions or the Patents, including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances ("**Foreign Rights**"), and including the right to file foreign applications directly in the name of Assignee, its successors and assigns;
4. the right to claim priority rights deriving from the Patents;
5. all causes of action and remedies related to the Patents, the Inventions, or Foreign Rights (including, without limitation, the right to sue for past, present, or future infringement, misappropriation or violation of rights related to any of the foregoing and the right to collect royalties and other payments under or on account of any of the foregoing); and


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6. any and all other rights and interests arising out of, in connection with, or in relation to, the Patents, the Inventions, or Foreign Rights.

IN WITNESS WHEREOF, Assignor has executed this Assignment by proper persons duly authorized.

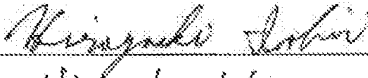
NEC Corporation

Date: April 18, 2011


Name: Kazuhiko Yamada
Title: General Manager,
Intellectual Asset Development Promotion Division

Witness:

Date: April 18, 2011


Hiroyuki Ishii

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Schedule A

US Patents

Application No.	Patent No.
09/711504	6730970
09/874296	6812528
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08/261120	5579184
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PATENT
REEL: 026312 FRAME: 0216

US Patent Applications

Application No.	Patent No.
12/381737	
12/504981	
12/609786	

RECORDED: 05/19/2011

**PATENT
REEL: 026312 FRAME: 0217**

Exhibit C

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
NEC Corporation	04/18/2011
RECEIVING PARTY DATA	
Name:	Getner Foundation LLC
Street Address:	160 Greentree Drive, Suite 101
City:	Dover
State/Country:	DELAWARE
Postal Code:	19904
PROPERTY NUMBERS Total: 464	
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Application Number:	10160071
Application Number:	10035222
Application Number:	10173518
Application Number:	09707816
Application Number:	10279211
Application Number:	11441213
Application Number:	10173922
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501526419

PATENT
 REEL: 026254 FRAME: 0381

OP \$18560.00 10160071

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PATENT
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Application Number:	10893298
Application Number:	09473274

CORRESPONDENCE DATA

Fax Number: (216)696-8731
Correspondence will be sent via US Mail when the fax attempt is unsuccessful.
Phone: 216-696-8730
Email: rwardzala@thepatentattorneys.com
Correspondent Name: Turocy & Watson, LLP

PATENT
REEL: 026254 FRAME: 0394

Address Line 1: 127 Public Square
Address Line 2: 57th Floor, Key Tower
Address Line 4: Cleveland, OHIO 44114

ATTORNEY DOCKET NUMBER:

VNEC MATTERS

NAME OF SUBMITTER:

Thomas E. Watson

Total Attachments: 8

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PATENT
REEL: 026254 FRAME: 0395

(US)

Assignment

Whereas, NEC Corporation, a Japanese corporation, having a place of business at 7-1, Shiba 5-chome, Minato-ku, Tokyo 108-8001, Japan, hereinafter called Assignor, is the owner and assignee of record of the patents and pending applications listed on the attached Schedule A; and

Whereas, Getner Foundation LLC, having a place of business at 160 Greentree Drive, Suite 101, Dover, DE, 19904, U.S.A, hereinafter called Assignee, desires to acquire the Assignor's entire right, title, and interest in said patents and pending applications listed on the attached Schedule A;

Now therefore, for valuable consideration, receipt whereof is hereby acknowledged;

Assignor hereby sells, assigns and transfers to Assignee, its successors and assigns, its entire right, title and interest including rights to recover for past infringement in:

1. (a) the patents and pending applications listed on the attached Schedule A, (b) all continuations, divisions, reissues and continuations-in-part thereof and all other patent applications claiming priority rights from the items in category (a), and (c) all U.S. patents or other governmental grants or issuances that may be granted on any applications within the foregoing categories (a) and (b), the items in categories (a), (b), and (c) individually and collectively referred to hereafter as the "**Patents**";
2. all inventions claimed or described in the Patents (the "**Inventions**");
3. all non-United States patents, patent applications, and counterparts relating to any or all of the Inventions or the Patents, including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances ("**Foreign Rights**"), and including the right to file foreign applications directly in the name of Assignee, its successors and assigns;
4. the right to claim priority rights deriving from the Patents;
5. all causes of action and remedies related to the Patents, the Inventions, or Foreign Rights (including, without limitation, the right to sue for past, present, or future infringement, misappropriation or violation of rights related to any of the foregoing and the right to collect royalties and other payments under or on account of any of the foregoing); and


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6. any and all other rights and interests arising out of, in connection with, or in relation to, the Patents, the Inventions, or Foreign Rights.

IN WITNESS WHEREOF, Assignor has executed this Assignment by proper persons duly authorized.


NEC Corporation

Date: April 18, 2011


Name: Kazuhiko Yamada
Title: General Manager,
Intellectual Asset Development Promotion Division

Witness:

Date: April 18, 2011


Hiroyuki Ishii

PATENT
REEL: 026254 FRAME: 0397

Schedule A

US Patents

Application No.	Patent No.
09/711504	6730970
09/874296	6812528
10/084648	6767694
10/077226	7033951
11/268811	7226865
10/160071	6566154
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PATENT
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