

4. This Court has personal jurisdiction over Defendant. Defendant regularly conducts business and has committed acts of patent infringement and/or has induced acts of patent infringement by others in this Judicial District and/or has contributed to patent infringement by others in this Judicial District, the State of Texas, and elsewhere in the United States.

5. Venue is proper in this Judicial District pursuant to 28 U.S.C. § 1391 because, among other things, Defendant is not a resident in the United States, and thus may be sued in any judicial district pursuant to 28 U.S.C. § 1391(c)(3).

6. Defendant is subject to this Court's jurisdiction pursuant to due process and/or the Texas Long Arm Statute due at least to its substantial business in this State and Judicial District, including (a) at least part of its past infringing activities, (b) regularly doing or soliciting business in Texas, and/or (c) engaging in persistent conduct and/or deriving substantial revenue from goods and services provided to customers in Texas.

PATENTS-IN-SUIT

7. On October 13, 2009, the United States Patent and Trademark Office duly and legally issued U.S. Patent No. 7,602,773 (the "'773 Patent") entitled "Transferring Data to a Target Device". A true and correct copy of the '773 Patent is available at: <https://patentimages.storage.googleapis.com/bf/a8/c2/fd0f41a4b7a690/US7602773.pdf>.

8. On January 5, 2010, the United States Patent and Trademark Office duly and legally issued U.S. Patent No. 7,643,476 (the "'476 Patent") entitled "Communication Protocols, Systems and Methods". A true and correct copy of the '476 Patent is available at: <https://patentimages.storage.googleapis.com/20/4f/bb/101d5c8c87039b/US7643476.pdf>.

9. On March 30, 2010, the United States Patent and Trademark Office duly and legally issued U.S. Patent No. 7,688,814 (the "'814 Patent") entitled Methods of Conveying Information

Using Fixed Sized Packets”. A true and correct copy of the ’814 Patent is available at: <https://patentimages.storage.googleapis.com/48/89/15/9514d2f5f05d39/US7688814.pdf>.

10. On June 22, 2010, the United States Patent and Trademark Office duly and legally issued U.S. Patent No. 7,742,473 (the “’473 Patent”) entitled “Accelerator Module”. A true and correct copy of the ’473 Patent is available at: <https://patentimages.storage.googleapis.com/d3/93/ca/537f61aef95435/US7742473.pdf>.

11. ServStor is the sole and exclusive owner of all right, title, and interest in the ’773 Patent, ’476 Patent, ’814 Patent, and the ’473 Patent (the “Patents-in-Suit”) and holds the exclusive right to take all actions necessary to enforce its rights to the Patent-in-Suit, including the filing of this patent infringement lawsuit. ServStor also has the right to recover all damages for past, present, and future infringement of the Patents-in-Suit and to seek injunctive relief as appropriate under the law.

FACTUAL ALLEGATIONS

12. The Patents-in-Suit generally cover systems and methods for routing data over a network.

13. The ’773 Patent, the ’476 Patent, and the ’814 Patent discloses methods and apparatuses for data transfer and communication, including packets, to disaggregated elements. The technology in the ’773 Patent, the ’476 Patent, and the ’814 Patent was developed by Charles Frank, Thomas Ludwig, Thomas Hanan, and William Babbitt. For example, this technology is implemented in microchips, servers, routers, ethernet switches, systems-on-a-chip (“SoCs”), and ASICs, among other products.

14. The ’473 Patent discloses methods and apparatuses for stateless storage accelerator modules comprising network interfaces and filter functions that enable the accelerated processing

of stateless protocols associated with network storage. The technology in the '473 Patent was developed by Mark Adams, Willam R. Babbitt, Jr., and Rochack Sharma. For example, this technology is implemented in microchips, servers, routers, ethernet switches, systems-on-a-chip ("SoCs"), and ASICs, among other products.

15. MediaTek has infringed and is continuing to infringe the Patents-in-Suit by making, using, offering to sell, selling, and/or importing network switches, routers, and software which implement the technology disclosed in the above patents-in-suit.

COUNT I
(Infringement of the '773 Patent)

16. Paragraphs 1 through 15 are incorporated by reference as if fully set forth herein.

17. ServStor has not licensed or otherwise authorized Defendant to make, use, offer for sale, sell, or import any products that embody the inventions of the '773 Patent.

18. Defendant has and continues to directly infringe the '773 Patent, either literally or under the doctrine of equivalents, without authority and in violation of 35 U.S.C. § 271, by making, using, offering to sell, selling, and/or importing into the United States products that satisfy each and every limitation of one or more claims of the '773 Patent. Such products include but are not limited to microchips, SoCs, and ASICs that communicatively couple an apparatus to a network, such as the MediaTek MT7620.

19. For example, Defendant has and continues to directly infringe at least claim 11 of the '773 Patent by making, using, offering to sell, selling, and/or importing into the United States products that include microchips, SoCs, and ASICs communicatively couple an apparatus to a network, such as the MediaTek MT7620.

20. For example, the MediaTek MT7620 comprises a network interface configured to communicatively couple the apparatus to a network. The MediaTek MT7620 comprises storage

having a storage location. The MediaTek MT7620 a controller coupled to the network interface and the storage and configured to receive, from a first device via the network interface, a first packet including a command, a first address that corresponds to the storage location of the apparatus, and a second address that corresponds to a storage location of a second device. The MediaTek MT7620 a controller coupled to the network interface and the storage and configured to transmit, to the second device via the network interface, a second packet to effect a transfer of data between the storage location of the apparatus and the storage location of the second device based at least in part on the command.

Overview

The MT7620 router-on-a-chip includes an 802.11n MAC and baseband, a 2.4 GHz radio and FEM, a 580 MHz MIPS® 24K™ CPU core, a 5-port 10/100 switch and two RGMII. The MT7620 includes everything needed to build an AP router from a single chip. The embedded high performance CPU can process advanced applications effortlessly, such as routing, security and VoIP. The MT7620 also includes a selection of interfaces to support a variety of applications, such as a USB port for accessing external storage.


Applications:

- Routers
- NAS devices
- iNICs
- Dual band concurrent routers

Features

- Embedded MIPS24KEc (580 MHz) with 64 KB I-Cache and 32 KB D-Cache
- 2T2R 2.4 GHz with 300 Mbps PHY data rate
- Legacy 802.11b/g and HT 802.11n modes
- 20/40 MHz channel bandwidth
- Legacy 802.11b/g and HT 802.11n modes
- Reverse Data Grant (RDG)
- Maximal Ratio Combining (MRC)
- Space Time Block Coding (STBC)
- 16-bit SDRAM up to 64 Mbytes
- 16-bit DDR1/2 up to 128/256 Mbytes (MT7620A)
- SPI, NAND Flash/SD-XC
- 1x USB 2.0, 1x PCIe host/device
- 5-port 10/100 SW and two RGMII
- An optimized PMU
- Green AP
 - Intelligent Clock Scaling (exclusive)
 - DDRII: ODT off, Self-refresh mode
 - SDRAM: Pre-charge power down
- I2C, I2S, SPI, PCM, UART, JTAG, MDC, MDIO, GPIO
- Hardware NAT with IPv6 and 2 Gbps wired speed
- 16 Multiple BSSID
- WEP64/128, TKIP, AES, WPA, WPA2, WAPI
- QoS: WMM, WMM-PS
- WPS: PBC, PIN
- Voice Enterprise: 802.11k+r
- AP Firmware: Linux 2.6 SDK, eCOS with IPv6
- RGMII iNIC Driver: Linux 2.4/2.6

¹ https://w.electrodragon.com/w/images/3/34/MT7620_Datasheet.pdf at pg. 2 of 54.

		MT7620 PROGRAMMING GUIDE Integrated 802.11n MAC/BBP and 2.4 GHz RF/FEM Router-on-a-Chip		
Bits	Type	Name	Description	Initial Value
8:6	RW	IGMP_EG_TAG	IGMP Message Egress VLAN Tag Attribution 3'b000: System Default (Disable) 3'b001: Consistent 3'b010,3'b011: Reserved 3'b100: Untagged 3'b101: Swap 3'b110: Tagged 3'b111: Stack	0x0
5	RW	IGMP_LKY_VLAN	IGMP Leaky VLAN Enable 1'b0: Disable 1'b1: Enable	0x0
4	RW	IGMP_PRI_HIGH	IGMP Force the Highest Priority 1'b0: System default 1'b1: Assigned to the highest priority queue	0x1
3	RW	IGMP_QUE_MIR	IGMP Query Message to Mirror Port 1'b0: Disable 1'b1: Frame copied to Mirror port	0x0
2:0	RW	IGMP_QUE_FW	IGMP Query Message TO_CPU Forwarding 3'b0xx: System default (disable) 3'b100: System default and CPU port excluded 3'b101: System default and CPU port included 3'b110: CPU port only (As long as the ingress port is not the CPU port. If the ingress port is the CPU port, then the system default and CPU port are excluded.) 3'b111: Frame dropped	0x0
296. APC: ARP and PPPoE Control Register (offset: 0x0020)				
Bits	Type	Name	Description	Initial Value
31:28	-	-	Reserved	0x0
27	RW	PPP_MANG_FR	PPPoE Discovery as Management Frame 1'b0: Disable 1'b1: Regarded as management frame	0x1
26	RW	PPP_PAE_FR	PPPoE Discovery as PAE Frame 1'b0: Disable 1'b1: Regarded as PAE frame	0x0
25	RW	PPP_BPDU_FR	PPPoE Discovery as BPDU Frame 1'b0: Non-BPDU Frame 1'b1: Regarded as BPDU frame	0x0

² <https://www.manualslib.com/manual/1639995/Mediatek-Ralink-Mt7620.html?page=260>.

21. Defendant has and continues to indirectly infringe one or more claims of the '773 Patent by knowingly and intentionally inducing others, including MediaTek customers and end-users, to directly infringe, either literally or under the doctrine of equivalents, by making, using, offering to sell, selling and/or importing into the United States products that implement the infringing technology.

22. Defendant, with knowledge that these products, or the use thereof, infringe the '773 Patent at least as of the date of this Complaint, knowingly and intentionally induced, and continues to knowingly and intentionally induce, direct infringement of the '773 Patent by providing these products to end users for use in an infringing manner. Alternatively, on information and belief, Defendant has adopted a policy of not reviewing the patents of others, including specifically those related to Defendant's specific industry, thereby remaining willfully blind to the Patents-in-Suit at least as early as the issuance of the Patents-in-Suit.

23. Defendant induced infringement by others, including end users, with the intent to cause infringing acts by others or, in the alternative, with the belief that there was a high probability that others, including end users, infringe the '773 Patent, but while remaining willfully blind to the infringement. Defendant has and continues to induce infringement by its customers and end-users by supplying them with instructions on how to operate the infringing technology in an infringing manner, while also making publicly available information on the infringing technology via Defendant's website, product literature and packaging, and other publications.

24. ServStor has suffered damages as a result of Defendant's direct and indirect infringement of the '773 Patent in an amount to be proved at trial.

25. ServStor has suffered, and will continue to suffer, irreparable harm as a result of Defendant's infringement of the '773 Patent, for which there is no adequate remedy at law, unless Defendant's infringement is enjoined by this Court.

COUNT II
(Infringement of the '467 Patent)

26. Paragraphs 1 through 15 are incorporated by reference as if fully set forth herein.

27. ServStor has not licensed or otherwise authorized Defendant to make, use, offer for sale, sell, or import any products that embody the inventions of the '476 Patent.

28. Defendant has and continues to directly infringe the '476 Patent, either literally or under the doctrine of equivalents, without authority and in violation of 35 U.S.C. § 271, by making, using, offering to sell, selling, and/or importing into the United States products that satisfy each and every limitation of one or more claims of the '476 Patent. Such products include but are not limited to microchips, SoCs, and ASICs that transmit and encapsulate packets to a target device, such as the MediaTek MT7620.

29. For example, Defendant has and continues to directly infringe at least claim 11 of the '476 Patent by making, using, offering to sell, selling, and/or importing into the United States products that include microchips, SoCs, and ASICs that transmit and encapsulate packets to a target device, such as the MediaTek MT7620.

30. For example, the MediaTek MT7620 comprises a controller configured to generate a first packet with a control portion having a first identifier segment of a split identifier, the first identifier segment corresponding to a storage area of a target device remotely disposed from the apparatus across a network. The MediaTek MT7620 comprises a controller configured to generate a second packet with a control portion having a second identifier segment of the split identifier, the second identifier segment corresponding to a storage block of the storage area. The MediaTek

MT7620 comprises a controller configured to encapsulate the second packet within the first packet. The MediaTek MT7620 comprises a network interface coupled to the controller, and configured to transmit the first packet, with the encapsulated second packet, to the target device across the network.

Overview

The MT7620 router-on-a-chip includes an 802.11n MAC and baseband, a 2.4 GHz radio and FEM, a 580 MHz MIPS® 24K™ CPU core, a 5-port 10/100 switch and two RGMII. The MT7620 includes everything needed to build an AP router from a single chip. The embedded high performance CPU can process advanced applications effortlessly, such as routing, security and VoIP. The MT7620 also includes a selection of interfaces to support a variety of applications, such as a USB port for accessing external storage.

Applications:

- Routers
- NAS devices
- iNICs
- Dual band concurrent routers

Features

- Embedded MIPS24KEc (580 MHz) with 64 KB I-Cache and 32 KB D-Cache
- 2T2R 2.4 GHz with 300 Mbps PHY data rate
- Legacy 802.11b/g and HT 802.11n modes
- 20/40 MHz channel bandwidth
- Legacy 802.11b/g and HT 802.11n modes
- Reverse Data Grant (RDG)
- Maximal Ratio Combining (MRC)
- Space Time Block Coding (STBC)
- 16-bit SDRAM up to 64 Mbytes
- 16-bit DDR1/2 up to 128/256 Mbytes (MT7620A)
- SPI, NAND Flash/SD-XC
- 1x USB 2.0, 1x PCIe host/device
- 5-port 10/100 SW and two RGMII
- An optimized PMU
- Green AP
 - Intelligent Clock Scaling (exclusive)
 - DDRII: ODT off, Self-refresh mode
 - SDRAM: Pre-charge power down
- I2C, I2S, SPI, PCM, UART, JTAG, MDC, MDIO, GPIO
- Hardware NAT with IPv6 and 2 Gbps wired speed
- 16 Multiple BSSID
- WEP64/128, TKIP, AES, WPA, WPA2, WAPI
- QoS: WMM, WMM-PS
- WPS: PBC, PIN
- Voice Enterprise: 802.11k+r
- AP Firmware: Linux 2.6 SDK, eCOS with IPv6
- RGMII iNIC Driver: Linux 2.4/2.6

³ https://w.electrodragon.com/w/images/3/34/MT7620_Datasheet.pdf at pg. 2 of 54.

Ralink A MEDIATEK COMPANY		MT7620 PROGRAMMING GUIDE Integrated 802.11n MAC/BBP and 2.4 GHz RF/FEM Router-on-a-Chip		
Bits	Type	Name	Description	Initial Value
8:6	RW	IGMP_EG_TAG	IGMP Message Egress VLAN Tag Attribution 3'b000: System Default (Disable) 3'b001: Consistent 3'b010,3'b011: Reserved 3'b100: Untagged 3'b101: Swap 3'b110: Tagged 3'b111: Stack	0x0
5	RW	IGMP_LKY_VLAN	IGMP Leaky VLAN Enable 1'b0: Disable 1'b1: Enable	0x0
4	RW	IGMP_PRI_HIGH	IGMP Force the Highest Priority 1'b0: System default 1'b1: Assigned to the highest priority queue	0x1
3	RW	IGMP_QUE_MIR	IGMP Query Message to Mirror Port 1'b0: Disable 1'b1: Frame copied to Mirror port	0x0
2:0	RW	IGMP_QUE_FW	IGMP Query Message TO_CPU Forwarding 3'b0xx: System default (disable) 3'b100: System default and CPU port excluded 3'b101: System default and CPU port included 3'b110: CPU port only (As long as the ingress port is not the CPU port. If the ingress port is the CPU port, then the system default and CPU port are excluded.) 3'b111: Frame dropped	0x0
296. APC: ARP and PPPoE Control Register (offset: 0x0020)				
Bits	Type	Name	Description	Initial Value
31:28	-	-	Reserved	0x0
27	RW	PPP_MANG_FR	PPPoE Discovery as Management Frame 1'b0: Disable 1'b1: Regarded as management frame	0x1
26	RW	PPP_PAE_FR	PPPoE Discovery as PAE Frame 1'b0: Disable 1'b1: Regarded as PAE frame	0x0
25	RW	PPP_BPDU_FR	PPPoE Discovery as BPDU Frame 1'b0: Non-BPDU Frame 1'b1: Regarded as BPDU frame	0x0
PGMT7620_V.1.0_040503		Page 260 of 523		

⁴ <https://www.manualslib.com/manual/1639995/Mediatek-Ralink-Mt7620.html?page=260>.

31. Defendant has and continues to indirectly infringe one or more claims of the '476 Patent by knowingly and intentionally inducing others, including MediaTek customers and end-users, to directly infringe, either literally or under the doctrine of equivalents, by making, using, offering to sell, selling and/or importing into the United States that implement the infringing technology.

32. Defendant, with knowledge that these products, or the use thereof, infringe the '476 Patent at least as of the date of this Complaint, knowingly and intentionally induced, and continues to knowingly and intentionally induce, direct infringement of the '476 Patent by providing these products to end users for use in an infringing manner. Alternatively, on information and belief, Defendant has adopted a policy of not reviewing the patents of others, including specifically those related to Defendant's specific industry, thereby remaining willfully blind to the Patents-in-Suit at least as early as the issuance of the Patents-in-Suit.

33. Defendant induced infringement by others, including end users, with the intent to cause infringing acts by others or, in the alternative, with the belief that there was a high probability that others, including end users, infringe the '476 Patent, but while remaining willfully blind to the infringement. Defendant has and continues to induce infringement by its customers and end-users by supplying them with instructions on how to operate the infringing technology in an infringing manner, while also making publicly available information on the infringing technology via Defendant's website, product literature and packaging, and other publications.

34. ServStor has suffered damages as a result of Defendant's direct and indirect infringement of the '476 Patent in an amount to be proved at trial.

35. ServStor has suffered, and will continue to suffer, irreparable harm as a result of Defendant's infringement of the '476 Patent, for which there is no adequate remedy at law, unless Defendant's infringement is enjoined by this Court.

COUNT III
(Infringement of the '814 Patent)

36. Paragraphs 1 through 15 are incorporated by reference as if fully set forth herein.

37. ServStor has not licensed or otherwise authorized Defendant to make, use, offer for sale, sell, or import any products that embody the inventions of the '814 Patent.

38. Defendant has and continues to directly infringe the '814 Patent, either literally or under the doctrine of equivalents, without authority and in violation of 35 U.S.C. § 271, by making, using, offering to sell, selling, and/or importing into the United States products that satisfy each and every limitation of one or more claims of the '814 Patent. Such products include but are not limited to microchips, SoCs, and ASICs that transmit and encapsulate packets to a target device, such as the MediaTek MT7620.

39. For example, Defendant has and continues to directly infringe at least claim 11 of the '814 Patent by making, using, offering to sell, selling, and/or importing into the United States products that microchips, SoCs, and ASICs that transmit and encapsulate packets to a target device, such as the MediaTek MT7620.

40. For example, the MediaTek MT7620 comprises a controller configured to generate a first packet having a data portion that is at least approximately equal in size to a native block size of a target device, which is smaller than a native block size of the apparatus, the first packet further having a first segment of a split identifier, the controller further configured to encapsulate the first packet into a data portion of a second packet, the second packet having a control portion with a second segment of the split identifier. The MediaTek MT7620 comprises a network interface

coupled to the controller, and configured to transmit the packet to the target device across a network.

Overview

The MT7620 router-on-a-chip includes an 802.11n MAC and baseband, a 2.4 GHz radio and FEM, a 580 MHz MIPS® 24K™ CPU core, a 5-port 10/100 switch and two RGMII. The MT7620 includes everything needed to build an AP router from a single chip. The embedded high performance CPU can process advanced applications effortlessly, such as routing, security and VoIP. The MT7620 also includes a selection of interfaces to support a variety of applications, such as a USB port for accessing external storage.


Applications:

- Routers
- NAS devices
- iNICs
- Dual band concurrent routers

Features

- Embedded MIPS24KEc (580 MHz) with 64 KB I-Cache and 32 KB D-Cache
- 2T2R 2.4 GHz with 300 Mbps PHY data rate
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- Reverse Data Grant (RDG)
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- SPI, NAND Flash/SD-XC
- 1x USB 2.0, 1x PCIe host/device
- 5-port 10/100 SW and two RGMII
- An optimized PMU
- Green AP
 - Intelligent Clock Scaling (exclusive)
 - DDRII: ODT off, Self-refresh mode
 - SDRAM: Pre-charge power down
- I2C, I2S, SPI, PCM, UART, JTAG, MDC, MDIO, GPIO
- Hardware NAT with IPv6 and 2 Gbps wired speed
- 16 Multiple BSSID
- WEP64/128, TKIP, AES, WPA, WPA2, WAPI
- QoS: WMM, WMM-PS
- WPS: PBC, PIN
- Voice Enterprise: 802.11k+r
- AP Firmware: Linux 2.6 SDK, eCOS with IPv6
- RGMII iNIC Driver: Linux 2.4/2.6

⁵ https://w.electrodragon.com/w/images/3/34/MT7620_Datasheet.pdf at pg. 2 of 54.

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5	RW	IGMP_LKY_VLAN	IGMP Leaky VLAN Enable 1'b0: Disable 1'b1: Enable	0x0
4	RW	IGMP_PRI_HIGH	IGMP Force the Highest Priority 1'b0: System default 1'b1: Assigned to the highest priority queue	0x1
3	RW	IGMP_QUE_MIR	IGMP Query Message to Mirror Port 1'b0: Disable 1'b1: Frame copied to Mirror port	0x0
2:0	RW	IGMP_QUE_FW	IGMP Query Message TO_CPU Forwarding 3'b0xx: System default (disable) 3'b100: System default and CPU port excluded 3'b101: System default and CPU port included 3'b110: CPU port only (As long as the ingress port is not the CPU port. If the ingress port is the CPU port, then the system default and CPU port are excluded.) 3'b111: Frame dropped	0x0
296. APC: ARP and PPPoE Control Register (offset: 0x0020)				
Bits	Type	Name	Description	Initial Value
31:28	-	-	Reserved	0x0
27	RW	PPP_MANG_FR	PPPoE Discovery as Management Frame 1'b0: Disable 1'b1: Regarded as management frame	0x1
26	RW	PPP_PAE_FR	PPPoE Discovery as PAE Frame 1'b0: Disable 1'b1: Regarded as PAE frame	0x0
25	RW	PPP_BPDU_FR	PPPoE Discovery as BPDU Frame 1'b0: Non-BPDU Frame 1'b1: Regarded as BPDU frame	0x0

⁶ <https://www.manualslib.com/manual/1639995/Mediatek-Ralink-Mt7620.html?page=260>.

41. Defendant has and continues to indirectly infringe one or more claims of the '814 Patent by knowingly and intentionally inducing others, including MediaTek customers and end-users, to directly infringe, either literally or under the doctrine of equivalents, by making, using, offering to sell, selling and/or importing into the United States that implement the infringing technology.

42. Defendant, with knowledge that these products, or the use thereof, infringe the '814 Patent at least as of the date of this Complaint, knowingly and intentionally induced, and continues to knowingly and intentionally induce, direct infringement of the '814 Patent by providing these products to end users for use in an infringing manner. Alternatively, on information and belief, Defendant has adopted a policy of not reviewing the patents of others, including specifically those related to Defendant's specific industry, thereby remaining willfully blind to the Patents-in-Suit at least as early as the issuance of the Patents-in-Suit.

43. Defendant induced infringement by others, including end users, with the intent to cause infringing acts by others or, in the alternative, with the belief that there was a high probability that others, including end users, infringe the '814 Patent, but while remaining willfully blind to the infringement. Defendant has and continues to induce infringement by its customers and end-users by supplying them with instructions on how to operate the infringing technology in an infringing manner, while also making publicly available information on the infringing technology via Defendant's website, product literature and packaging, and other publications.

44. ServStor has suffered damages as a result of Defendant's direct and indirect infringement of the '814 Patent in an amount to be proved at trial.

45. ServStor has suffered, and will continue to suffer, irreparable harm as a result of Defendant's infringement of the '814 Patent, for which there is no adequate remedy at law, unless Defendant's infringement is enjoined by this Court.

COUNT IV
(Infringement of the '473 Patent)

46. Paragraphs 1 through 15 are incorporated by reference as if fully set forth herein.

47. ServStor has not licensed or otherwise authorized Defendant to make, use, offer for sale, sell, or import any products that embody the inventions of the '473 Patent.

48. Defendant has and continues to directly infringe the '473 Patent, either literally or under the doctrine of equivalents, without authority and in violation of 35 U.S.C. § 271, by making, using, offering to sell, selling, and/or importing into the United States products that satisfy each and every limitation of one or more claims of the '473 Patent. Such products include but are not limited to microchips, SoCs, and ASICs that accelerate the transmission of packets across a network, such as the MediaTek MT7620.

49. For example, Defendant has and continues to directly infringe at least claim 13 of the '473 Patent by making, using, offering to sell, selling, and/or importing into the United States products that include microchips, SoCs, and ASICs that accelerate the transmission of packets across a network, such as the MediaTek MT7620.

50. For example, the MediaTek MT7620 comprises an application and an accelerator module to be coupled with the application and configured to receive a packet transmitted over a network, the packet having an address of a storage location. The MediaTek MT7620 comprises an accelerator module to access a destination context associated with the storage location based at least in part on the address. The MediaTek MT7620 comprises an accelerator module to pass at

least a portion of the packet to the application by bypassing at least a portion of a communication stack of the apparatus based at least in part on the destination context.

Overview

The MT7620 router-on-a-chip includes an 802.11n MAC and baseband, a 2.4 GHz radio and FEM, a 580 MHz MIPS® 24K™ CPU core, a 5-port 10/100 switch and two RGMII. The MT7620 includes everything needed to build an AP router from a single chip. The embedded high performance CPU can process advanced applications effortlessly, such as routing, security and VoIP. The MT7620 also includes a selection of interfaces to support a variety of applications, such as a USB port for accessing external storage.

Applications:

- Routers
- NAS devices
- iNICs
- Dual band concurrent routers

Features

<ul style="list-style-type: none"> ▪ Embedded MIPS24KEc (580 MHz) with 64 KB I-Cache and 32 KB D-Cache ▪ 2T2R 2.4 GHz with 300 Mbps PHY data rate ▪ Legacy 802.11b/g and HT 802.11n modes ▪ 20/40 MHz channel bandwidth ▪ Legacy 802.11b/g and HT 802.11n modes ▪ Reverse Data Grant (RDG) ▪ Maximal Ratio Combining (MRC) ▪ Space Time Block Coding (STBC) ▪ 16-bit SDRAM up to 64 Mbytes ▪ 16-bit DDR1/2 up to 128/256 Mbytes (MT7620A) ▪ SPI, NAND Flash/SD-XC ▪ 1x USB 2.0, 1x PCIe host/device ▪ 5-port 10/100 SW and two RGMII 	<ul style="list-style-type: none"> ▪ An optimized PMU ▪ Green AP <ul style="list-style-type: none"> – Intelligent Clock Scaling (exclusive) – DDRII: ODT off, Self-refresh mode – SDRAM: Pre-charge power down ▪ I2C, I2S, SPI, PCM, UART, JTAG, MDC, MDIO, GPIO ▪ Hardware NAT with IPv6 and 2 Gbps wired speed ▪ 16 Multiple BSSID ▪ WEP64/128, TKIP, AES, WPA, WPA2, WAPI ▪ QoS: WMM, WMM-PS ▪ WPS: PBC, PIN ▪ Voice Enterprise: 802.11k+r ▪ AP Firmware: Linux 2.6 SDK, eCOS with IPv6 ▪ RGMII iNIC Driver: Linux 2.4/2.6
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⁷ https://w.electrodragon.com/w/images/3/34/MT7620_Datasheet.pdf at pg. 2 of 54.

Bits		Type	Name	Description	Initial Value
8:6	RW	IGMP_EG_TAG	IGMP Message Egress VLAN Tag Attribution	3'b000: System Default (Disable) 3'b001: Consistent 3'b010, 3'b011: Reserved 3'b100: Untagged 3'b101: Swap 3'b110: Tagged 3'b111: Stack	0x0
5	RW	IGMP_LKY_VLAN	IGMP Leaky VLAN Enable	1'b0: Disable 1'b1: Enable	0x0
4	RW	IGMP_PRI_HIGH	IGMP Force the Highest Priority	1'b0: System default 1'b1: Assigned to the highest priority queue	0x1
3	RW	IGMP_QUE_MIR	IGMP Query Message to Mirror Port	1'b0: Disable 1'b1: Frame copied to Mirror port	0x0
2:0	RW	IGMP_QUE_FW	IGMP Query Message TO_CPU Forwarding	3'b0xx: System default (disable) 3'b100: System default and CPU port excluded 3'b101: System default and CPU port included 3'b110: CPU port only (As long as the ingress port is not the CPU port. If the ingress port is the CPU port, then the system default and CPU port are excluded.) 3'b111: Frame dropped	0x0

296. APC: ARP and PPPoE Control Register (offset: 0x0020)

Bits	Type	Name	Description	Initial Value	
31:28	-	-	Reserved	0x0	
27	RW	PPP_MANG_FR	PPPoE Discovery as Management Frame	1'b0: Disable 1'b1: Regarded as management frame	0x1
26	RW	PPP_PAE_FR	PPPoE Discovery as PAE Frame	1'b0: Disable 1'b1: Regarded as PAE frame	0x0
25	RW	PPP_BPDU_FR	PPPoE Discovery as BPDU Frame	1'b0: Non-BPDU Frame 1'b1: Regarded as BPDU frame	0x0

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⁸ <https://www.manualslib.com/manual/1639995/Mediatek-Ralink-Mt7620.html?page=260>.

51. Defendant has and continues to indirectly infringe one or more claims of the '473 Patent by knowingly and intentionally inducing others, including MediaTek customers and end-users, to directly infringe, either literally or under the doctrine of equivalents, by making, using, offering to sell, selling and/or importing into the United States that implement the infringing technology.

52. Defendant, with knowledge that these products, or the use thereof, infringe the '473 Patent at least as of the date of this Complaint, knowingly and intentionally induced, and continues to knowingly and intentionally induce, direct infringement of the '473 Patent by providing these products to end users for use in an infringing manner. Alternatively, on information and belief, Defendant has adopted a policy of not reviewing the patents of others, including specifically those related to Defendant's specific industry, thereby remaining willfully blind to the Patents-in-Suit at least as early as the issuance of the Patents-in-Suit.

53. Defendant induced infringement by others, including end users, with the intent to cause infringing acts by others or, in the alternative, with the belief that there was a high probability that others, including end users, infringe the '473 Patent, but while remaining willfully blind to the infringement. Defendant has and continues to induce infringement by its customers and end-users by supplying them with instructions on how to operate the infringing technology in an infringing manner, while also making publicly available information on the infringing technology via Defendant's website, product literature and packaging, and other publications.

54. ServStor has suffered damages as a result of Defendant's direct and indirect infringement of the '473 Patent in an amount to be proved at trial.

55. ServStor has suffered, and will continue to suffer, irreparable harm as a result of Defendant's infringement of the '473 Patent, for which there is no adequate remedy at law, unless Defendant's infringement is enjoined by this Court.

DEMAND FOR JURY TRIAL

Plaintiff hereby demands a jury for all issues so triable.

PRAYER FOR RELIEF

WHEREFORE, ServStor prays for relief against Defendant as follows:

a. Entry of judgment declaring that Defendant has directly and/or indirectly infringed one or more claims of the Patents-in-Suit;

b. An order pursuant to 35 U.S.C. § 283 permanently enjoining Defendant, its officers, agents, servants, employees, attorneys, and those persons in active concert or participation with it, from further acts of infringement of one or more of the Patents-in-Suit;

c. An order awarding damages sufficient to compensate ServStor for Defendant's infringement of the Patents-in-Suit, but in no event less than a reasonable royalty, together with interest and costs;

d. Entry of judgment declaring that this case is exceptional and awarding ServStor its costs and reasonable attorney fees under 35 U.S.C. § 285; and,

e. Such other and further relief as the Court deems just and proper.

Dated: September 19, 2024

Respectfully submitted,

/s/ Vincent J. Rubino, III

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