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11 Attorneys for Plaintiff  
12 PATRIOT SCIENTIFIC CORPORATION

13 UNITED STATES DISTRICT COURT  
14 NORTHERN DISTRICT OF CALIFORNIA

15 TECHNOLOGY PROPERTIES LIMITED  
16 LLC, PHOENIX DIGITAL SOLUTIONS  
17 LLC, and PATRIOT SCIENTIFIC  
CORPORATION,

18 Plaintiffs,

19 vs.

20 LG ELECTRONICS, INC. and LG  
ELECTRONICS U.S.A., INC.,

21 Defendants.

Cas. No.

**CV 12-03880**

**COMPLAINT FOR PATENT  
INFRINGEMENT**

**DEMAND FOR JURY TRIAL**

22  
23 Technology Properties Limited LLC (“TPL”), Phoenix Digital Solutions LLC (“PDS”)  
24 and Patriot Scientific Corporation (“PTSC”) (collectively “Plaintiffs”) hereby allege for their  
25 Complaint for Patent Infringement (“Complaint”) against Defendants LG Electronics and LG  
26 Electronics U.S.A., Inc. (collectively “Defendants”) on personal knowledge as to their own  
27 actions and on information and belief as to the actions of Defendants, as follows:

E-FILING

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FILED

JUL 24 2012

RICHARD W. WILSON  
CLERK, U.S. DISTRICT COURT  
NORTHERN DISTRICT OF CALIFORNIA

**PARTIES**

1  
2 1. Plaintiff Technology Properties Limited LLC is a California limited liability  
3 company with its principal place of business at 20883 Stevens Creek Blvd., Suite 100, Cupertino,  
4 California 95014.

5 2. Plaintiff Phoenix Digital Solutions LLC is a Delaware limited liability company  
6 with its principal place of business at 20883 Stevens Creek Blvd., Suite 100, Cupertino,  
7 California 95014.

8 3. Plaintiff Patriot Scientific Corporation is a Delaware limited liability company  
9 with its principal place of business at 701 Palomar Airport Road, Suite 170, Carlsbad, California  
10 92011.

11 4. On information and belief, Defendant LG Electronics is a Korean corporation  
12 with a principal place of business at LG Twin Towers, 20 Yeouido-dong, Yeongdeungpo-gu,  
13 Seoul 150-721, Republic of Korea.

14 5. On information and belief, Defendant LG Electronics U.S.A., Inc., is a Delaware  
15 corporation with a principal place of business at 1000 Sylvan Avenue, Englewood Cliffs, New  
16 Jersey 08632.

17 **JURISDICTION, VENUE AND INTRADISTRICT ASSIGNMENT**

18 6. This is an action for damages and injunctive relief based on patent infringement  
19 arising under the patent laws of the United States, Title 35 of the United States Code.

20 7. This Court has jurisdiction over the subject matter of this action pursuant to 28  
21 U.S.C. § 1331 and 28 U.S.C. § 1338.

22 8. On information and belief, Defendants have transacted business in this District,  
23 contracted to supply goods or services in this District directly or through their agents, have  
24 offered for sale, sold and/or advertised their products and services in the this District, and have  
25 otherwise purposely availed themselves of the privileges and benefits of the laws of the State of  
26 California. This Court has jurisdiction over Defendants because Defendants have committed  
27 acts of patent infringement during the course of their business in this District.

1 9. Venue is proper in this District pursuant to 28 U.S.C. § 1400(a) and (b) and 28  
2 U.S.C. § 1391.

3 10. This matter is an intellectual property action and is not subject to intradistrict  
4 assignment under Civil L.R. 3-2(c).

5 **THE ASSERTED PATENTS**

6 **The '749 Patent**

7 11. United States Patent No. 5,440,749 (“the '749 Patent), entitled “High  
8 Performance, Low Cost Microprocessor Architecture,” issued on August 8, 1995 to Charles H.  
9 Moore and Russell H. Fish, III. A true and correct copy of the '749 Patent is attached as Exhibit  
10 A to this Complaint.

11 12. The '749 Patent teaches a processor that fetches multiple instructions at a time,  
12 and then supplies them to the CPU’s instruction register in parallel during the same memory  
13 cycle they are fetched. Since memory is generally slower than the CPU, being able to fetch and  
14 supply more than one instruction at a time increases the number of instructions the CPU can  
15 receive in a given time, and thus increases instruction bandwidth.

16 13. Plaintiffs TPL, PDS and PTSC collectively hold all substantial rights to the '749  
17 Patent.

18 **The '890 Patent**

19 14. United States Patent No. 5,530,890 (“the '890 Patent”), entitled “High  
20 Performance, Low Cost Microprocessor,” issued on June 25, 1996 to Charles H. Moore and  
21 Russell H. Fish, III. A true and correct copy of the '890 Patent is attached as Exhibit B to this  
22 Complaint.

23 15. The '890 Patent teaches a dual stack architecture and the use of stack pointers that  
24 can reference memory in any location to provide more architectural flexibility and faster access  
25 to data elements. A stack architecture is sometimes analogized to a spring-loaded stack of plates  
26 of the kind used in a restaurant. The last plate placed (or “pushed”) on the top of the stack is the  
27 first plate removed (or “popped”) off the stack when needed. Like plates, data elements can be

1 “pushed” onto or “popped” off the stack. However, by using a “stack pointer,” the CPU does not  
2 need to be an actual top-to-bottom “spring-loaded” stack. Instead, the stack pointer keeps track  
3 of where the “top of stack” item is in a “virtual stack,” so it can be accessed directly as if it were  
4 on the “top.” Combining this with other features, such as a memory controller and direct  
5 memory access, the ’890 Patent allows the CPU to off-load memory transfer of data to achieve  
6 further efficiencies and higher performance.

7 16. Plaintiffs TPL, PDS and PTSC collectively hold all substantial rights to the ’890  
8 Patent.

9 **The ’336 Patent**

10 17. United States Patent No. 5,809,336 (“the ’336 Patent”), entitled “High  
11 Performance Microprocessor Having Variable Speed System Clock,” was duly and legally issued  
12 on September 15, 1998 to Charles H. Moore and Russell H. Fish, III. A true and correct copy of  
13 the ’336 Patent is attached as Exhibit C to this Complaint.

14 18. The ’336 Patent teaches the use of two independent clocks in a microprocessor  
15 system: (1) an on-chip clock to time the CPU; and (2) a second independent clock to time the  
16 input/output (I/O) interface. This innovation was widely adopted by the industry and became  
17 fundamental to the increased speed and efficiency of modern microprocessors. Decoupling the  
18 system clock from the I/O clock allows the clocks to run independently (or “asynchronously”).

19 19. Plaintiffs TPL, PDS and PTSC collectively hold all substantial rights to the ’336  
20 Patent.

21 **COUNT I**

22 **INFRINGEMENT OF THE ’749 PATENT**

23 20. Plaintiffs reallege and incorporate herein by reference the allegations contained in  
24 paragraphs 1-19.

25 21. On information and belief, without a license or permission from Plaintiffs,  
26 Defendants have infringed and continue to infringe at least claim 1 of the ’749 Patent.  
27 Defendants’ infringing activities in the United States and in this District include importing,

1 making, using, offering to sell, and/or selling products and devices that embody and/or practice  
2 the patented invention, including but not limited to Lucid 4G LTE.

3 22. On information and belief, Defendants' direct infringement of the '749 Patent has  
4 caused and continues to cause substantial damage to Plaintiffs.

5 23. On information and belief, Defendants' direct infringement of the '749 Patent has  
6 been and continues to be willful and deliberate, entitling Plaintiffs to enhanced damages and  
7 attorneys' fees.

8 **COUNT II**

9 **INFRINGEMENT OF THE '890 PATENT**

10 24. Plaintiffs reallege and incorporate herein by reference the allegations contained in  
11 paragraphs 1-23.

12 25. On information and belief, without a license or permission from Plaintiffs,  
13 Defendants have infringed and continue to infringe at least claim 7 of the '890 Patent.  
14 Defendants' infringing activities in the United States and in this District include importing,  
15 making, using, offering to sell, and/or selling products and devices that embody and/or practice  
16 the patented invention, including but not limited to the Lucid 4G LTE.

17 26. On information and belief, Defendants' direct infringement of the '890 Patent has  
18 caused and continues to cause substantial damage to Plaintiffs.

19 27. On information and belief, Defendants' direct infringement of the '890 Patent has  
20 been and continues to be willful and deliberate, entitling Plaintiffs to enhanced damages and  
21 attorneys' fees.

22 **COUNT III**

23 **INFRINGEMENT OF THE '336 PATENT**

24 28. Plaintiffs reallege and incorporate herein by reference the allegations contained in  
25 paragraphs 1-27.

26 29. On information and belief, without a license or permission from Plaintiffs,  
27 Defendants have infringed and continue to infringe, induced others to infringe and continue to

1 induce others to infringe, and/or has committed and continue to commit acts of contributory  
2 infringement, literally or under the doctrine of equivalents, at least claim 1 of the '336 Patent.  
3 Defendants' infringing activities in the United States and in this District include importing,  
4 making, using, offering to sell, and/or selling products and devices that embody and/or practice  
5 the patented invention, including but not limited to the Lucid 4G LTE, and contributing to, and  
6 inducing consumers and users to make and use the patented invention and to practice the claimed  
7 methods.

8 30. On information and belief, Defendants induce others to infringe at least claim 1 of  
9 the '336 Patent by encouraging and facilitating others to perform actions known by Defendants  
10 to infringe and with the intent that performance of the actions will infringe. TPL provided LG  
11 notice of the '336 Patent by letter (with an enclosed disk identifying the MMP patents) dated  
12 October 3, 2005.

13 31. On information and belief, Defendants induce consumers to make and use the  
14 claimed inventions and to practice the claimed methods by (i) providing the LG LUCID 4G LTE  
15 with a USB input/output interface for connecting the accused devices to a peripheral device, the  
16 peripheral device having a clock independent of the CPU clock (*e.g.*, ring oscillator) connected  
17 to the central processing unit on the microprocessors of the LG LUCID 4G LTE, and (ii)  
18 instructing consumers to connect the accused products to a peripheral device such that the  
19 combination includes each element of the asserted apparatus claims of the '336 Patent and use of  
20 the combination, as intended, practices each of the elements of at least claim 1 of the '336 Patent.

21 32. On information and belief, consumers make and use the claimed inventions and  
22 practice the claimed methods by using the LG LUCID 4G LTE in combination with a peripheral  
23 device having a clock that originates clock signals from a source other than the clock connected  
24 to the central processing unit on the microprocessors of the LG accused product, thereby directly  
25 infringing at least claim 1 of the '336 Patent.




**DEMAND FOR JURY TRIAL**

Pursuant to Rule 38(b) of the Federal Rules of Civil Procedure, Plaintiffs demand a jury trial as to all issues so triable.

Dated: July 24, 2012

Respectfully submitted,

AGILITY IP LAW LLP

  
James C. Otteson

Attorneys for Plaintiffs  
TECHNOLOGY PROPERTIES LIMITED LLC  
and PHOENIX DIGITAL SOLUTIONS LLC

KIRBY NOONAN LANCE & HOGE LLP

/s/ Charles T. Hoge  
Charles T. Hoge

Attorneys for Plaintiff  
PATRIOT SCIENTIFIC CORPORATION

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