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(12) United States Design Patent

Laurino

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(54) **ELECTRICAL TEST AND MEASUREMENT APPARATUS**

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(**) Term: **15 Years**

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(51) **LOC (12) Cl.** **14-04**

(52) **U.S. Cl.**
USPC **D14/489**

(58) **Field of Classification Search**
USPC D10/80, 485, 489
CPC G06F 3/048; G06F 3/0481; G06F 3/04812; G06F 3/04815; G06F 3/04817; G06F 3/0482; G06F 3/0483; G06F 3/0484; G06F 3/04842; G06F 3/04845; G06F 3/04847; G06F 3/0485; G06F 3/04855; G06F 3/0486; G06F 3/0487; G06F 3/0488; G06F 3/04883; G06F 3/04886; H04N 1/00424
See application file for complete search history.

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(57) **CLAIM**

The ornamental design for an electrical test and measurement apparatus, as shown and described.

DESCRIPTION

FIG. **1** is a front elevation view of an electrical test and measurement apparatus showing my new design.

FIG. **2** is another front elevation view of an electrical test and measurement apparatus with my new design as shown in FIG. **1**.

FIG. **3** is a detailed front elevation view of an electrical test and measurement apparatus or a portion thereof with my new design as shown in FIGS. **1** and **2**.

FIG. **4** is another front elevation view of an electrical test and measurement apparatus showing my new design.

FIG. **5** is another front elevation view of an electrical test and measurement apparatus with my new design as shown in FIG. **4**.

FIG. **6** is a detailed front elevation view of an electrical test and measurement apparatus or a portion thereof with my new design as shown in FIGS. **4** and **5**.

The broken line perimeter in FIGS. **3** and **6** represents an electrical test and measurement apparatus or a portion thereof and forms no part of the claimed design. The broken lines in FIGS. **1**, **2**, **4**, and **5** illustrate environment only and form no part of the claimed design.

1 Claim, 6 Drawing Sheets

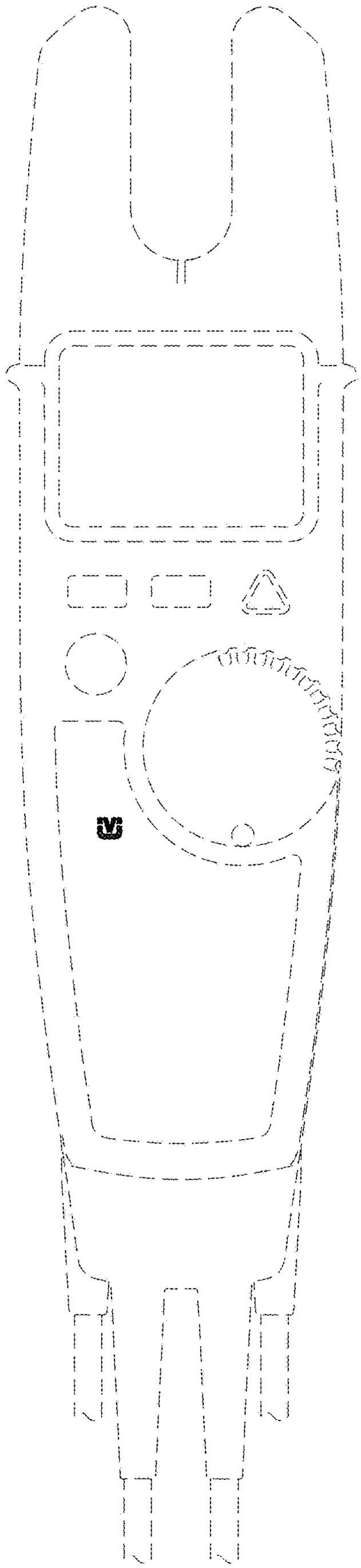

FIG. 1

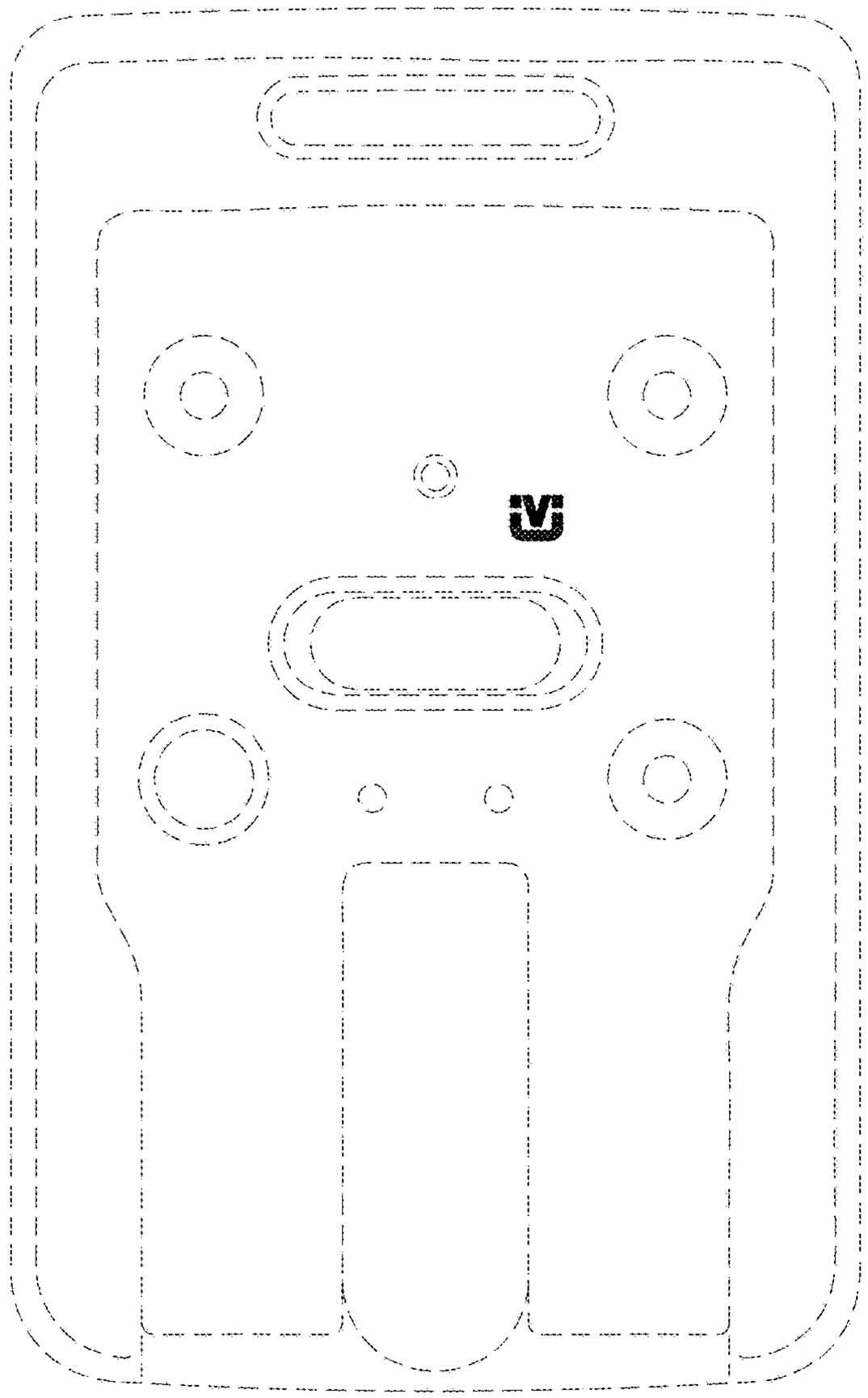

FIG. 2

FIG. 3

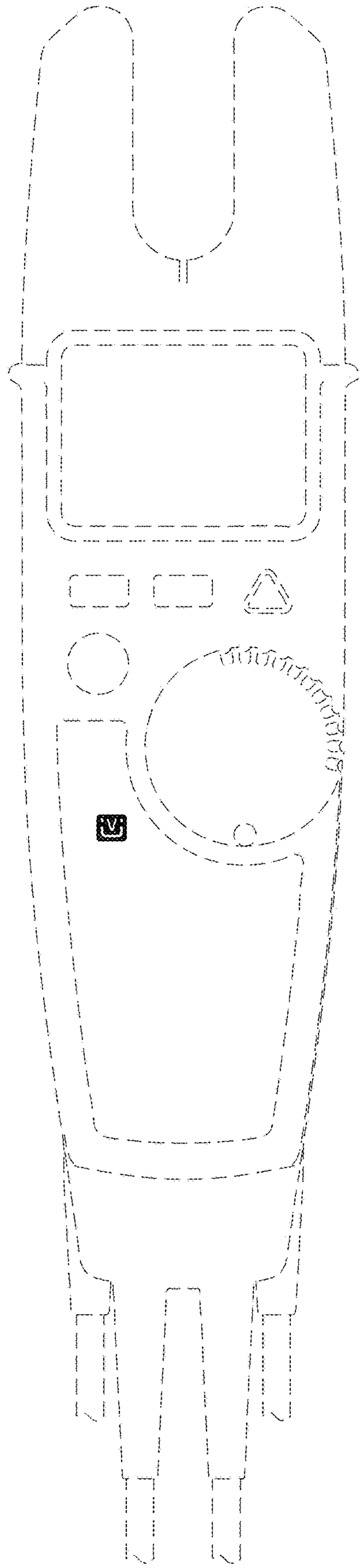

FIG. 4

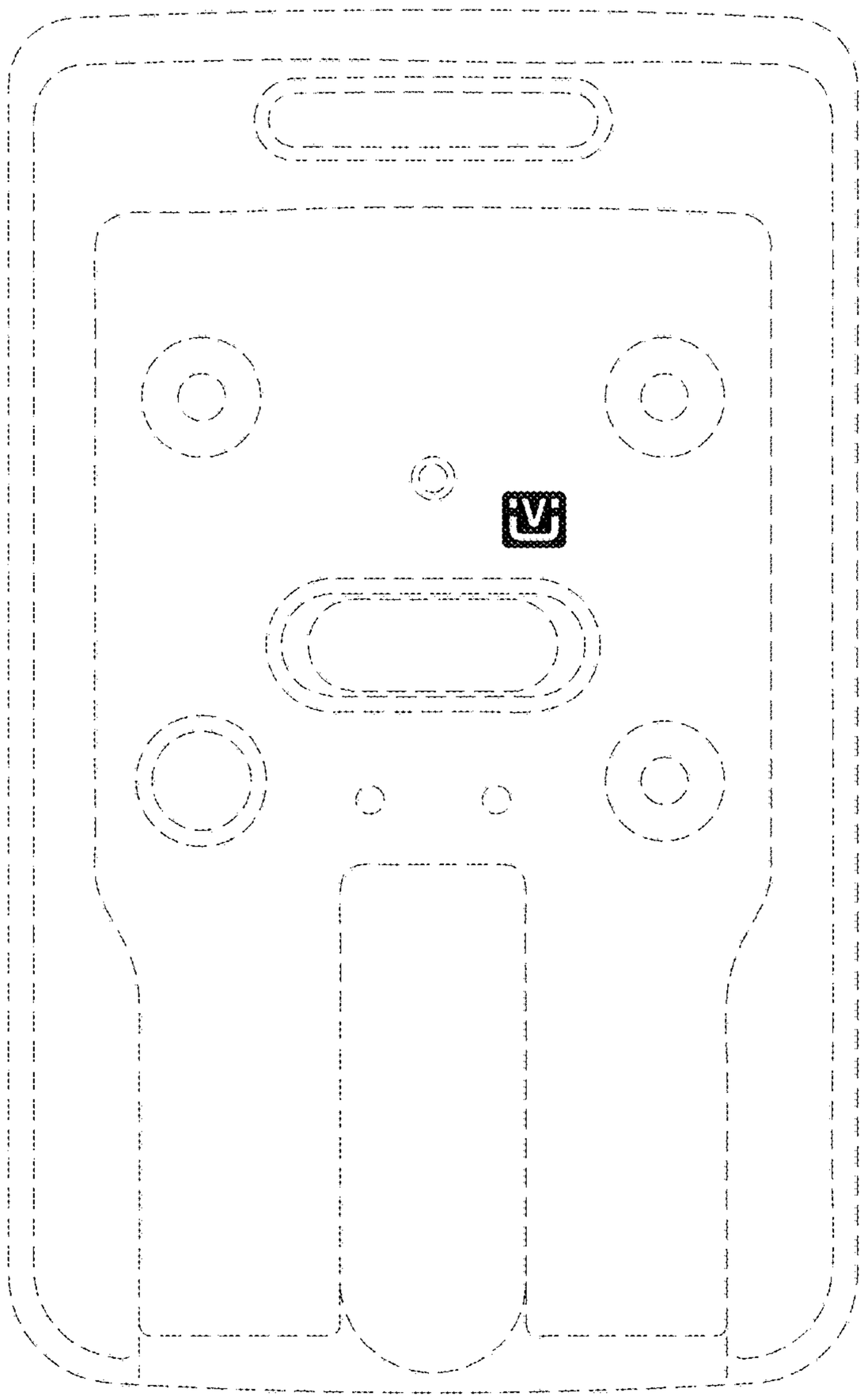

FIG. 5

FIG. 6